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# Supplementary Materials: A Single Nanobelt Transistor for Gas Identification: Using a Gas-Dielectric Strategy

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Material	Туре	Operating Temperature (°C)	Surface Modification	Target Species	Data Evaluation Modeling	Ref.
SnO2 nanowire array	Resistor	240–285	Pristine Ag Pd	H2 CO C2H4	LDA ª	[1]
SnO2 nanobelt with changed diameter	Resistor	290	Pd with changed density	Toluene (1–30 ppm) Ethanol (1–30 ppm) 2–Propanol (1–30 ppm) CO (1–30 ppm)	LDA	[2]
SnO2 nanowire In2O3 nanowire SnO2:Ni nanowire TiO2 nanowire	Resistor	350	_	H <sub>2</sub> CO	Radial plots of the response signals	[3]
SnO2 nanowire array with changed density	Resistor	247–327	_	2–Propanol (0.5–50 ppm) Ethanol (0.5–50 ppm) CO (0.5–10 ppm)	LDA	[4]
SnO2 nanowire array	Resistor	192–373	_	Acetone (20–80 ppm) Ethanol (20–80 ppm) MEK <sup>b</sup> (20–80 ppm)	LDA	[5]
CNT-SnO <sub>2</sub> film	Resistor	250–300	_	Ethanol (100–1000 ppm) Methanol (100–1000 ppm)	-	[6]
In2O3 nanowire ZnO nanowire SnO2 nanowire SWNT array	Resistor	25 200	_	H2 (500–2000 ppm) Ethanol (50–200 ppm) NO2 (0.1–1 ppm)	PCA <sup>c</sup>	[7]
In2O3:Mg nanowire	FET	RT	Pristine Au Ag Pt	CO (0.5–100 ppm) Ethanol (100 ppm) H2 (100 ppm)	-	[8]
Single SnO <sub>2</sub> nanowire	FET	RT	_	NO2 (10–100 ppb) NO (50–300 ppb) H2S (50–300 ppb)	LDA	Our work

1. Table S1. Strategies and models for the reported metal oxide semiconductor (MOS) based E-noses.	
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<sup>a</sup> LDA: Linear Discriminant Analysis; <sup>b</sup> MEK: Methylethylketone; <sup>c</sup> PCA: Principal Component Analysis.

Single-crystal nanowires/nanobelts of SnO<sub>2</sub> were synthesized by vapor transport as previously reported [9]. The nanowires/nanobelts have the regular shape and the smooth surface with diameters of 100–800 nm and lengths of tens of micrometers.



Figure S1. SEM images of SnO2 nanowires/nanobelts.

## 3. Fabrication Process of SnO2 Nanobelt FET with Gas Dielectric

The device fabrication process is shown in Figure S2. (a) The patterned Ti/Au gate electrodes were deposited on the insulated glass substrate by ultraviolet lithography. The polymethyl methacrylate (PMMA) layer was spin-coated onto the substrate as dielectric. The thickness of the PMMA layer is 500 nm; (b) Electron Beam Lithography was used to remove part of the PMMA and to create a groove with the width ranged from a few micrometers to tens of micrometers; (c) A single-crystal nanobelt of SnO<sub>2</sub> was suspended on the groove of the PMMA layer by nanomechanical manipulation; (d) The gold films were placed by a " stamping gold layer" technique [10], which serves as the mask for source-drain electrode deposition; (e) The Ni/Au (40 nm/40 nm) electrodes were deposited by thermal evaporation; (f) The adhered gold films were removed by mechanical probe.



Figure S2. Schematic representations of the device fabrication process.

## 4. Multiple Measurement Results of Gas-Dielectric Devices

The comparative results in Figure S3 show that both SnO<sub>2</sub> and CuPc nanowire FETs with gas dielectric present the excellently reproducible electrical characteristics. For the same semiconductor material, both low and high-mobility devices present the good repeatable electrical characteristics.



**Figure S3.** The multimeasured transfer curves of the gas-dielectric nanowire FETs with different mobilities and semiconductor materials: (**a**) SnO<sub>2</sub> with mobility at 91.4 cm<sup>2</sup>·V<sup>-1</sup>·s<sup>-1</sup>; (**b**) SnO<sub>2</sub> with mobility at 163.7 cm<sup>2</sup>·V<sup>-1</sup>·s<sup>-1</sup>; (**c**) CuPc with mobility at 0.007 cm<sup>2</sup>·V<sup>-1</sup>·s<sup>-1</sup>; (**d**) CuPc with mobility at 0.22 cm<sup>2</sup>·V<sup>-1</sup>·s<sup>-1</sup>.

As shown in Figures S4, the electrical characteristic of the SnO<sub>2</sub> nanobelt FET with gas dielectric was measured in dry air, and then in N<sub>2</sub>. The measurements in N<sub>2</sub> were carried out after the N<sub>2</sub> stream was introduced into the chamber for 1 h and 3 h, respectively. These measured results show that the device performance is highly repeatable both in dry air and N<sub>2</sub>.



Figure S4. Transfer curves of the gas-dielectric SnO2 nanobelt based FET tested in dry air and N2.

#### 5. Testing Process for Gas Sensing

Figure S5 shows a schematic representation of the experimental setup for gas detection. The device was placed into the stainless testing chamber and its leading wires were connected to a Keithley 4200-SCS station, for electrical characterization. The testing gas was introduced to the chamber through the stainless pipes. Prior to the testing, pure dry N<sub>2</sub> was introduced into the chamber for 2 h, so as to purge the testing chamber of undesired residual gases. The electrical characteristics of the FET nanosensor were first measured in the N<sub>2</sub> for 2 h to confirm the stability of devices. Subsequently, the testing gas was introduced to the chamber and diluted by N<sub>2</sub>. The stream of N<sub>2</sub> and the testing gas was introduced under the controlled flow rate, by Mass Flow Controllers (MFC CS200A). The total gas flow rate was kept at 500 sccm. At the end of the testing cycle, the testing gas was collected by an alkaline solution.



Figure S5. Schematic images of the experimental setup used for gas sensing.

6. Response to Three Analytes (NO<sub>2</sub>, NO and H<sub>2</sub>S) in a Solid-Dielectric Device



**Figure S6.** (a) Schematic image of a solid-dielectric nanobelt device; (b) Transfer curves of the solid-dielectric device to various concentrations of H<sub>2</sub>S. The well overlapped curves show that the solid-dielectric device does not respond to H<sub>2</sub>S; (c, d) Parameter percentage variation at different NO<sub>2</sub> and NO concentrations in solid-dielectric device. All electrical measurements were carried out at room temperature.

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