

1 *Article ESI*

# 2 **Modular Pressure And Flow Rate Balanced** 3 **Microfluidic Serial Dilution Networks for** 4 **Miniaturised Point-Of-Care Diagnostic Platforms**

5 **Nikolaos Vasilakis <sup>1,\*</sup>, Konstantinos I. Papadimitriou <sup>1</sup>, Hywel Morgan <sup>1,2</sup> and Themistoklis**  
6 **Prodromakis <sup>3</sup>**

7 <sup>1</sup> Nanoelectronics & Nanotechnology Research Group, Electronics and Computer Science, University of  
8 Southampton, Southampton, SO17 1BJ, UK

9 <sup>2</sup> Institute for Life Sciences, University of Southampton, Southampton, SO17 1BJ, UK

10 <sup>3</sup> Zepler Institute for Photonics and Nanoelectronics, University of Southampton, Southampton, SO17 1BJ,  
11 UK

12 \* Correspondence: n.vasilakis@soton.ac.uk;

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## 15 **PCB-based device fabrication process:**

16 In Figure 1S the various layers that comprise the PCB-based device are illustrated. The  
17 fabrication procedure is as follows:

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19 1. The PCB microfluidic network (rigid FR-4 substrate and lithographically patterned DPR) was  
20 fabricated by our industrial partner (Newbury Electronics Ltd) utilising standard PCB  
21 manufacturing techniques.

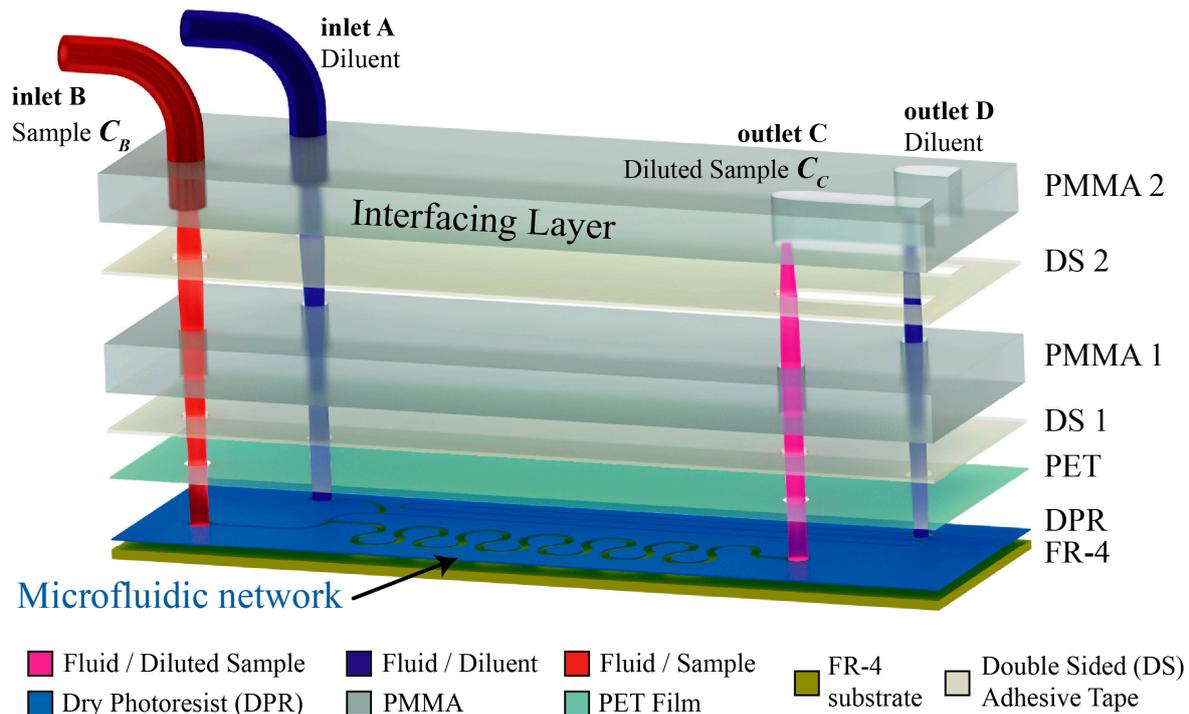
22 2. A PET film (50  $\mu\text{m}$  thickness, PET, VWR <sup>®</sup> Polyester Sealing Films for ELISA) was machined  
23 (opening inlet and outlet ports) using the CO<sub>2</sub> laser cutter (Epilog Mini 24 Legend Laser System,  
24 USA) and subsequently both PCB microfluidic network and PET film were placed in a pouch  
25 laminator (60 °C, lamination speed around 4 mm/s).

26 3. The two interfacing PMMA layers (3 mm thick PMMA 1, and 5 mm thick PMMA 2 as shown in  
27 SF1) were prepared in two steps:

28 a. A double-sided (DS) adhesive 127  $\mu\text{m}$  thick film (3M<sup>™</sup> High Performance  
29 Acrylic Adhesive 200MP) was laminated on the PMMA surface employing  
30 the pouch laminator (room temperature, lamination speed around 4 mm/s).

31 b. The stack (PMMA and DS) was milled using the CO<sub>2</sub> laser cutter opening  
32 inlet and outlet ports.

33 4. Finally, the two interfacing layers (PMMA 1 – DS and PMMA 2 – DS) were aligned and  
34 stacked on top of the sealed PCB microfluidic device. A 40 kPa constant pressure was applied  
35 for 1h to bond the components together at room temperature.



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Figure 1S: Exploded view presenting the various PMMA, PET and double sided adhesive tape (DS) layers used to interface the PCB fabricated prototype with the required tubing for the dilution performance characterisation experiments. Both outlets C and D supply the two wells with diluted sample and bypassing diluent respectively.

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#### PMMA device fabrication process:

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The top PMMA interfacing layer (see Figure SF2, 3 mm thick PMMA 3 and DS 3) was fabricated in two steps:

- a. A DS adhesive 127  $\mu\text{m}$  thick film (3M™ High Performance Acrylic Adhesive 200MP) was laminated on the PMMA surface employing the pouch laminator (room temperature, lamination speed  $\sim$  4 mm/s).
- b. The stack (see Figure SFa, PMMA 3 and DS 3) was milled using the CO<sub>2</sub> laser cutter opening inlet and outlet ports.

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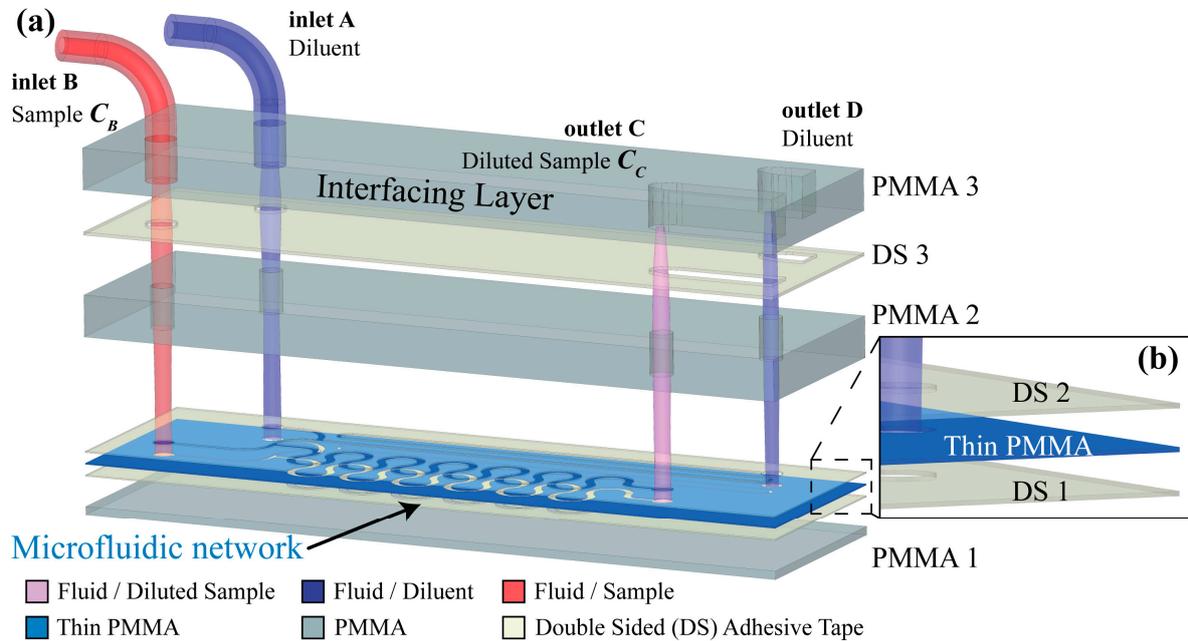
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The final step of the PMMA-based prototype required the top interfacing PMMA layer (Figure SF2a, PMMA 3 and DS 3, fabrication steps 5 and 6) and the microfluidic device (fabrication steps 1 to 4) to be aligned and stacked. A 40 kPa constant pressure was applied for 1h to bond the components together at room temperature.



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Figure 2S: Exploded view presenting the various PMMA and double sided adhesive tape (DS) layers used to fabricate the microfluidic PMMA prototype.

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