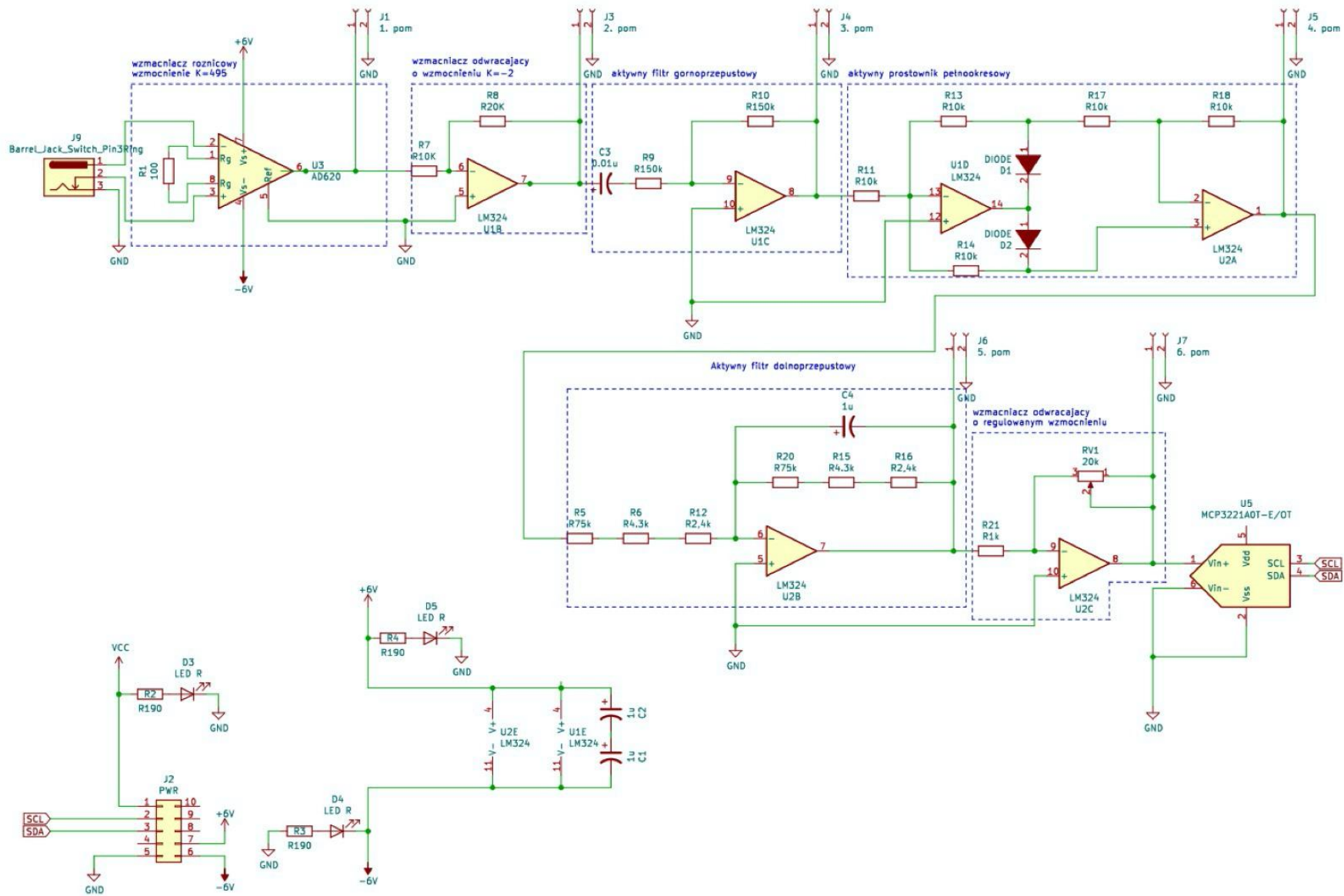
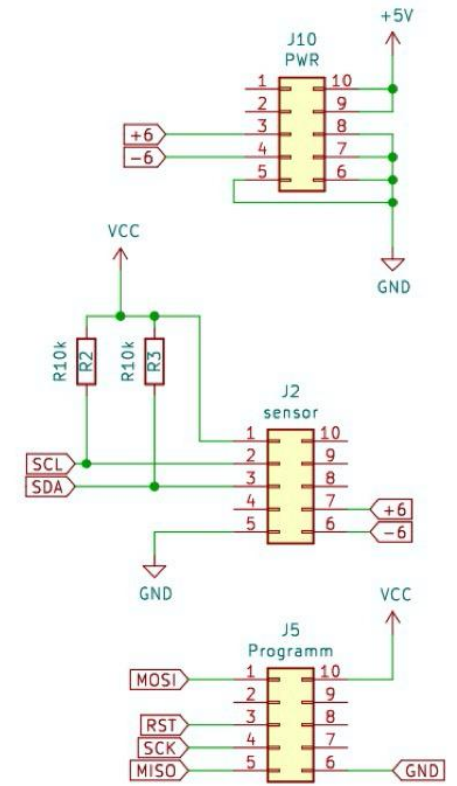
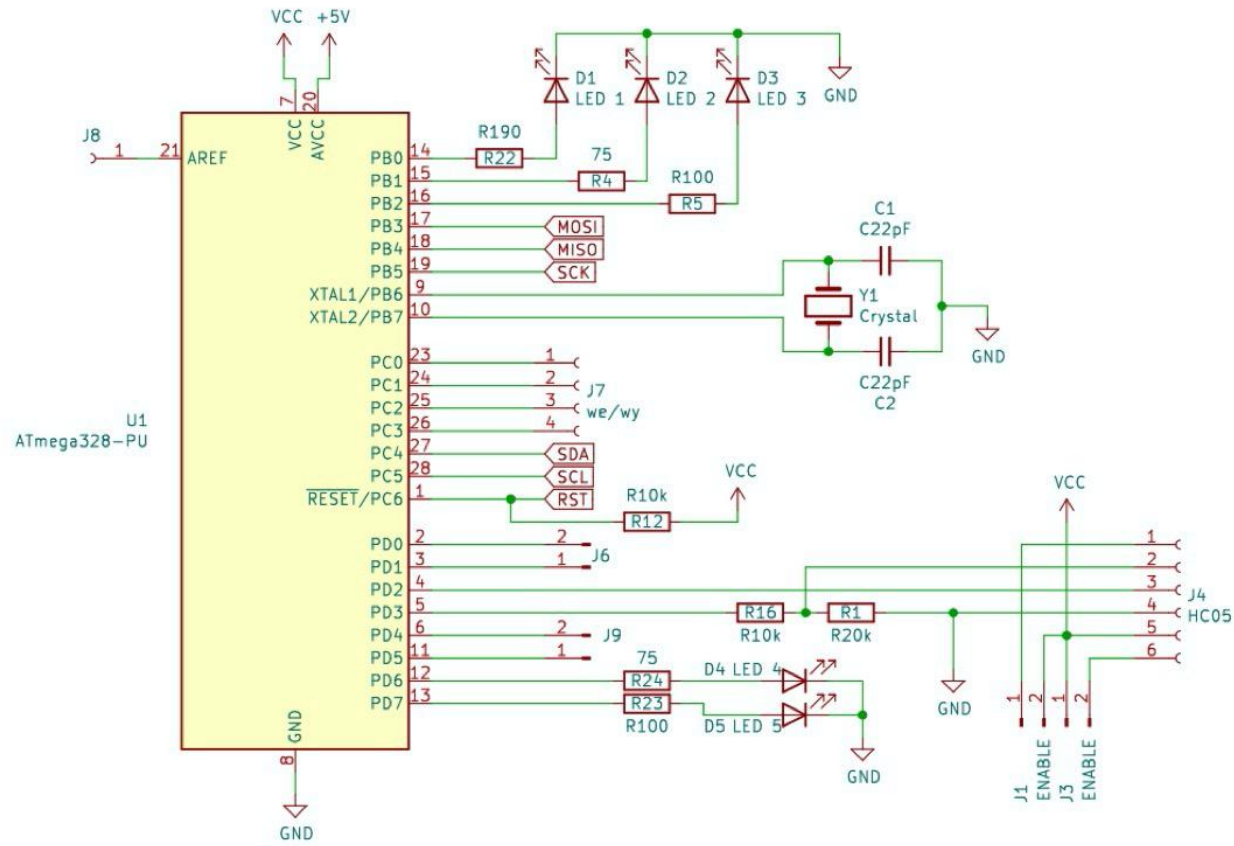
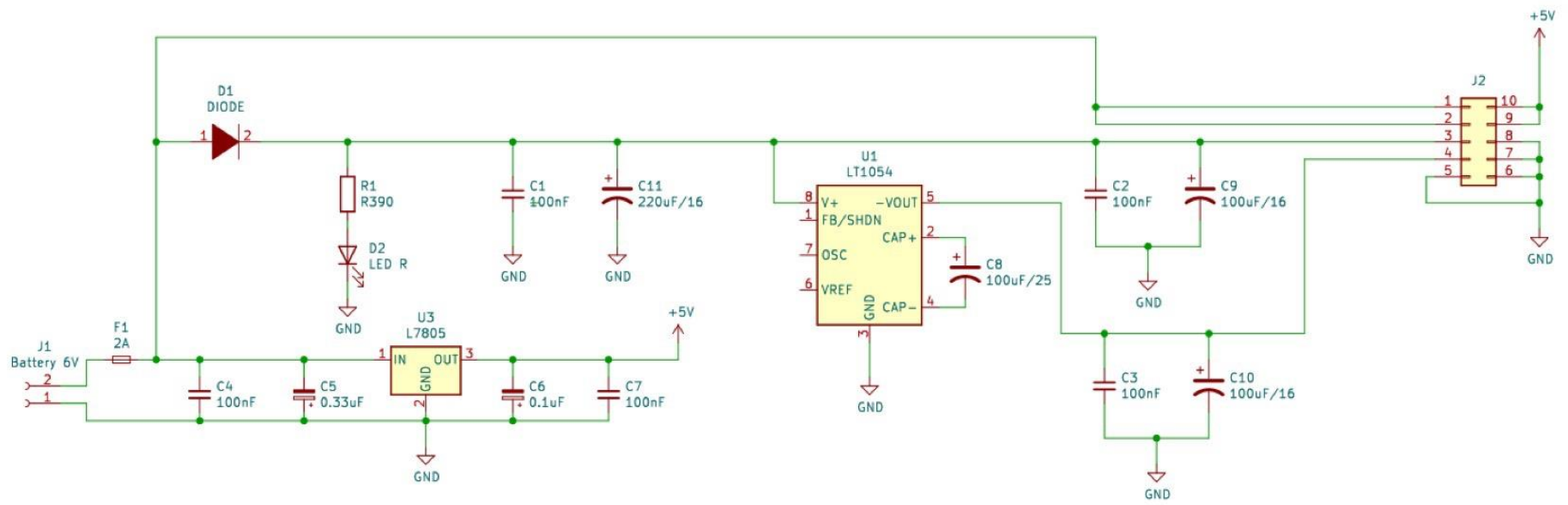


Supplementary S1. Diagram of data acquisition; communication handling circuit diagram; diagram of a symmetrical power supply circuit







Supplementary S2. Diagrams of designed electric circuits of the BLDC motor control system made in the KiCad environment

