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An Improved Commutation Prediction Algorithm to Mitigate Commutation Failure in High Voltage Direct Current

Xinnian Li 1,* , Fengqi Li 2 , Shuyong Chen 3 , Yanan Li 4 , Qiang Zou 5 , Ziping Wu 6 and Shaobo Lin 3

- School of Electrical Engineering, Beijing Jiaotong University, Beijing 100044, China
- State Grid Operation Company, Beijing 100052, China; fq-li@sgcc.com.cn
- State Key Laboratory of Power Grid Safety and Energy Conservation (China Electric Power Research Institute), Beijing 100192, China; chensy@epri.sgcc.com.cn (S.C.); linshaobo@epri.sgcc.com.cn (S.L.)
- State Power Economic Research Institute, Beijing 102209, China; liyanan@chinasperi.sgcc.com.cn
- ⁵ NR Electric Co., Ltd., Nanjing 211102, China; zouq@nrec.com
- Information Trust Institute, University of Illinois at Urbana-Champaign, Champaign, IL 61801, USA; zw18@illinois.edu
- * Correspondence: 12117376@bjtu.edu.cn or lixn@epri.sgcc.com.cn; Tel.: +86-010-8281-4193

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Abstract: Commutation failure is a common fault for line-commutated converters in the inverter. To reduce the possibility of commutation failure, many prediction algorithms based on alternating current (AC) voltage detection have already been implemented in high voltage direct current (HVDC) control and protection systems. Nevertheless, there are currently no effective methods to prevent commutation failure due to transformer excitation surge current. In this paper, an improved commutation failure prediction algorithm based on the harmonic characteristics of the converter bus voltage during transformer charging is proposed. Meanwhile, a sliding-window iterative algorithm of discrete Fourier transformation (DFT) is developed for detecting the voltage harmonic in real time. This method is proved to be an effective solution, which prevents commutation failure in cases of excitation surge current, through experimental analysis. This method is already implemented into TianShan-ZhongZhou (TianZhong) \pm 800 kV ultra high voltage direct current (UHVDC) system.

Keywords: commutation failure prevention (CFPREV); high voltage direct current (HVDC); inrush current; harmonic detection; predictive control

1. Introduction

High voltage direct current (HVDC) is widely used for bulk power transmission with lower energy loss and flexible control ability [1–6]. Thus, in the last decade, more than 20 HVDC projects have been completed in China. It is inevitable for line commutate converter (LCC) thyristor valves to experience a commutation failure when a fault occurs around the inverter AC network, especially under conditions in which the inverter AC bus is 10% to 15% below the rated voltage [7].

Efforts have been made to study the reasons for commutation failure and the methods to mitigate such an issue [8,9]. Many methods regarding the prediction and prevention of commutation failures were presented in most of the literature [10–20], such as power component fault detection, direct-current predictive control, commutation area detection, and so on. A more detailed description of the commutation prediction method, which has been successfully used in HVDC projects, was proposed [21,22]. In general, the above commutation prediction methods can effectively detect voltage sags due to AC side ground fault and prevent the potential commutation failure or even continuous commutation failure via increasing the extinction angle proportionally to the voltage sag.

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However, the above methods are difficult to perform correctly when the converter bus voltage is affected by harmonics such as the transformer excitation surge current. TuanLin-FengJing \pm 500 kV HVDC was tripped by commutation failure protection in 2013, when LianTang, a ultra high voltage alternating current UHVAC station nearby, was energizing its transformer [23]. GaoLing back-to-back HVDC went through four continuous commutation failures on 21 August 2015, when SuiZhong, an adjacent power plant, was energizing its boost transformer. The active power through TianShan-ZhongZhou (TianZhong) \pm 800 kV UHVDC fluctuated between 3660 MW and 4040 MW for 13 s after GuanDu, a substation nearby, energized its transformer on 21 March 2016.

The excitation surge current produced by energizing a large transformer could be damaging to the grid. Since an excitation surge current can be sustained for longer than 10 s and can be magnified after traveling along the power transmission line [24], it could be the most reasonable practical explanation for the continuous commutation failure of HVDC.

Following the analysis of the response of the commutation failure prediction method applied to the TianZhong \pm 800 kV UHVDC project on 21 March 2016, an improved commutation prediction method based on the harmonic characteristics of the converter bus voltage is proposed in this paper. This method is tested using an real-time digital simulator (RTDS) simulation platform, and the experimental results demonstrate its effectiveness. Moreover, this method is already implemented in the TianZhong \pm 800 kV UHVDC system and will be applied to other HVDCs in the near future; it is especially suitable for those HVDCs that feed power into the same nearby grid.

2. Commutation Failure

For a three-pulse thyristor bridge, during the commutation of the direct current from one valve to another, if the conducting valve cannot recover interdiction capability from the reverse voltage, the valve will continue to conduct while the succeeding valve fails to do so. This phenomenon is called commutation failure.

Influencing Factors of Commutation Failure

The following equation can be obtained from:

$$\gamma = \arccos(\frac{2X_r I_d}{\sqrt{3}E_{2m}} + \cos\beta) \tag{1}$$

Equation (1) reveals the main impact factors of the converter commutation, which is characterized by the extinction angle (γ), including the commutating reactance X_r , direct current (DC) steady-state current at the inverter side I_d , the amplitude of commutation voltage E_{2m} , and the trigger advance angle β .

The partial derivatives of the variables on the right side of the Formula (1) can be obtained:

$$\begin{cases}
\frac{\partial \gamma}{\partial I_d} = \frac{-1}{\sqrt{1 - \cos^2 \gamma}} \frac{2X_r}{\sqrt{3}E_{2m}} \\
\frac{\partial \gamma}{\partial E_{2m}} = \frac{1}{\sqrt{1 - \cos^2 \gamma}} \frac{2X_r I_d}{\sqrt{3}E_{2m}^2} \\
\frac{\partial \gamma}{\partial X_r} = \frac{-1}{\sqrt{1 - \cos^2 \gamma}} \frac{2I_d}{\sqrt{3}E_{2m}} \\
\frac{\partial \gamma}{\partial \beta} = \frac{\sin \beta}{\sqrt{1 - \cos^2 \gamma}}
\end{cases} (2)$$

On the inverter side, the firing angle is greater than $\pi/2$ radians. The overlap angle varies roughly with DC current, which changes approximately inversely with the DC voltage on the inverter side. Equation (2) shows that commutation failures in HVDC systems are mainly caused by voltage magnitude reduction, DC current increment, triggering advance angle decrement, and commutation reactance increment.

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There are two methods to prevent commutation failure:

- (1) Increasing the γ angle in normal conditions, which will consume much more reactive power.
- (2) When a commutation failure is detected in an HVDC system, the inverter decreases the α angle in order to increase the commutation margin and avoid commutation failure.

3. Analysis of the Response of the Commutation Failure Prediction Method for TianZhong \pm 800 kV Ultra High Voltage Direct Current Project

3.1. The Commutation Failure Prediction Method in an High Voltage Direct Current Project

The basic reason for commutation failure is a lack of commutation margin and results over a short extinguishing time. If a high level γ angle is maintained, this will cost much more in terms of reactive power. In the TianZhong \pm 800 kV UHVDC control system, there is a model for commutation failure prediction (CFPREV), which can solve this antinomy preferably, as shown in Figure 1. This method includes two parallel parts: one is based on a zero-sequence component for unsymmetrical faults such as single-phase earth faults, and the other one is based on an abc- $\alpha\beta$ transformation to detect three-phase symmetrical faults.

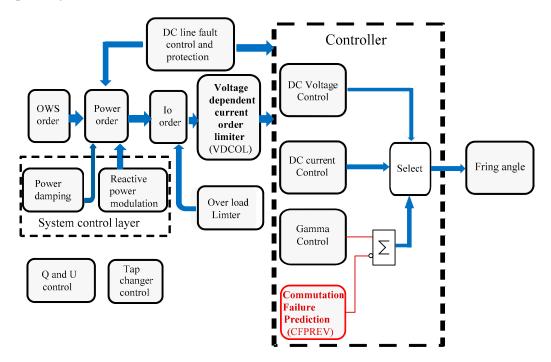


Figure 1. Structure of the TianZhong \pm 800 kV ultra-high voltage direct current (HVDC) control system.

If an AC system has a single-line ground fault, the converter bus voltage will contain a zero-sequence component, Z, which is obtained by adding up the three-phase instantaneous voltage, as shown in Equation (3):

$$u_0 = u_a + u_b + u_c \tag{3}$$

As shown in Figure 2, if the zero sequence component of the converter bus voltage is greater than a preset value, the commutation prediction logic will switch from a normal control strategy to another strategy in the next 20 ms by immediately decreasing the firing angle for a certain degree, which is proportional to the maximal zero sequence component, and will maintain it for a certain time (12 ms in this case). Once the AC fault has been cleared, i.e., when the zero sequence component of the converter bus voltage is lower than its setting, the prediction logic will increase the firing angle order slowly with a time constant of 30 ms, enabling the commutation prediction logic to return to the normal control strategy smoothly.

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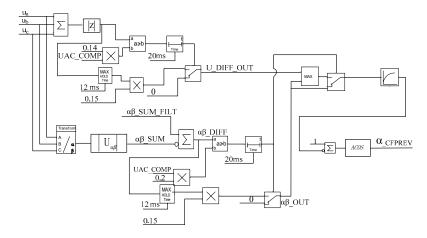


Figure 2. A simplified logic diagram of commutation failure prediction (CFPREV).

Another part is based on abc- $\alpha\beta$ transformation to detect three-phase faults. abc- $\alpha\beta$ transformation uses the rotating vector to express three phase voltages:

$$u_{\alpha} = \frac{2}{3}u_a - \frac{1}{3}(u_b + u_c) \tag{4}$$

$$u_{\beta} = \frac{\sqrt{3}}{3}(u_b - u_c) \tag{5}$$

In the above formula, u_{α} and u_{β} correspond to the projection of the vector on to the α -axis and the β -axis in the $\alpha\beta$ -plane. By transformation, we can obtain a rotating vector, ω , which rotates in the $\alpha\beta$ -plane at a fixed speed. The magnitude of rotating vector $\alpha\beta$ _SUM can be calculated using the formula below:

$$u_{\alpha\beta} = \sqrt{u_{\alpha}^2 + u_{\beta}^2} \tag{6}$$

When a fault occurs in the AC system, $\alpha\beta_SUM$ is compared to a filtered $\alpha\beta_SUM$, with a relatively large time constant as the pre-fault voltage. If the difference between the two values is greater than the pre-defined level, the CFPREV model will kick in, convert the difference to an angle value, and finally subtract the angle value from the inverter α angle, advancing the firing instant, and leaving a bigger commutation margin. In the TianZhong HVDC project, the CFPREV program is installed in digital signal processor (DSP), and the program step is 50 µs. For a 50 Hz AC system, this value is converted to the angle value of 0.9°. CFPREV can act immediately after an AC system fault occurs.

3.2. The Performance of Commutation Failure Prediction During the Disturbance

The TianZhong \pm 800 kV UHVDC project was operating with four bipolar converters rated at 4000 MW on 21 March 2016 before the disturbance occurred.

The waveform during the disturbance is given in Figure 3. The output signal of the CFPREV system, α_{CFPREV} in the third row, was unstable. It jumped 8.4° in 1 ms, remained stable for 20 ms, and then decreased back in the next 20 ms, repeating the process every 40 ms. Since the firing angle order increased quickly and decreased slowly, the extinction angle increased quite a lot over a long period of time. Since more reactive power was consumed by the inverters, three seconds later a filter sub-bank was switched in. During this process, the transmission power changed in the range between 3660 MW and 4040 MW as a result of the extinction angle variations.

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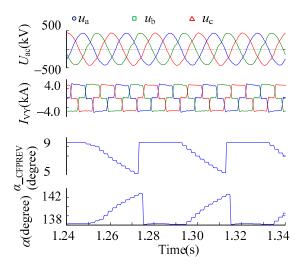


Figure 3. The performance of the commutation failure prediction during the disturbance. (Note: U_{ac} is the converter bus voltage in the inverter, I_{VY} is the Yy transformer current in the valve side, $\alpha_{\text{-CFPREV}}$ is the output degree of commutation failure prediction (CFPREV), and α is firing angle.)

3.3. Analysis of the Converter Bus Voltage

The zero sequence component of the converter bus voltage, which repetitively changed between 60 kV at 20 ms with abundant harmonics, is shown in Figure 4. The peak value is around 60 kV. This explains the reason why the output of the commutation failure prediction also varied every 40 ms.

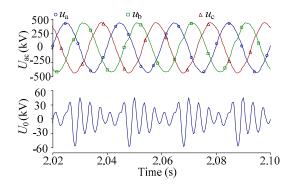


Figure 4. Zero sequence component of the converter bus voltage.

The converter bus voltage frequency analysis is conducted to understand the reason for the above zero sequence component. Figure 5 shows that abnormal third, fourth, and fifth harmonic voltages are observed in phase B, which is the key feature of transformer excitation surge current.

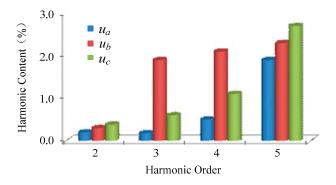


Figure 5. Frequency analysis of the converter bus voltage.

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Further investigation demonstrates that the transformer was energized before the disturbance in the GuanDu substation, which is a 500 kV substation near the ZhongZhou converter station. The connection between the GuanDu substation and the ZhongZhou converter station is presented in Figure 6. It is easy to observe that the transformer surge current transferred from GuanDu to ZhongZhou through the AC lines connecting them.

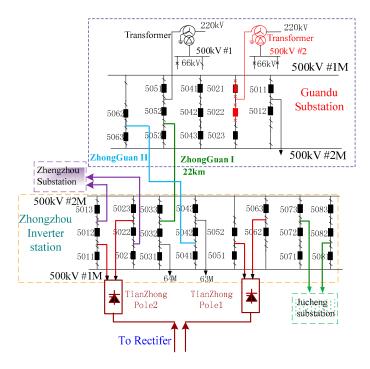


Figure 6. The wiring diagram of the AC/DC system.

The transformer excitation surge current recorded at the GuanDu substation is shown in Figure 7, with phase B containing high inrush current. It agrees with the frequency analysis result of the converter bus voltage at the ZhongZhou converter station in Figure 5.

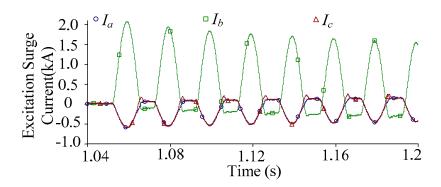


Figure 7. The transformer excitation surge current recorded at the GuanDu substation.

4. A Novel Commutation Failure Prediction Method Based on the Harmonic Characteristics of the Converter Bus Voltage

According to above analysis, the present commutation failure prediction method fails to correctly handle the long term and periodically varying zero sequence component stimulated by transformer excitation current. To solve this problem, a new commutation prediction method based on the harmonic characteristics of the converter bus voltage during transformer charging is developed as follows.

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4.1. Harmonic Detection Based on the Sliding-Window Iterative Algorithm of Discrete Fourier Transformation (DFT)

For any periodic signal $x(\tau)$ with a finite bandwidth, if its period is T, the bandwidth is the fundamental angular frequency ω to $N_{\text{max}} \cdot \omega$, and the DFT [25] is expressed as follows:

$$x(k\tau) = \sum_{n=1}^{N_{max}} A_n \cos(n\omega k\tau) + B_n \sin A_n \cos(n\omega k\tau)$$
 (7)

$$A_n = \frac{2}{N} \sum_{i=0}^{N-1} x(i\tau) \cos(n\omega i\tau)$$
 (8)

$$B_n = \frac{2}{N} \sum_{i=0}^{N-1} x(i\tau) \sin(n\omega i\tau)$$
(9)

where $k = 0, 1, 2, ..., (N - 1), \tau = T/N$, and N is the periodic sampling frequency.

It can be seen that the N sampling data for a complete sampling period, which is defined by the fixed starting point (i = 0), is simultaneously involved in these formulas, so the amount of calculation is huge. Obviously, these formulae are not suitable for the fast detection of instantaneous harmonic voltage.

According to the real-time requirement of harmonic voltage detection, Formulas (8) and (9) are improved as follows:

$$A_n = \frac{2}{N} \sum_{i=N_{\text{cur}}}^{N_{\text{cur}}-N+1} x(i\tau) \cos(n\omega i\tau)$$
 (10)

$$B_n = \frac{2}{N} \sum_{i=N_{\rm cur}}^{N_{\rm cur}-N+1} x(i\tau) \sin(n\omega i\tau)$$
 (11)

where N_{cur} represents the latest sampled data points and $x(i\tau)$ represents the sampling data before i sampling period. By comparing these equations with the previous two, in the improved equation for A_n , B_n , $(i = N_{\text{cur}})$ replaced (i = 0) and $(N_{\text{cur}} - N + 1)$ replaced (N - 1); that is, the latest real-time sampling data is used for the calculation of harmonic voltage, while the oldest sampling data are eliminated accordingly.

At the same time, the second harmonic component can be calculated by the following formula:

$$x_2(k\tau) = A_2 \cos(2\omega k\tau) + B_2 \sin(2\omega k\tau) \tag{12}$$

$$A_2 = \frac{2}{N} \sum_{i=N_{\text{cur}}}^{N_{\text{cur}}-N+1} x(i\tau) \cos(2\omega i\tau)$$
(13)

$$B_2 = \frac{2}{N} \sum_{i=N_{\text{cur}}}^{N_{\text{cur}}-N+1} x(i\tau) \sin(2\omega i\tau)$$
(14)

 A_2 and B_2 must be calculated simultaneously through Formulas (13) and (14) during one sampling interval in order to obtain the desired second harmonic voltage in real time. The amount of computation is great when the sampling frequency of the system is high (i.e., N is relatively large). For this reason, a new computational model is developed to simplify the calculation process of the sliding-window iterative algorithm. It is shown in Figure 8.

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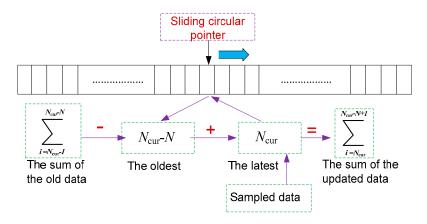


Figure 8. The sliding window iterative discrete Fourier transformation (DFT) method.

According to the calculation model in Figure 8, N point sampled data in the power frequency period is stored in the continuous data space after being multiplied with the appropriate rotation factors. The present sampled data storage location is determined by the loop pointer. After completing a full period of N points sampled data calculation, the pointer should point back to the starting position of the storage space so as to start the next cycle of replacement data simultaneously. Therefore, Equations (15) and (16) can be simplified as the calculation of Formulas (13) and (14):

$$\sum_{i=N_{\text{cur}}-N+1}^{N_{\text{cur}}-N+1} x(i\tau) \cos(\omega i\tau) = \sum_{i=N_{\text{cur}}-1}^{N_{\text{cur}}-N} x(i\tau) \cos(\omega i\tau) - x((N_{\text{cur}}-N)\tau) \cos(\omega(N_{\text{cur}}-N)\tau) + x(N_{\text{cur}}\tau) \cos(\omega N_{\text{cur}}\tau)$$
(15)

$$\sum_{i=N_{\text{cur}}-N+1}^{N_{\text{cur}}-N+1} x(i\tau) \sin(\omega i\tau) = \sum_{i=N_{\text{cur}}-1}^{N_{\text{cur}}-N} x(i\tau) \sin(\omega i\tau) - x((N_{\text{cur}}-N)\tau) \sin(\omega(N_{\text{cur}}-N)\tau) + x(N_{\text{cur}}\tau) \sin(\omega N_{\text{cur}}\tau)$$
(16)

The sum operation of Formulas (13) and (14) is reduced to a subtraction and an addition by means of the above transformation; the new data sum is restored to the unit of the old data sum storage after the computation. Therefore, the whole process of the iterative calculation of the sliding window can be completed in the power frequency cycle at the initialization stage. The summation operation can be completed absolutely one sampling period later. In this way, the operation duration of harmonic voltage detection will be significantly reduced by using the iterative sliding window algorithm, and the real-time performance of such a system is also improved greatly.

The sliding window iterative algorithm is much simpler and faster than fast Fourier transformation (FFT) when the single harmonic voltage is calculated. It is very important for the frequency dividing control when designing a relevant controller.

4.2. A New Commutation Failure Prediction Method

Through comparison with the present CFPREV logic used in the TianZhong \pm 800 kV UHVDC project, which is shown in Figure 2, an improved commutation failure prediction algorithm is proposed based on the harmonic characteristics of the converter bus voltage during transformer charging. The second, fourth, and fifth harmonics of the converter bus are calculated and compared with their settings. If any of them are greater than their settings, the signal (Uac_Harm_Blk) is set, and the output of the prediction logic is held as its setting value (CFPREV_Harm). The logic is shown in Figure 9.

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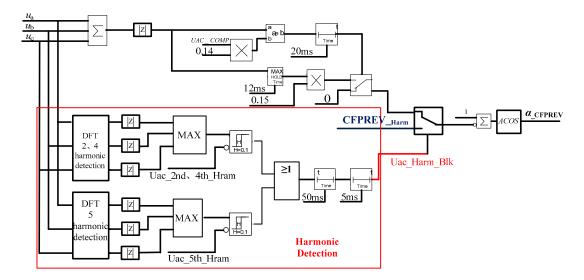


Figure 9. A simplified logic diagram of the new commutation failure prediction method to protect against transformer excitation surge current.

$$U_{\text{harm_max}} = \text{MAX}(U_{\text{ac_2}}\text{nd_Harm}, U_{\text{ac_4}}\text{th_Harm}, U_{\text{ac_5}}\text{th_Harm})$$
 (17)

$$CFPREV_{Harm} = \begin{cases} \frac{U_{harm_max}}{k_1} * k_{set}, \ U_{harm_max} > k_1 \\ k_{set}, \ U_{harm_max} \le k_1 \end{cases}$$
(18)

5. Experiments and Results

The experiment through RTDS is carried out to reproduce the above process and verify the proposed algorithm. The RTDS experimental system is shown in Figure 10. The topology of the TianZhong \pm 800 kV UHVDC transmission system and the AC network of the ZhongZhou converter station nearby are modeled in detail. The model includes the main circuit with the primary equipment, consisting of converters, transformers, DC lines, DC filter, AC filter, and a control and protection system in accordance with the practical system configuration. These detailed parameters are shown in Tables 1 and 2 and Figure 11.

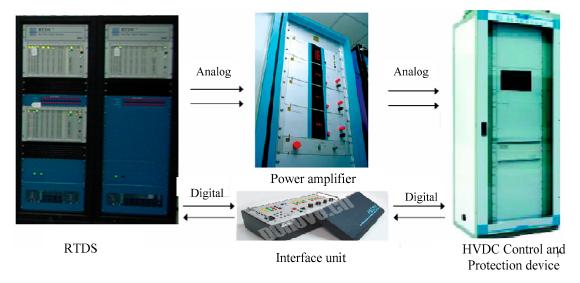


Figure 10. The real-time digital simulator experimental system.

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Serial Number	Items	Units	Parameters
1	Bipolar rated power	MW	8000
2	Rated voltage	kV	800
3	Rated current	kA	5.0
4	Rated firing angle at rectifier	0	15
5	Rated extinction angle at inverter	0	17
6	Smoothing reactor	mΗ	6×50
7	the commutating reactance	%	24
8	The distance of DC lines	km	2192

Table 1. The main parameters of Tianzhong \pm 800 kV ultra high voltage direct current (UHVDC).

Table 2. The AC/DC filter of TianZhong \pm 800 kV UHVDC.

Items	Location	Туре	
DC filter	TianShan ZhongZhou	1*HP2/39, 1*HP12/24 1*HP2/39, 1*HP12/24	
AC filter	TianShan ZhongZhou	4*BP11BP13, 4*HP24/36, 3*HP3,5*SC 8*HP12/24, 2*HP3, 9*SC	

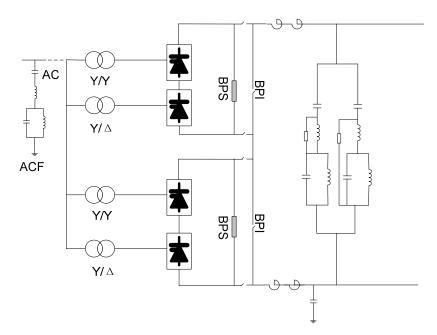


Figure 11. A simplified diagram of the TianZhong \pm 800 kV UHVDC transmission project.

5.1. To Reproduce the Process with the Traditional Commutation Failure Prediction Strategy

After the transformer excitation current recorded in Figure 7 is injected into the bus bar of the GuanDu substation, the same transient behavior is observed in HVDC according to Figure 12. The output of the commutation failure prediction (α_{CFPREV}) swings in the range between nine and 0° at a periodicity of 40 ms, resulting in a disturbance of both the DC voltage and current.

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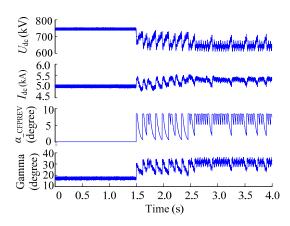


Figure 12. Reproduction of the disturbance due to the transformer excitation current recorded at the GuanDu substation and the traditional commutation failure prediction algorithm.

5.2. To Verify the Proposed Commutation Failure Prediction Strategy under the Same System Configuration

The proposed commutation failure prediction strategy is implemented into the control system and evaluated by injecting the same transformer excitation current into the bus bar of the GuanDu substation. The RTDS experimental results are shown in Figure 13.

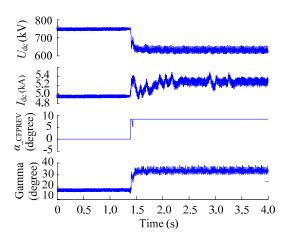


Figure 13. Reproduction of the disturbance due to the transformer excitation current recorded at GuanDu substation and the proposed commutation failure prediction algorithm.

The output of the commutation failure prediction changes from 0° to 9° as soon as the transformer of the GuanDu substation is energized and the zero sequence component is detected at the converter bus of the ZhongZhou converter station. It is beneficial for the inverter to prevent commutation failure due to the converter bus voltage distortion caused by the nearby excitation surge current.

Different from Figures 12 and 13, α_{CFPREV} remained at 9° the whole time, until the excitation current of GuanDu's transformer decreased to an acceptable level. Throughout the entire process, the DC voltage and DC current remained stable.

5.3. To Verify the Proposed Commutation Failure Prediction Strategy Under 1000 kV Ultra High Voltage Transformer Charging

With the increase in the AC voltage level, the capacity of the 1000 kV UHV AC transformer gradually developed from 3000 MVA to 4500 MVA. When the UHV Transformer is charged, the inrush current amplitude is larger and lasts longer, and the problem of harmonic voltage distortion and voltage reduction becomes more and more serious.

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Figure 14 shows the waveforms of the charging inrush current of the 1000 kV UHV Transformer affecting the HVDC transmission system. The commutation failure of the inverter occurs at the same time, which is caused by the charging inrush current of the UHV Transformer. The CFPREV model kicks in, and the output signal of this model is 20.6°. As the inrush current attenuates slowly, the zero sequence component voltage of the original commutation failure prediction is greater than the set value for a long time, and the extinction angle is maintained near 41°. The DC voltage drops to 561 kV, which leads to the voltage dependent current order limiter (VDCOL) acting (as shown in Figure 15), and the DC current is limited at 0.88 p.u. (DC power is 0.64 p.u.). In 2.4 s, the CFPREV model based on the harmonic detection function is put into operation, and the output signal of this model drops to 6°. The extinction angle is maintained near 22°; the DC voltage rises to 694 kV, which is less than the action value of VDCOL; and the DC current goes up to 1.06 p.u. (DC power is 0.93 p.u.). Therefore, our model can not only prevent continuous commutation failure from transformer excitation surge current but also can mitigate the sharp decrease of DC power.

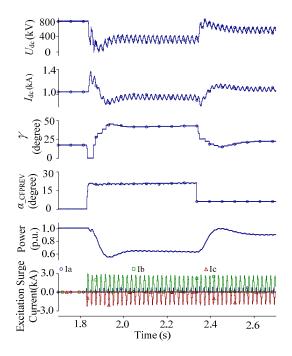


Figure 14. Reproduction of the disturbance due to a 1000 kV UHV AC transformer excitation current and the proposed commutation failure prediction algorithm.

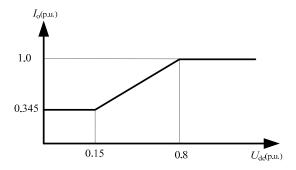


Figure 15. The static characteristic of the voltage dependent current order limiter (VDCOL).

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5.4. To Verify the Proposed Commutation Failure Prediction Strategy in TuanLin-FengJing \pm 500 kV High Voltage Direct Current

TuanLin-FengJing (LinFeng) \pm 500 kV HVDC was tripped by commutation failure protection in 2013, when LianTang, a UHVAC station nearby, was energizing its transformer [23]. In reference [23], the LinFeng HVDC model was established by using a detailed model consistent with the practical project. Through simulation studies, we put forward the measures to optimize the pole control system such as (1) to cancel the instantaneous current compensation function in the pole control system and (2) to optimize the functions of VDCOL and DC controller commutation failure prediction.

In order to verify the proposed commutation failure prediction strategy in LinFeng HVDC, the LinFeng HVDC model was established in RTDS, and an improved commutation failure prediction algorithm based on the harmonic characteristics of the converter bus voltage is applied. In order to analyze a cyclical commutation failure problem, the practical magnetizing surge current of LianTang first needs to be calculated by the simulation (the initial value is 4699 A). Next, the same magnetizing surge current is injected into the AC system at the inverter side of LinFeng so as to study the commutation failure of LinFeng HVDC under the magnetizing surge current distortion and propose a solution for periodic commutation failure accordingly. Figure 16 is the waveform before the optimization of the commutation failure prediction. Five periodic commutation failures happen at the inverter, which behaves in the same way as in [23].

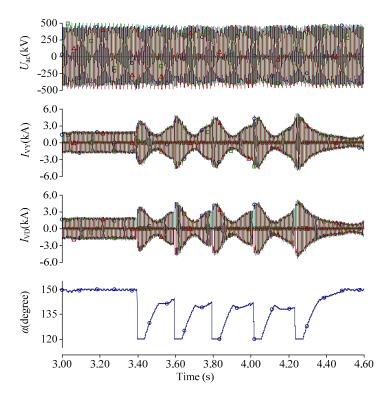


Figure 16. The test waveform before the optimization of the commutation failure prediction.

Figure 17 is a test waveform applied to an improved commutation failure prediction algorithm. In the third second, the second, fourth, and fifth harmonic voltage of the converter bus are greater than their settings, and the output signal of this model (CFPREV $_{-Harm}$) increases to 6°. The firing angle is maintained near 144°, and the extinction angle rises up to 23°, which increase the commutation margin. Therefore, it can prevent the continuous commutation failure caused by transformer excitation surge current. The method proposed in this paper is more straightforward and effective in cases in which commutation failure is caused by inrush current.

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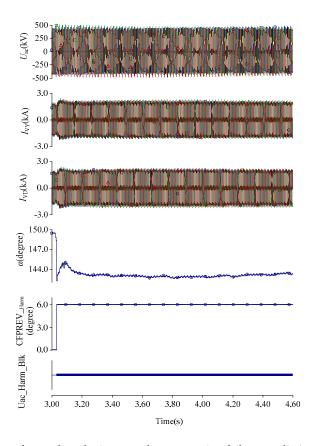


Figure 17. The test waveform when the improved commutation failure prediction algorithm is applied. (Note: $I_{\rm VD}$ is the Yd transformer current in the valve side; CFPREV_Harm is the output of CFPREV based on harmonic detection, Uac_Harm_Blk is the action signal of CFPREV based on harmonic detection.).

6. Conclusions

The traditional prediction algorithm of commutation failure will be inappropriate when the long-term voltage distortion of a converter bus is caused by the energizing transformer near the inverter station, and the distortion consequently leads to continuous commutation failures. This is potentially dangerous to AC/DC systems.

In this paper, an improved commutation failure prediction algorithm is proposed based on the harmonic characteristics of the converter bus voltage during transformer charging. Meanwhile, a sliding-window iterative algorithm of discrete Fourier transformation (DFT) is developed for detecting the harmonic voltage, which is simpler and faster than FFT when the single harmonic voltage is calculated. If the harmonic voltage is greater than the settings, the correct additional minimal extinction angle is held until the excitation current decreases to an acceptable level. Therefore, it can not only prevent continuous commutation failure due to transformer excitation surge current but can also mitigate the sharp decrease of DC power. This method has been proved to be effective through RTDS experiments in the LinFeng and TianZhong HVDC systems. Moreover, it is already implemented in the TianZhong \pm 800 kV UHVDC system.

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Abbreviation

The following abbreviations are used in this manuscript:

 γ Extinction Angle α Firing Angle

 X_r Commutating Reactance

 $I_{\rm d}$ Dc Current

 E_{2m} Commutation Voltage β Trigger Advance Angle

 I_{VY} YY Transformer Current in Valve Side I_{VD} YD Transformer Current in Valve Side CFPREV Commutation Failure Prediction

 $\alpha_{\text{-CFPREV}}$ The Output of CFPREV u_0 Zero Sequence Component

 u_{α} $u_{\alpha\beta}$ in α axis u_{β} $u_{\alpha\beta}$ in β axis

 $u_{\alpha\beta}$ The Magnitude of Rotating Vector

 u_a , u_b , u_c Three-phase Instantaneous Voltage of Converter Bus

Uac_COMP Magnitude of Uac Before Fault N_{cur} The Latest Sampled Data Points

 $x(i\tau)$ Sampling Data Before i Sampling Period

TianZhong TianShan-ZhongZhou

UHVDC Ultra High Voltage Direct Current
DFT Discrete Fourier Transform
FFT Fast Fourier Transformation

CFPREV_Harm The output of CFPREV based on harmonic detection Uac_Harm_Blk Action signal of CFPREV based on harmonic detection

RTDS Real-time Digital Simulator

HP High-pass Filter
BP Band-pass Filter
SC Shunt Capacitor

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