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A Dual-Function Instantaneous Power Theory for Operation of Three-Level Neutral-Point-Clamped Inverter-Based Shunt Active Power Filter

Yap Hoon ^{1,2,*} , Mohd Amran Mohd Radzi ^{1,2} , Mohd Khair Hassan ¹  and Nashiren Farzilah Mailah ^{1,2}

¹ Department of Electrical and Electronic Engineering, Faculty of Engineering, Universiti Putra Malaysia, Serdang 43400, Malaysia; amranmr@upm.edu.my (M.A.M.R.); khair@upm.edu.my (M.K.H.); nashiren@upm.edu.my (N.F.M.)

² Centre for Advanced Power and Energy Research (CAPER), Faculty of Engineering, Universiti Putra Malaysia, Serdang 43400, Malaysia

* Correspondence: davidhoon0304@hotmail.com; Tel.: +60-14-9258-109

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Abstract: This paper proposes a simple yet effective reference current generation algorithm based on instantaneous power pq theory to enhance mitigation performance of a three-phase three-level neutral-point-clamped (NPC) inverter-based shunt active power filter (SAPF). The proposed algorithm is developed for dual functionality: generate reference current and synchronization phase to effectively govern operation of SAPF in mitigating harmonic current and compensating reactive power. Three key modifications are implemented: (1) replacement of numerical low-pass filter (LPF) with an average power detector to improve mitigation performance; (2) removal of needless reactive element to reduce algorithm complexity; and (3) integration of phase tracking feature to eliminate the needs of phase-locked loop (PLL). Simulation work of SAPF with the proposed algorithm was conducted and assessed in MATLAB–Simulink. In addition, to verify feasibility of the proposed algorithm, a laboratory prototype as constructed with TMS320F28335 digital signal processor (DSP) programmed as the controller. Performance of SAPF achieved by utilizing the proposed algorithm was thoroughly investigated and benchmarked with that demonstrated using the existing pq theory algorithm to evaluate the inherent advantages. Simulation and experimental results are obtained for different nonlinear loads and test conditions. Responses demonstrated by SAPF in both simulation and experimental works reveal superiority of the proposed algorithm over the existing algorithm.

Keywords: active filter; current harmonics; multilevel inverter; pq theory; reference current generation; synchronization phase; total harmonic distortion (THD)

1. Introduction

High current harmonics resulted from broad utilizations of nonlinear loads is one of the noteworthy power quality issues in electrical power system which has attracted huge research interests. Other than deteriorating the overall power system efficiency through reduction of power factor (PF), the presence of harmonic currents may also cause capacitor blowing, malfunction of sensitive devices, and equipment overheating [1,2]. As a result, it is obligatory to limit harmonic contents in power system and maintain it at an acceptable level. In fact, various innovative techniques have been proposed in the literature [3] to reduce the impacts of harmonic currents, where shunt active power filter (SAPF) is found to be most effective. Nevertheless, to improve mitigation capability of SAPF, major research works have been conducted pertaining to control algorithm design [4].

Generally, operation of SAPF is managed by a set of control algorithms which work in sync together, starting from extraction of harmonic current from the connected power system and ending with generation of switching signals that bring the SAPF into operation. In mitigation process, effectiveness of SAPF is strongly dependent on how accurately and quickly its reference current generation algorithm performs. Note that reference current generation algorithm is also known as harmonic extraction algorithm in some studies [5,6]. Its main purpose is to derive a reference current signal that directs operation of SAPF from the extracted harmonic current. By working as indicated by an exact reference current signal, the SAPF ought to have the ability to mitigate harmonics optimally. Various techniques with unique merits are available for this particular algorithm which includes instantaneous power pq theory [7–9], synchronous reference frame (SRF) [10–12], fast Fourier transform (FFT) [13], dq theory with Fourier (dqF) [14], artificial neural network (ANN) [15,16], and many others.

Among the available techniques, reference current generation algorithm based on pq theory technique is particularly best suited to be implemented as it offers the least complex control structure, thus substantially reduces system necessities for practical implementation. In the context of pq theory algorithm, the answer to its effectiveness lies within its capability in detecting fundamental active power element. For this matter, numerical low-pass filter (LPF) [9,17] is commonly applied. However, a typical LPF operates based on fixed value of cutting frequency and thus may not perform well under dynamic-state conditions, where the active power varies dynamically. Besides, the performance of a typical LPF is also influenced by the selected order of the filter. A higher order filter normally exhibits better filtering performance as compared to the lower order filter, but with the price of longer time delay [11,18]. Hence, it must carefully be selected and matches the value of the cutting frequency to assure optimum filtering performance. For that matter, there is a need to test various combinations of the filter's order and cutting frequency in an iterative manner to find out the best combination. As a result, large amount of time will be wasted just for the tuning purposes, and thus not worthwhile to be implemented.

Another notable weakness of the existing pq theory algorithm is that it is as yet perceived to possess redundant elements which are insignificant in the procedure of generating reference current, thus can be removed to reduce complexity of the designed controller. Moreover, in the process of generating reference current, the existing pq theory algorithm still needs to work together with a phase-locked loop (PLL) [7–9]. The need for an additional PLL greatly complicates structure of pq theory algorithm. Besides, a proportional-integral (PI) controller is still being applied in the conventional PLL technique [9,19,20] and thus substantial amount of time will be spent just for proper tuning of its gain parameters as the tuning procedure is normally empirical-based and tedious [21]. Hence, its complexity can further be reduced if the algorithm itself can be designed to generate its own synchronization phase without needing any PLL or other additional synchronization elements.

Based on the described limitations of the existing pq theory algorithm, this paper proposes a dual-function pq theory algorithm that can accurately generate reference current and at the same time provides synchronization phase to ensure effective operation of SAPF without using any PLL element. Three notable enhancements are implemented: replacement of LPF with average power detector, simplification of algorithm complexity, and integration of phase tracking feature. Simulation assessments in MATLAB-Simulink and laboratory tests were performed to confirm validity and feasibility of the proposed algorithm. The remainder of the paper is arranged as follows. Section 2 presents circuit configuration of SAPF and its control strategies. In Section 3, particulars of the proposed dual-function pq theory algorithm are described and compared to the existing pq theory algorithm, highlighting the enhancements made. Sections 4 and 5 present and critically discuss the simulation and experimental findings, respectively. The work is concluded and contributions are highlighted in Section 6.

2. Shunt Active Power Filter: Circuit Connection and Control Structure

Figure 1 shows circuit connection and control structure of the SAPF. A three-level neutral-point-clamped (NPC) inverter (serving as the SAPF) is connected to the operating power system between power supply and rectifier load at point of common coupling (PCC). The rectifier is further commonly connected to two types of loads: either capacitive (RC) or inductive (RL) loads. As illustrated in Figure 1, a typical SAPF operates by injecting mitigation current (hereinafter referred to as injection current) in opposite phase into the polluted power system via PCC to mitigate the harmonic current and concurrently drawing a certain amount of current (hereinafter referred to as instantaneous dc-link charging current) to regulate its switching losses. In this manner, the harmonic current present in the power system will slowly be removed and eventually the polluted source current will regain its sinusoidal appearance with fundamental frequency.

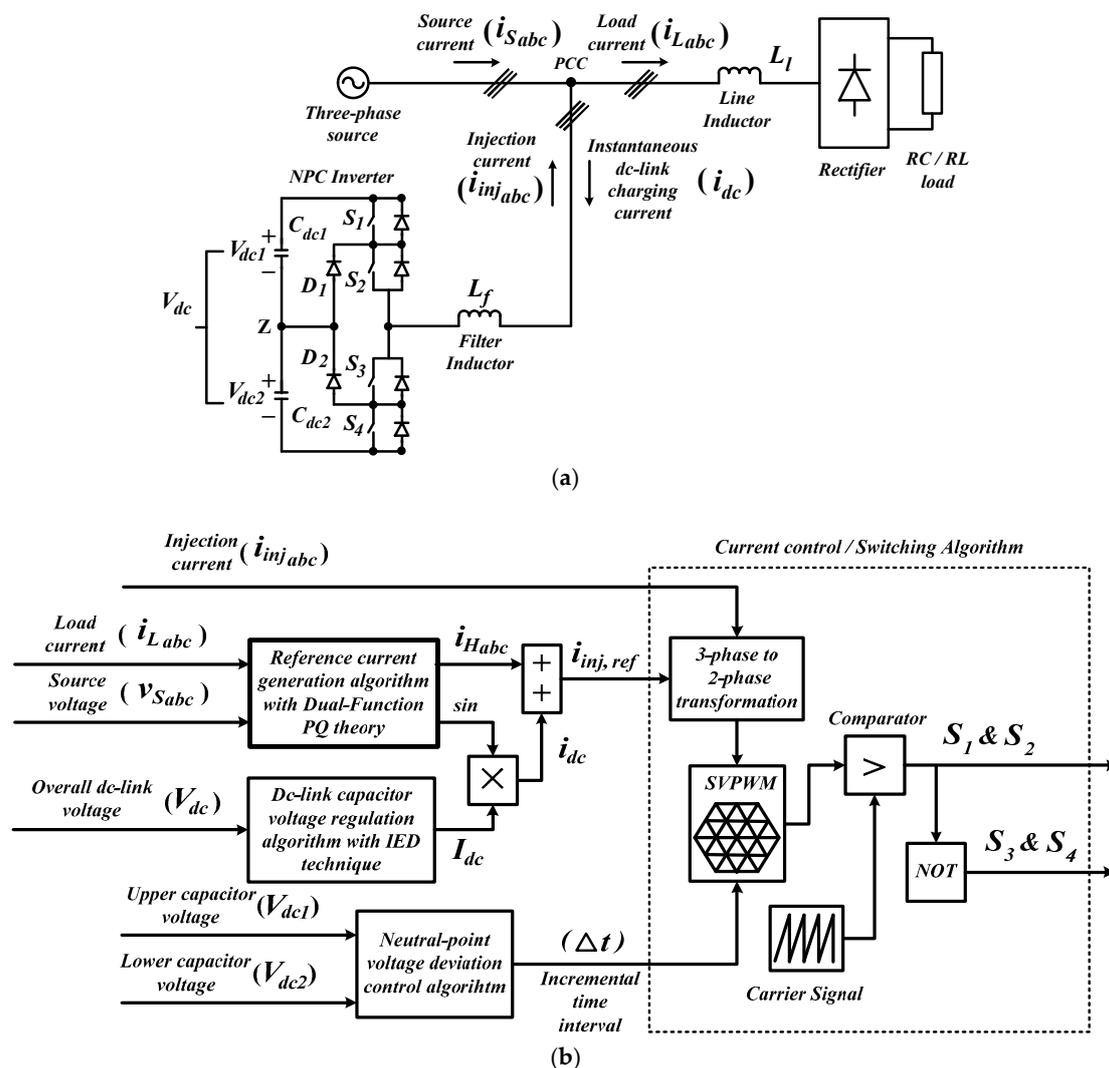


Figure 1. Three-level NPC inverter working as SAPF: (a) circuit connection; and (b) control algorithms.

Meanwhile, its control structure is made up of four control algorithms that work in sync together, i.e., reference current generation, dc-link capacitor voltage regulation, voltage balancing and current control or switching algorithms. First, reference current generation algorithm, as stated in Section 1, is responsible for generating a reference current signal that directs operation of SAPF. Further description on this particular algorithm is provided in Section 3. Secondly, dc-link capacitor voltage

regulation algorithm is responsible in regulating and maintaining overall dc-link voltage V_{dc} at suitable level. In this case, inverted error deviation (IED) technique is used [22]. It performs by computing magnitude I_{dc} of the instantaneous dc-link charging current i_{dc} and delivers it to the reference current generation algorithm for further control procedures, so that similar amount of current can be drawn by the SAPF to retain its dc-link voltage level.

Thirdly, voltage balancing algorithm is responsible for keeping a voltage balance across all individual capacitors. In the case when NPC inverter is employed, the algorithm is more commonly referred to as neutral-point voltage deviation control algorithm. Moreover, in the case of three-level NPC inverter (as employed in this work), which consists of two individual capacitors (V_{dc1} and V_{dc2}) at the dc-side, the voltage across each individual capacitor must be equal and maintained at half the value of V_{dc} . For this purpose, dwell time allocation (DTA) technique is applied [23]. It performs by delivering the required incremental time interval ΔT to adjust switching period of each power switch according to degree of voltage difference between V_{dc1} and V_{dc2} . Note that, voltage balancing is required only when dc-side of the adopted inverter possesses more than one capacitor especially when dealing with multilevel inverter topologies. Fourthly, current control or switching control algorithm is responsible in generating switching pulses S_{1-4} to direct operation of the SAPF. In this case, space vector pulse-width modulation (SVPWM) control algorithm that performs at switching frequency of 25 kHz is applied [24].

3. Dual-Function Instantaneous Power pq Theory Algorithm

Control structure and operating principle of the pq theory algorithm applied in this work is provided in this section. To ease the explanation process and clearly present the implemented enhancements, details of the existing pq theory algorithm is first presented. Subsequently, working concept of the proposed dual-function pq theory algorithm is described emphasizing on the differences and implemented modifications.

3.1. The Existing pq Theory Algorithm

Figure 2 shows control structure of the existing pq theory algorithm. Basically, the reference current generation processes involves two consecutive computation processes: (1) a sequence of instantaneous power calculations in a balanced three-phase system to extract harmonic current i_{Habc} from the polluted power system; and (2) derivation of reference current $i_{inj,abc}$ by using the extracted harmonic current and instantaneous dc-link charging current i_{dc} (delivered by dc-link capacitor voltage regulation algorithm). The calculations to extract harmonic current are conducted in $\alpha\beta$ domain where all the required three-phase abc signals must first be transformed into their corresponding two-phase $\alpha\beta$ representation by using a transformation matrix M_{pq} [25,26] which is expressed as:

$$\mathbf{M}_{pq} = \sqrt{\frac{2}{3}} \begin{bmatrix} \cos\theta_1(t) & \cos\theta_3(t) & \cos\theta_2(t) \\ -\sin\theta_1(t) & -\sin\theta_3(t) & -\sin\theta_2(t) \end{bmatrix} \quad (1)$$

where

$$\theta_{ph}(t) = \theta(t) + \frac{2\pi}{3}(ph - 1), \quad ph = 1, 2, 3 \quad (2)$$

and $\theta(t)$ is an angular arbitrary function and is considered as $\theta(t) = 0$.

Let the three-phase source voltage and load current expressed in matrix form be $v_{Sabc} = [v_{Sa} \ v_{Sb} \ v_{Sc}]^T$ and $i_{Labc} = [i_{La} \ i_{Lb} \ i_{Lc}]^T$, respectively. Hence, their corresponding $\alpha\beta$ representations can be expressed as

$$\begin{bmatrix} v_{S\alpha} & v_{S\beta} \end{bmatrix}^T = \mathbf{M}_{pq} v_{Sabc} \quad (3)$$

$$\begin{bmatrix} i_{L\alpha} & i_{L\beta} \end{bmatrix}^T = \mathbf{M}_{pq} i_{Labc} \quad (4)$$

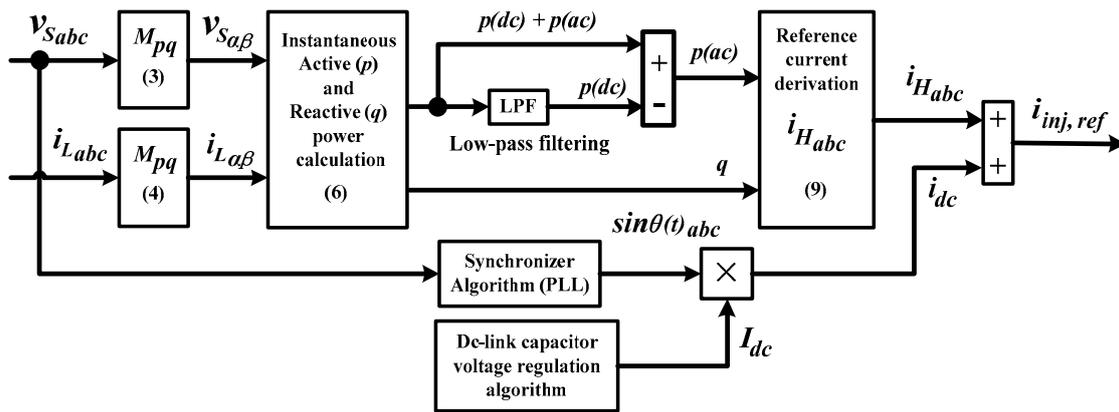


Figure 2. Control structure of reference current generation algorithm based on the existing pq theory.

Meanwhile, in $\alpha\beta$ domain, complex power s can be expressed as

$$s = p + jq = v_{S\alpha\beta} i_{L\alpha\beta}^* = (v_{S\alpha} - jv_{S\beta})(i_{L\alpha} + ji_{L\beta}) \tag{5}$$

$$= (v_{S\alpha}i_{L\alpha} + v_{S\beta}i_{L\beta}) + j(v_{S\alpha}i_{L\beta} - v_{S\beta}i_{L\alpha})$$

where p and q symbolize the instantaneous active and reactive power, respectively, and $*$ represents a complex conjugate.

For simplicity, from Equation (5), the computed active and reactive power can be rewritten in a more concise form as

$$\begin{bmatrix} p \\ q \end{bmatrix} = \begin{bmatrix} v_{S\alpha} & v_{S\beta} \\ -v_{S\beta} & v_{S\alpha} \end{bmatrix} \begin{bmatrix} i_{L\alpha} \\ i_{L\beta} \end{bmatrix}. \tag{6}$$

Note that, due to contamination of harmonic current, the computed p and q can actually be separated into fundamental (dc) and harmonic (ac) elements and thus a new relationship of instantaneous power can be formed as

$$\begin{bmatrix} p \\ q \end{bmatrix} = \begin{bmatrix} p_{(dc)} \\ q_{(dc)} \end{bmatrix} + \begin{bmatrix} p_{(ac)} \\ q_{(ac)} \end{bmatrix} \tag{7}$$

where $p_{(dc)}$ and $p_{(ac)}$ symbolize the fundamental (dc) and harmonic (ac) elements of instantaneous active power, respectively. Similar relation applies to reactive power $q_{(dc)}$ and $q_{(ac)}$.

According to the existing pq theory algorithm, $p_{(ac)}$ and q are the two main parameters needed to extract harmonic current i_{Habc} . Note that the harmonic element of active power $p_{(ac)}$ contains information on the magnitude of the harmonic current. Meanwhile, the reactive power q is mainly responsible in representing the resulted phase difference (due to presence of harmonics) between source voltage and current. The $p_{(ac)}$ element is commonly obtained via indirect identification approach [4], i.e., by using a second order Butterworth-type LPF with cutting frequency of 10 Hz [7,18] which can be represented as

$$p_{(ac)} = p - LPF(p_{(dc)} + p_{(ac)}). \tag{8}$$

Note that, according to this approach, dc element of the active power $p_{(dc)}$ is first detected by the LPF and is then subtracted from the actual active power p .

Thereafter, an inverse transformation approach is applied to $p_{(ac)}$ or $p - p_{(dc)}$ and q , to derive the desired harmonic current i_{Habc} . A compact style of the inverse transformation approach can be expressed as

$$i_{Habc} = \frac{M_{pq}^T}{\det(pq)} \begin{bmatrix} v_{S\alpha} & -v_{S\beta} \\ v_{S\beta} & v_{S\alpha} \end{bmatrix} \begin{bmatrix} p - p_{(dc)} \\ q \end{bmatrix} \tag{9}$$

where $det(pq) = v_{S\alpha}^2 + v_{S\beta}^2$.

With availability of i_{Habc} , the reference current $i_{inj,ref}$ can subsequently be acquired according to the following approach:

$$i_{inj,ref} = i_{Habc} + I_{dc} \sin\theta(t)_{abc} \tag{10}$$

where I_{dc} is the magnitude of instantaneous dc-link charging current i_{dc} (a controlled variable responsible for regulating the overall dc-link voltage V_{dc} of SAPF) obtained by using dc-link capacitor voltage regulation algorithm and $\sin\theta(t)_{abc}$ is the synchronization phase delivered by synchronizer algorithm particularly a PLL to ensure proper synchronization of I_{dc} with i_{Habc} so that $i_{inj,ref}$ can precisely be generated. It is important to note that, for the sole purpose of extracting harmonic current i_{Habc} , the existing pq theory algorithm itself can actually perform without needing any external PLL. However, to effectively generate the desired reference current, there is a need to properly synchronize operation of the reference current generation with the dc-link capacitor voltage regulation algorithms and thus a PLL ought to be applied.

3.2. The Three Proposed Enhancements

By referring to the existing pq theory algorithm shown in Figure 2, three notable enhancements have been implemented to form the proposed dual-function pq theory algorithm, as illustrated in Figure 3. The first enhancement is implemented aimed to overcome two inherent weaknesses of tuned numerical LPF, i.e., the tedious workloads in finding the best match between cutting frequency and filter's order, and significant delay in detecting fundamental $p_{(dc)}$ element. For that purpose, an average power detection method is incorporated and it is expressed as

$$p_{(dc)} = p_{(average)} = \frac{1}{T} \int_0^T (p_{(dc)} + p_{(ac)}) dt \tag{11}$$

where T symbolizes period of the active power p signal and $p_{(average)}$ is its average value. This approach is possible because in $\alpha\beta$ rotating frame, dc element of any signal is actually equivalent to its average value. Therefore, by using the incorporated average power detection approach, there is no need for any traditional filtering. In this manner, the possibility of error in detecting dc element due to improper tuning of LPF can be avoided.

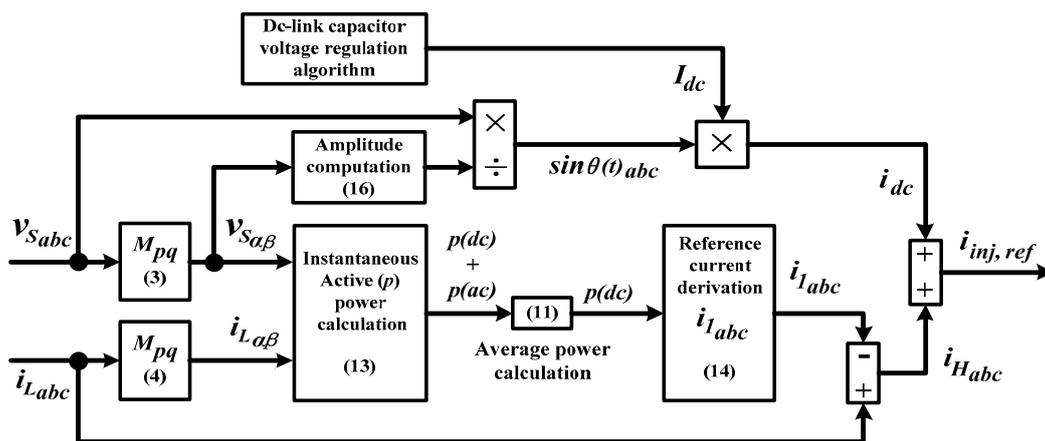


Figure 3. Control structure of reference current generation algorithm based on the proposed dual-function pq theory.

Next, the second improvement is implemented to achieve reduction in algorithm complexity. It is achieved by removing the reactive power q which is perceived to be insignificant in the process of generating reference current. For that purpose, operation of the existing pq theory algorithm needs to

be changed. Instead of generating i_{Habc} directly using Equation (9), the proposed algorithm is designed to produce it indirectly, i.e., by first computing the fundamental active current i_{1abc} and then directly subtract it from the load current i_{Labc} . The indirect approach in generating i_{Habc} can be expressed as

$$i_{Habc} = i_{Labc} - i_{1abc}. \quad (12)$$

According to the indirect approach, the computed $p_{(dc)}$ is first utilized to generate i_{1abc} without going through the calculation process of Equation (8). The fundamental active current i_{1abc} is expected to be a purely sinusoidal signal (contain only sine element) with fundamental frequency and phase similar to that of source voltage. With this expectation, reactive power q which represents the phase difference between source voltage and current (as described previously in Section 3.1), is no longer needed in the computation process. This simply means that using only active power p is good enough to represent the sinusoidal characteristic and in phase operation (with source voltage) of the resulted fundamental active current i_{1abc} . After removing the insignificant reactive power q , instantaneous power calculation approach expressed previously as Equation (6) can therefore be simplified as Equation (13).

$$p = \begin{bmatrix} v_{S\alpha} & v_{S\beta} \end{bmatrix} \begin{bmatrix} i_{L\alpha} \\ i_{L\beta} \end{bmatrix} \quad (13)$$

$$i_{1abc} = \frac{\mathbf{M}_{pq}^T p_{(dc)}}{\det(pq)} \begin{bmatrix} v_{S\alpha} \\ v_{S\beta} \end{bmatrix} \quad (14)$$

On the other hand, the concise inverse transformation approach expressed previously as Equation (9) can now be simplified as Equation (14) to transform the detected $p_{(dc)}$ back into actual three-phase fundamental active current i_{1abc} for derivation of reference current. Subsequently, the reference current $i_{inj,ref}$ is obtained using Equation (10).

Thirdly, the enhancement is made to eliminate reliance on PLL element which requires series of complex calculation processes in tracking the required phase information $\sin\theta(t)_{abc}$ from source voltage v_{Sabc} . For that purpose, a phase tracking technique is integrated allowing the proposed algorithm to produce its own synchronization phase. The integrated phase tracking technique makes good use of natural characteristic of the pq theory algorithm to transform the three-phase source voltage v_{Sabc} into $\sin\theta(t)_{abc}$ for synchronization purposes. It is accomplished according to two consecutive processes listed below:

- (1) Compute magnitude V_{mag} of v_{Sabc} based on the values of $v_{S\alpha}$ and $v_{S\beta}$ which is obtained using Equation (3).
- (2) Divide v_{Sabc} directly with the generated magnitude V_{mag} .

Since generation of $v_{S\alpha}$ and $v_{S\beta}$ is originally part of the operational stages in pq theory algorithm, hence the phase tracking technique can be integrated into the pq theory algorithm with minimal efforts. Operation of the proposed technique can effectively be simplified into a concise form as follow:

$$\sin\theta(t)_{abc} = \frac{v_{Sabc}}{V_{mag}} \quad (15)$$

where

$$V_{mag} = \sqrt{\frac{2}{3} \det(pq)}. \quad (16)$$

By referring to Equation (15), it is important to note that the generated sine function $\sin\theta(t)_{abc}$ is actually unity (magnitude) representation of v_{Sabc} . Since $\sin\theta(t)_{abc}$ is directly generated by processing the three-phase source voltage, it can be stated that $\sin\theta(t)_{abc}$ is in accordance with the phase of source voltage. The generated $\sin\theta(t)_{abc}$ is then used to synchronize I_{dc} with i_{Habc} (obtained from Equation (12)) according to Equation (10), thus producing the desired reference current $i_{inj,ref}$. With

incorporation of Equation (15), the proposed pq theory algorithm can now be used for generating reference current and synchronization purposes, and therefore dual functionality.

4. Simulation Results

Simulation work which included development of SAPF circuits, its control algorithms, and assessment of the proposed dual-function pq theory algorithm was conducted in MATLAB-Simulink platform (R2012a). Simple Simulink-blocks were used to design all the SAPF circuits and control algorithms, and the simulation was executed in discrete environment. Steady-state and dynamic-state simulation studies were conducted, considering balanced-sinusoidal source voltage (400 Vrms, 50 Hz). Meanwhile, the dc-link capacitors applied were 3300 μF (each), with reference overall dc-link voltage set to be 880 V (440 V for each individual capacitor). Next, a simple L -typed filter (5 mH inductor) was applied as the output filter of SAPF. The function of an output filter is to minimize switching ripples generated by SAPF during its operation. In this work, a L -typed filter was considered rather than other types such as LC -typed and LCL -typed filters, as it is the least complex among the filters and thus reduces complexity of the overall SAPF structure. Furthermore, for the sources of harmonics, two types of nonlinear loads were considered. The first nonlinear load comprised an uncontrolled bridge rectifier that is further connected (in parallel) to a 20 Ω resistor and 2200 μF capacitor (hereinafter referred to as capacitive load). The second nonlinear load comprised a similar uncontrolled bridge rectifier that is further connected (in series) to a 50 Ω resistor and 50 mH inductor (hereinafter referred to as inductive load). Analysis was performed in comparative manner, i.e., by benchmarking performance of SAPF while applying the proposed dual-function pq theory algorithm, with the one using the existing pq theory algorithm.

For steady-state analysis, performance of the proposed algorithm was analyzed based on degree of ripples retained in the detected fundamental (dc) element of active power and ability of SAPF to maintain THD value of source current (after mitigation) below 5% limit as stated in IEEE Standard 519-2014 [27]. The degree of retained ripples was measured in terms of percentage by using a key parameter known as percentage of ripple power $\%p_{(ripple)}$ which is defined as

$$\%p_{(ripple)} = \frac{P_{(dc),pp}}{P_{(dc),desired}} \times 100\% \quad (17)$$

where $P_{(dc),pp}$ and $P_{(dc),desired}$ symbolize peak-to-peak and the desired fundamental (dc) element of active power, respectively.

On the other hand, for dynamic-state analysis, its performance is judged based on its dynamic responses in detecting the desired dc element of active power, i.e., by observing the demonstrated overshoot or undershoot, and response time. Subsequently, the time taken by SAPF to effectively mitigate harmonic current was also recorded for performance judgment. For this analysis, the connected nonlinear load was switched from capacitive to inductive to form a dynamic-state condition.

4.1. Steady-State Condition Analysis

Figure 4 provides the simulation result that shows the performance of each reference current generation algorithm in detecting the desired fundamental element of active power $p_{(dc)}$ for capacitive and inductive loads. As can be observed in Figure 4, both proposed and existing pq theory algorithms are able to detect the desired $p_{(dc)}$ effectively, but there is a huge difference in terms of $\%p_{(ripple)}$ between the detected $p_{(dc)}$. For both nonlinear loads, $p_{(dc)}$ detected by the proposed algorithm is free from any ripples. In contrast, significant ripple power of 16 W ($\%p_{(ripple)} = 0.11\%$) and 1 W ($\%p_{(ripple)} = 0.02\%$) are revealed to retain in the $p_{(dc)}$ detected by the existing algorithm for capacitive and inductive loads, respectively. Hence, in terms of the ability to detect the desired $p_{(dc)}$, the average

power detector which has been integrated in the proposed algorithm is able to perform with higher accuracy as compared to the numerical LPF applied in the existing algorithm.

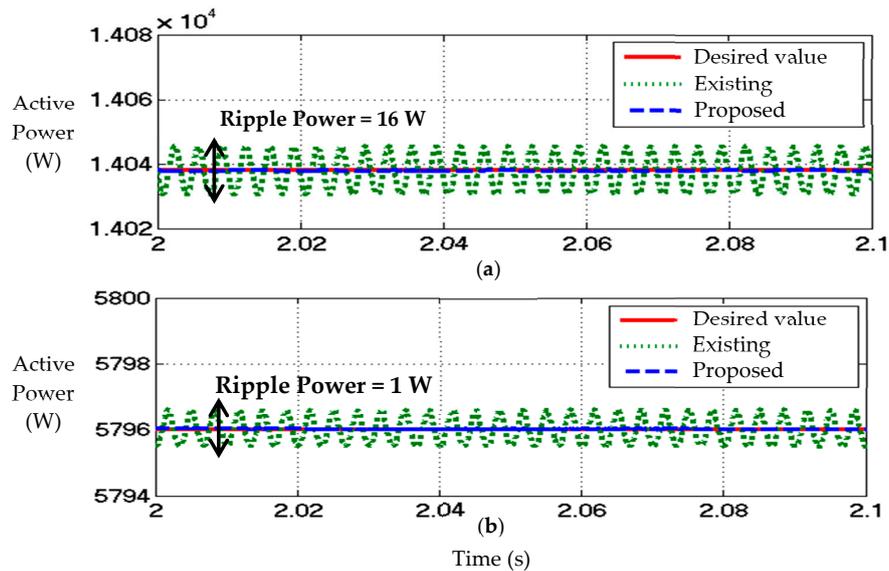


Figure 4. Fundamental element of active power $p_{(dc)}$ detected by each reference current generation algorithm for: (a) capacitive load; and (b) inductive load.

On the other hand, Figure 5 provides simulation result that shows the steady-state simulation waveforms of three-phase source voltage v_{Sabc} , load current i_{Labc} , injection current i_{injabc} , and source current i_{Sabc} , demonstrated by SAPF while applying the proposed algorithm, for capacitive and inductive loads. Meanwhile, all the resulted THD values of source current after mitigation by SAPF while applying its respective reference current generation algorithm are tabulated in Table 1. Both algorithms effectively direct their respective SAPF in maintaining THD values of source current below the allowable limit. However, SAPF which is controlled by the proposed algorithm shows superior effectiveness in mitigating harmonic current, i.e., by achieving lower THD values for both capacitive and inductive loads, as compared to the SAPF which is controlled by the existing algorithm. The THD values recorded while applying the proposed algorithm is 0.06–0.09% (capacitive) and 0.11–0.12% (inductive) lower than that obtained while applying the existing algorithm. Furthermore, it also can clearly be observed from Figure 5 that the mitigated source current (all three-phases) is working in phase with the source voltage for both nonlinear loads which leads to maximum power factor.

Table 1. Simulation result showing THD values of source current demonstrated by SAPF while applying its respective reference current generation algorithm, for capacitive and inductive loads.

Reference Current Generation Algorithm	Total Harmonic Distortion, THD (%)					
	Phase a		Phase b		Phase c	
	Capacitive	Inductive	Capacitive	Inductive	Capacitive	Inductive
Without Installation of SAPF						
N/A	43.03	27.34	43.03	27.34	43.03	27.34
With Installation of SAPF						
dual-function pq theory	1.08	1.72	1.09	1.70	1.09	1.72
Existing pq theory	1.17	1.83	1.15	1.82	1.16	1.84

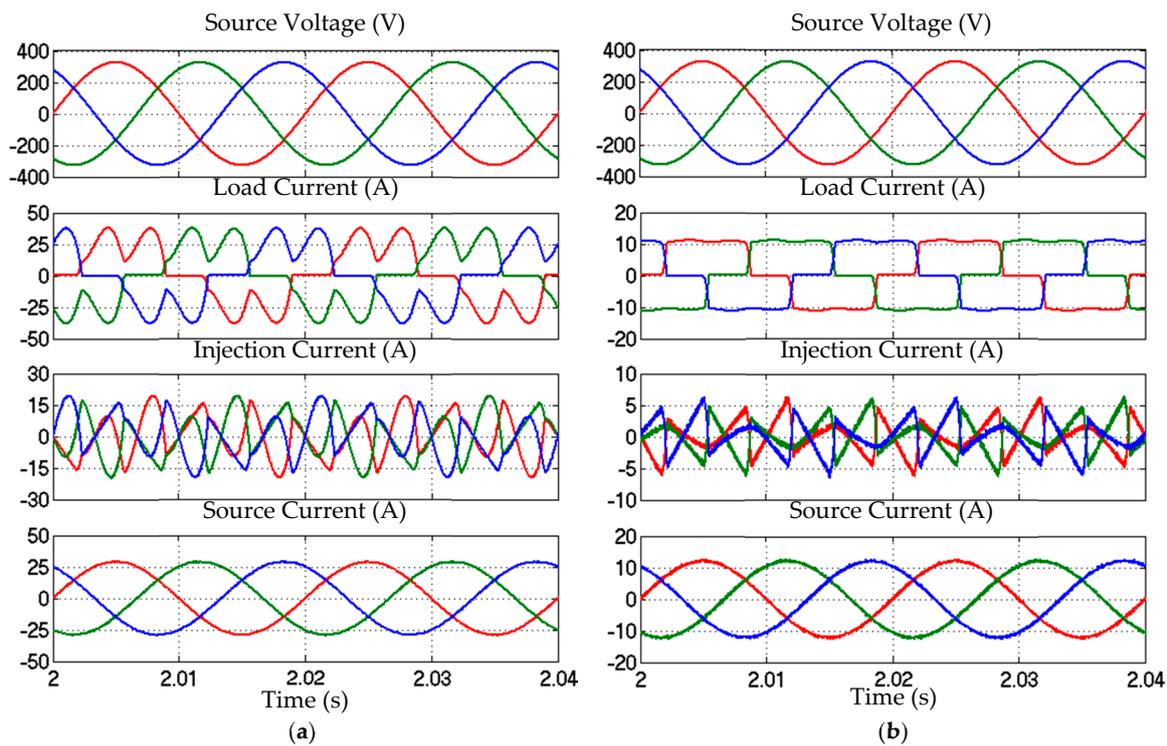


Figure 5. Simulation result showing the three-phase source voltage v_{Sabc} , load current i_{Labc} , injection current i_{injabc} , and source current i_{Sabc} , demonstrated by SAPF utilizing the proposed dual-function pq theory algorithm for: (a) capacitive load; and (b) inductive load.

Hence, from the conducted steady-state analysis, the existing pq theory algorithm is unable to provide a “ripple free” $p_{(dc)}$ detection. The ripples retained in the detected $p_{(dc)}$ have degraded quality of the generated reference current. Therefore, performance of SAPF in mitigating harmonic current is observed to have been degraded (indicated by higher THD value). However, by applying the proposed dual-function pq theory algorithm which can provide ripples-free detection of $p_{(dc)}$, an accurate reference current can be generated to effectively control operation of SAPF, and thus improving its mitigation performance (indicated by lower THD value). This is the main factor contributing to superior mitigation performance (in terms of the resulted THD value) of SAPF that applies the proposed algorithm over the SAPF that applies the existing algorithm.

4.2. Dynamic-State Condition Analysis

Simulation result showing dynamic responses of each reference current generation algorithm in detecting the desired $p_{(dc)}$, is provided in Figure 6. From the findings, when the nonlinear load changes from capacitive to inductive (at time = 4 s), the proposed algorithm is observed to perform outstandingly with fast response time of 0.02 s and without any notable undershoots. However, the existing algorithm is found to perform poorly with large response time of 0.12 s and significant undershoots of 400 W. In other words, in terms of the ability to detect $p_{(dc)}$, the proposed algorithm outperforms the existing algorithm by achieving a response time of 6 times faster.

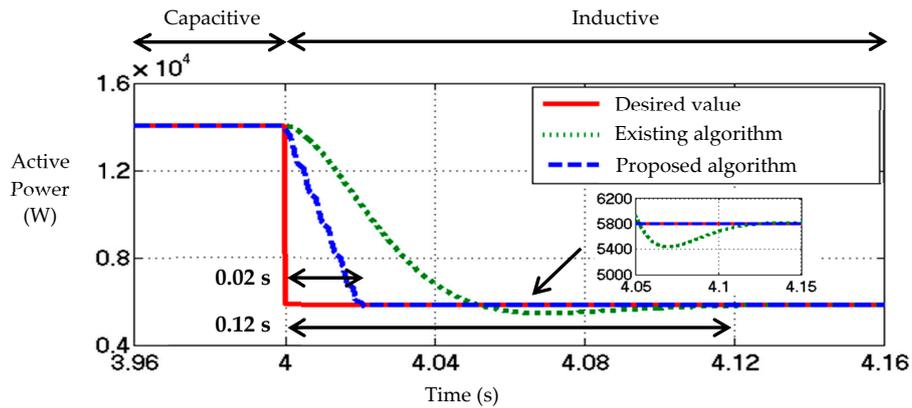


Figure 6. Fundamental element of active power $p_{(dc)}$ detected by each reference current generation algorithm, for dynamic-state condition of capacitive to inductive loads.

On the other hand, simulation result that shows dynamic responses of SAPF in mitigating harmonic current is provided in Figure 7. From the findings, when the nonlinear load changes from capacitive to inductive (at time = 4 s), SAPF which is controlled by the proposed algorithm is observed to perform outstandingly with fast response time of 0.02 s. Meanwhile, SAPF which is controlled by the existing algorithm is observed to perform poorly with slower response time of 0.04 s. In other words, in terms of the ability to mitigate harmonic current, by utilizing the proposed algorithm, SAPF can achieve a response time that is two times faster.

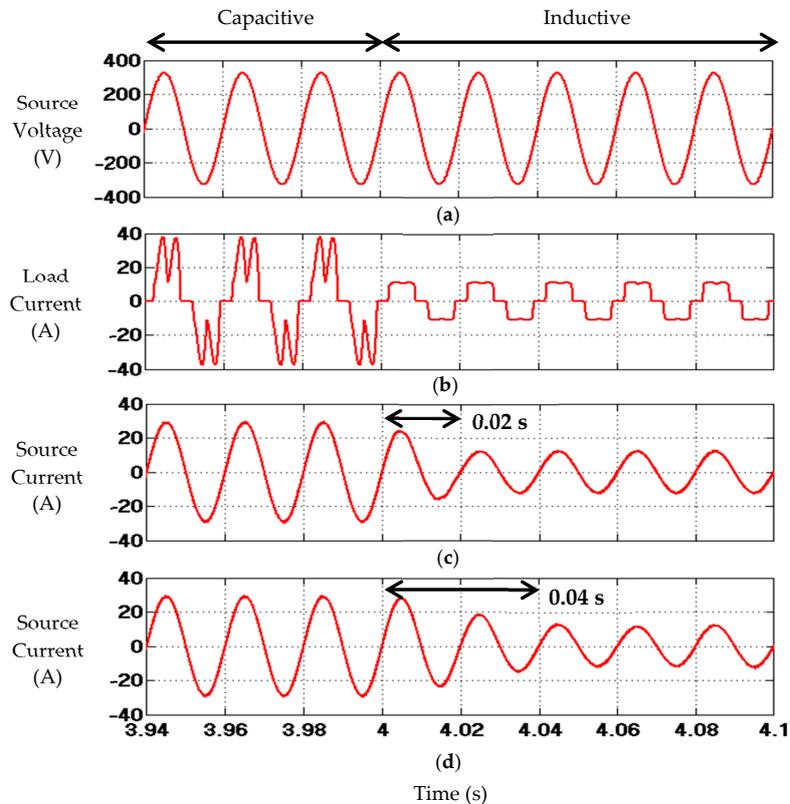


Figure 7. Simulation result showing: (a) phase a source voltage v_{Sa} ; (b) phase a load current i_{La} ; (c) phase a source current i_{Sa} demonstrated by SAPF while applying the proposed algorithm; and (d) phase a source current i_{Sa} demonstrated by SAPF while applying the existing algorithm, for dynamic-state condition of capacitive to inductive loads.

Hence, based on the findings obtained from dynamic-state analysis, the unique ability of the proposed algorithm in providing fast $p_{(dc)}$ detection has greatly enhanced dynamic response of SAPF in mitigating harmonic current. With fast $p_{(dc)}$ detection, the required reference current needed to control operation of SAPF can be generated at an earlier pace and thus achieving effective harmonic current mitigation with faster response time (as revealed in Figure 7). This is the main factor that contributes to superior mitigation performance (in term of the demonstrated response time) of SAPF that applies the proposed algorithm over the SAPF that applies the existing algorithm.

Other than mitigation performance, it is also important to assess behavior of dc-link voltages to ensure effective operation of SAPF. Figure 8 provides the related result. From the findings, SAPF utilizing each reference current generation algorithm is observed to function effectively where all its respective dc-link voltages are effectively regulated and continuously kept at the desired value. Concurrently, voltages across the two individual dc-link capacitors (V_{dc1} and V_{dc2}) are likewise found to be effectively regulated and are evenly balanced at half the value of overall dc-link voltage V_{dc} with minimal neutral-point voltage deviation. Besides, by comparing the behavior of dc-link voltages demonstrated by SAPF while using the proposed algorithm (as shown in Figure 8a) and SAPF while using the existing algorithm (as shown in Figure 8b), no significant differences can be observed. In other words, utilization of the proposed algorithm actually does not show significant impact towards the performance of dc-link voltages. This is another interesting feature of the proposed algorithm which makes it worth implementing.

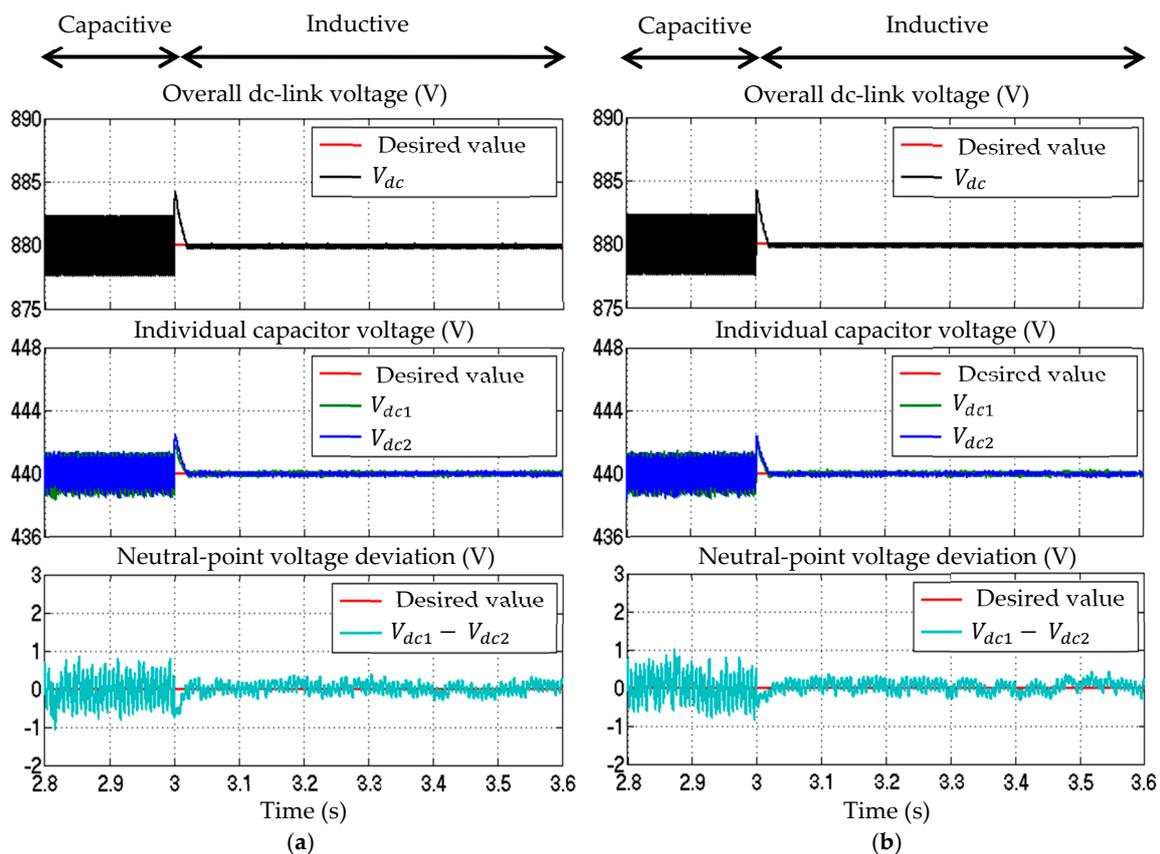


Figure 8. Simulation result showing the overall dc-link voltage V_{dc} , individual capacitor voltages V_{dc1} and V_{dc2} , and neutral-point voltage deviation ($V_{dc1} - V_{dc2}$), demonstrated by SAPF while applying: (a) the proposed algorithm; and (b) the existing algorithm, for dynamic-state condition of capacitive to inductive loads.

5. Experimental Verification

For experimental verification, power electronic circuits were assembled to form the SAPF and, meanwhile, all the control algorithms were downloaded to a TMS320F28335 digital signal processor (DSP) board via Code Composer Studio (CCS) v3.3 which served as the controller. The constructed experimental prototype is shown in Figure 9. In this case, a balanced-sinusoidal source voltage (100 Vrms, 50 Hz) was applied. It was supplied from a Chroma 6590 programmable *ac* source. Next, the reference overall dc-link voltage was scaled down to 220 V (110 V for each individual capacitor). Similar to the simulation work, performance of each reference current generation algorithm as also tested and assessed under both steady-state and dynamic-state conditions.

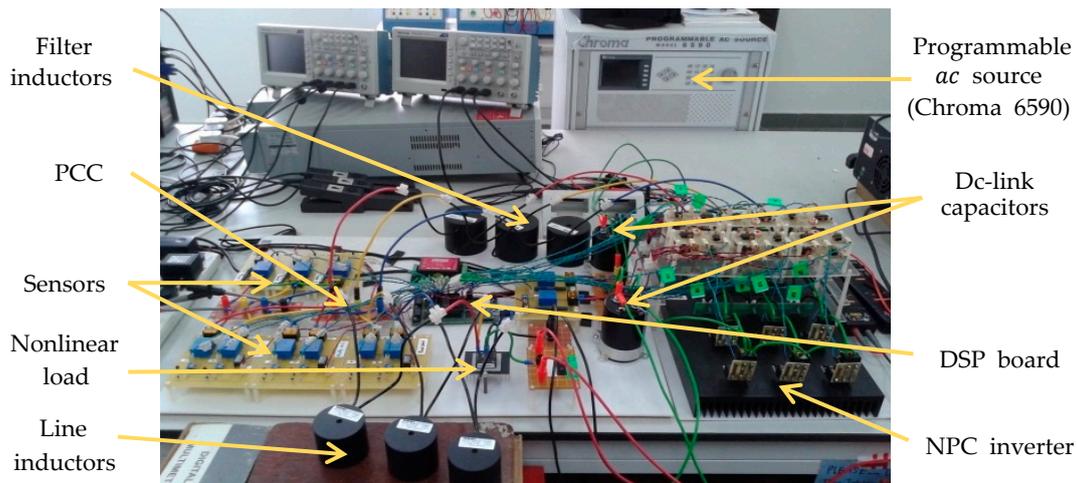


Figure 9. Laboratory prototype developed to validate design concept and performance of the proposed dual-function *pq* theory algorithm.

Figure 10 provides experimental result that shows steady-state waveforms of phase *a* source voltage v_{Sa} , load current i_{La} , injection current $i_{inj,a}$, and source current i_{Sa} , demonstrated by SAPF while applying the proposed algorithm, for capacitive and inductive loads. Meanwhile, all the resulted THD values of source current after mitigation by SAPF while applying its respective reference current generation algorithm are tabulated in Table 2. On the other hand, experimental result that shows dynamic responses of SAPF in mitigating harmonic current is provided in Figure 11.

Table 2. Experimental result showing THD values of source current demonstrated by SAPF while applying its respective reference current generation algorithm, for capacitive and inductive loads.

Reference Current Generation Algorithm	Total Harmonic Distortion, THD (%)					
	Phase <i>a</i>		Phase <i>b</i>		Phase <i>c</i>	
	Capacitive	Inductive	Capacitive	Inductive	Capacitive	Inductive
Without Installation of SAPF						
N/A	32.14	24.62	32.18	24.65	32.11	24.66
With Installation of SAPF						
dual-function <i>pq</i> theory	3.33	3.08	3.38	2.98	3.28	3.04
Existing <i>pq</i> theory	4.10	3.92	4.16	3.83	4.06	3.88

Based on the experimental findings, both algorithms perform effectively in directing their respective SAPF to remove the generated harmonic current. As a result, all the recorded THD values of source current are below the 5% limit, for both capacitive and inductive loads. However, THD values recorded for the case when the proposed algorithm is being applied (as tabulated in Table 2) is in a lower range, i.e., within 2.98–3.33% as compared to that recorded for the case when the existing algorithm is being applied where it performs within a higher range of 3.83–4.16%. This confirms the benefits of applying the proposed algorithm instead of using the existing algorithm under steady-state conditions. Moreover, as can be observed in Figure 10, phase a source current i_{s_a} seems to work in phase with phase a source voltage v_{s_a} for both nonlinear loads, hence almost unity power factor can be obtained. Most importantly, as clearly shown in Figure 11, under dynamic-state condition, SAPF which is controlled by the proposed algorithm can perform effectively in mitigating harmonic current with a faster response time of 0.025 s. However, SAPF which is controlled by the existing algorithm demonstrates a slower response time of 0.045 s. This confirms the benefits of applying the proposed algorithm instead of using the existing algorithm under dynamic-state conditions.

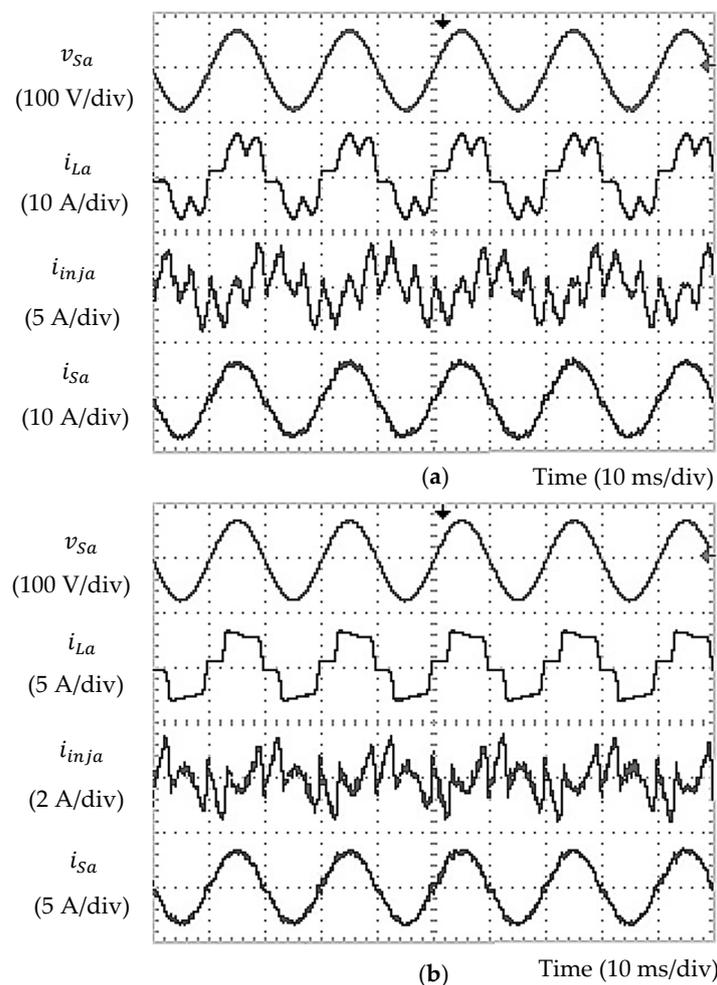


Figure 10. Experimental result showing phase a source voltage v_{s_a} , phase a load current i_{L_a} , phase a injection current i_{inj_a} and phase a source current i_{s_a} , demonstrated by SAPF while applying the proposed dual-function pq theory algorithm for: (a) capacitive load; and (b) inductive load.

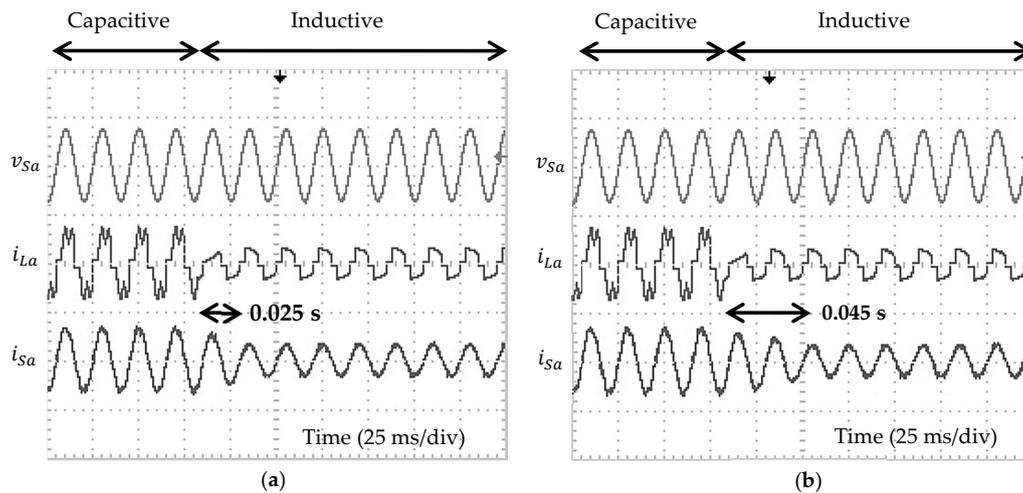


Figure 11. Experimental result showing phase a source voltage v_{Sa} (100 V/div), phase a load current i_{La} (10 A/div) and phase a source current i_{Sa} (10 A/div), demonstrated by SAPF while applying: (a) the proposed algorithm; and (b) the existing algorithm, for dynamic-state condition of capacitive to inductive loads.

Furthermore, Figure 12 provides experimental result that shows performance of SAPF while applying each reference current algorithm in terms of dc-link voltages regulation. From the findings, it can be confirmed that SAPF with each reference current generation algorithm is observed to function effectively where all its respective dc-link voltages are properly regulated and continuously kept at the desired value. At the same time, voltages across the two individual dc-link capacitors (V_{dc1} and V_{dc2}) are likewise found to be effectively regulated and are evenly balanced at half the value of overall dc-link voltage V_{dc} . Besides, by comparing Figure 12a,b, it can be observed that the behavior of dc-link voltages demonstrated by SAPF while utilizing the proposed algorithm (as shown in Figure 12a) and that demonstrated by SAPF while utilizing the existing algorithm (as shown in Figure 12b) is actually quite similar. In other words, it can also be confirmed that utilization of the proposed algorithm does not show significant impact towards performance of SAPF in term of dc-link voltages control.

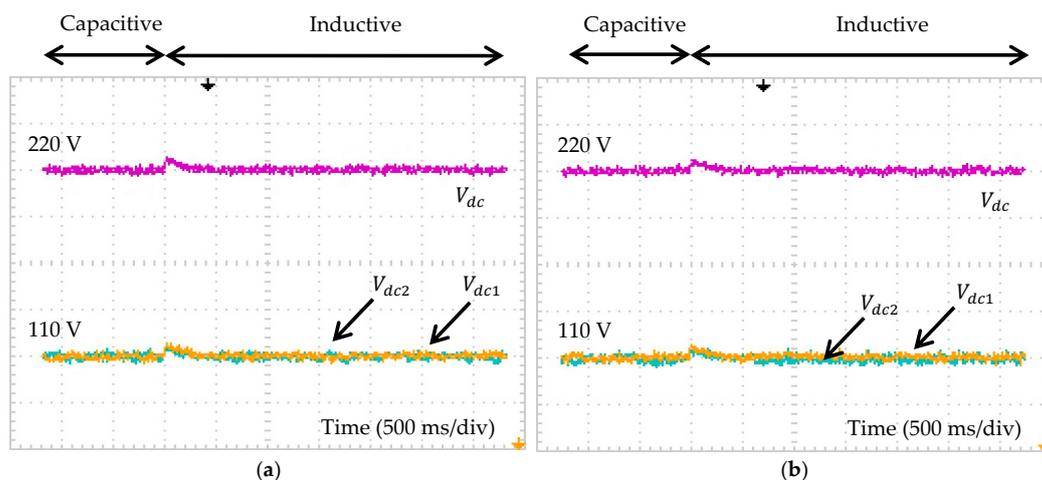


Figure 12. Experimental result showing the overall dc-link voltage V_{dc} (20 V/div) and individual capacitor voltages V_{dc1} (10 V/div) and V_{dc2} (10 V/div), demonstrated by SAPF while applying: (a) the proposed algorithm; and (b) the existing algorithm, for dynamic-state condition of capacitive to inductive loads.

Overall, as reported by the simulation and experimental results, design concept of the proposed dual-function pq theory algorithm itself and subsequently its operation with the other algorithms in the control system of SAPF can be confirmed to be valid and correct. By utilizing the proposed dual-function pq theory algorithm, mitigation performance of SAPF can be improved in terms of the resulted THD values and dynamic responses.

6. Conclusions

This paper has successfully demonstrated a dual-function pq theory algorithm which is able to generate reference current and simultaneously provides synchronization phase to ensure effective operation of SAPF, without relying on PLL element. Three notable improvements are implemented: replacement of numerical low-pass filter (LPF) with an average power detector to improve mitigation performance, removal of needless reactive element to reduce algorithm complexity and integration of phase tracking feature to eliminate the needs of PLL. Design concept and effectiveness of the proposed algorithm is thoroughly analyzed under steady-state and dynamic-state conditions. It is revealed from the simulation work that the proposed algorithm effectively eliminates ripples that are retained in the detected fundamental element and subsequently directs the SAPF in mitigating harmonic current with a lower THD value. Moreover, under dynamic-state condition, by applying the proposed algorithm, SAPF can perform with fast response time and without any undershoots. Effectiveness of the proposed algorithm is further supported and validated by experimental analysis. Overall, the low THD value of mitigated source current and fast response time in mitigating harmonic current are the key benefits of applying the proposed algorithm over the existing algorithm.

Author Contributions: Y.H. designed and developed the simulation model, experimental prototype, and performed all the tests and analyses for the research work. Y.H. was also mainly responsible for preparing the initial draft of the manuscript. M.A.M.R. actively contributed to the simulation and experimental works, and manuscript improvement. M.K.H. and N.F.M. contributed to verifying the work and finalizing the manuscript.

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References

1. Choi, W.-H.; Lam, C.-S.; Wong, M.-C.; Han, Y.-D. Analysis of dc-link voltage controls in three-phase four-wire hybrid active power filters. *IEEE Trans. Power Electron.* **2013**, *28*, 2180–2191. [[CrossRef](#)]
2. Kale, M.; Ozdemir, E. An adaptive hysteresis band current controller for shunt active power filter. *Electr. Power Syst. Res.* **2005**, *73*, 113–119. [[CrossRef](#)]
3. Akagi, H. Active harmonic filters. *Proc. IEEE* **2005**, *93*, 2128–2141. [[CrossRef](#)]
4. Green, T.C.; Marks, J.H. Control techniques for active power filters. *IEE Proc. Electr. Power Appl.* **2005**, *152*, 369–381. [[CrossRef](#)]
5. Asiminoaei, L.; Blaabjerg, F.; Hansen, S. Detection is key-harmonic detection methods for active power filter applications. *IEEE Ind. Appl. Mag.* **2007**, *13*, 22–33. [[CrossRef](#)]
6. Chudamani, R.; Vasudevan, K.; Ramalingam, C.S. Non-linear least-squares-based harmonic estimation algorithm for a shunt active power filter. *IET Power Electron.* **2009**, *2*, 134–146. [[CrossRef](#)]
7. Eskandarian, N.; Beromi, Y.A.; Farhangi, S. Improvement of dynamic behavior of shunt active power filter using fuzzy instantaneous power theory. *J. Power Electron.* **2014**, *14*, 1303–1313. [[CrossRef](#)]
8. Wang, L.; Lam, C.-S.; Wong, M.-C. Unbalanced control strategy for a thyristor-controlled LC-coupling hybrid active power filter in three-phase three-wire systems. *IEEE Trans. Power Electron.* **2017**, *32*, 1056–1069. [[CrossRef](#)]
9. Popescu, M.; Bitoleanu, A.; Suru, V. A DSP-based implementation of the p-q theory in active power filtering under nonideal voltage conditions. *IEEE Trans. Ind. Inform.* **2013**, *9*, 880–889. [[CrossRef](#)]
10. Pigazo, A.; Moreno, V.M.; Estebanez, E.J. A recursive park transformation to improve the performance of synchronous reference frame controllers in shunt active power filters. *IEEE Trans. Ind. Electron.* **2009**, *24*, 2065–2075. [[CrossRef](#)]
11. Monfared, M.; Golestan, S.; Guerrero, J.M. A new synchronous reference frame-based method for single-phase shunt active power filters. *J. Power Electron.* **2013**, *13*, 692–700. [[CrossRef](#)]

12. Musa, S.; Radzi, M.A.M.; Hizam, H.; Wahab, N.I.A.; Hoon, Y.; Zainuri, M.A.A.M. Modified synchronous reference frame based shunt active power filter with fuzzy logic control pulse width modulation inverter. *Energies* **2017**, *10*, 758. [[CrossRef](#)]
13. Vodyakho, O.; Mi, C.C. Three-level inverter-based shunt active power filter in three-phase three-wire and four-wire systems. *IEEE Trans. Power Electron.* **2009**, *24*, 1350–1363. [[CrossRef](#)]
14. Sujitjorn, S.; Areerak, K.-L.; Kulworawanichpong, T. The dq axis with Fourier (dqF) method for harmonic identification. *IEEE Trans. Power Deliv.* **2007**, *22*, 737–739. [[CrossRef](#)]
15. Bhattacharya, A.; Chakraborty, C. A shunt active power filter with enhanced performance using ANN-based predictive and adaptive controllers. *IEEE Trans. Ind. Electron.* **2011**, *58*, 421–428. [[CrossRef](#)]
16. Abdeslam, D.O.; Wira, P.; Merckle, J.; Flieller, D.; Chapuis, Y.-A. A unified artificial neural network architecture for active power filters. *IEEE Trans. Ind. Electron.* **2007**, *54*, 61–76. [[CrossRef](#)]
17. Wang, H.; Li, Q.; Wu, M. Investigation on a new algorithm for instantaneous reactive and harmonic currents detection applied to intensive nonlinear loads. *IEEE Trans. Power Deliv.* **2007**, *22*, 2312–2318. [[CrossRef](#)]
18. Ng, C.H.; Busawon, K.; Putrus, G.A.; Ran, L. Fast-individual-harmonic-extraction technique. *IEE Proc. Gener. Transm. Distrib.* **2005**, *152*, 556–562. [[CrossRef](#)]
19. Rodriguez, P.; Pou, J.; Bergas, J.; Candela, J.I.; Burgos, R.P.; Boroyevich, D. Decoupled double synchronous reference frame PLL for power converters control. *IEEE Trans. Power Electron.* **2007**, *22*, 584–592. [[CrossRef](#)]
20. Campanhol, L.B.G.; Silva, S.A.O.; Goedtel, A. Application of shunt active power filter for harmonic reduction and reactive power compensation in three-phase four-wire systems. *IET Power Electron.* **2014**, *7*, 2825–2836. [[CrossRef](#)]
21. Mailah, N.F.; Hoon, Y.; Radzi, M.A.M. Dc-link capacitor voltage regulation with effort-reduction fuzzy logic control for three-level inverter-based shunt active power filter. *Pertanika J. Sci. Technol.* **2017**, *25*, 11–20.
22. Hoon, Y.; Radzi, M.A.M.; Hassan, M.K.; Mailah, N.F. Dc-link capacitor voltage regulation for three-phase three-level inverter-based shunt active power filter with inverted error deviation control. *Energies* **2016**, *9*, 533. [[CrossRef](#)]
23. Hoon, Y.; Radzi, M.A.M.; Hassan, M.K.; Mailah, N.F. A simple neutral-point voltage deviation minimization method for three-level inverter-based shunt active power filter. *Int. J. Simul. Syst. Sci. Technol.* **2016**, *17*, 33.31–33.36.
24. Bhalodi, K.H.; Agarwal, P. Space vector modulation with dc-link voltage balancing control for three-level inverters. *ACEEE Int. J. Commun.* **2010**, *1*, 14–18.
25. Leva, S.; Morando, A.P.; Zaninelli, D. Evaluation of line voltage drop in presence of unbalance, harmonics, and interharmonics: Theory and applications. *IEEE Trans. Power Deliv.* **2005**, *20*, 390–396.
26. Herrera, R.S.; Salmeron, P.; Kim, H. Instantaneous reactive power theory applied to active power filter compensation: Different approaches, assessment, and experimental results. *IEEE Trans. Ind. Electron.* **2008**, *55*, 184–196. [[CrossRef](#)]
27. IEEE Power and Energy Society. *IEEE Standard 519, IEEE Recommended Practice and Requirement for Harmonic Control in Electric Power Systems*; Institute of Electrical and Electronics Engineers, Inc.: New York, NY, USA, 2014.

