



Model-Based Current Sharing Approach for DCM Interleaved Flyback Micro-Inverter

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Abstract: Current sharing control is a challenge for a discontinuous conduction mode (DCM) micro-inverter based on interleaved flyback topology. To solve this problem, this study proposes a novel and systemic model-based approach. Firstly, an accurate fourth-order model is presented for the interleaved flyback circuit, which takes the two flybacks' parameter mismatch and coupling into account. Secondly, based on the presented model, a continuous time sliding mode current controller is proposed to tackle the output imbalance caused by parameter mismatch, coupling and disturbance. The proposed controller is derived from the Lyapunov function without switching conditions. Finally, the effectiveness of the proposed model and control method is validated by simulation tests using MATLAB/SIMULINK. Simulation results show that the proposed approach improves the current sharing for the interleaved flyback micro-inverter when compared to the conventional current sharing approach.

Keywords: accurate modeling; sliding mode control (SMC); current control; interleaved flyback

1. Introduction

In recent years, power generation technology has been a popular research topic in the photovoltaic (PV) and wind energy field [1–3]. The typical PV generation module, which is called the micro-inverter, makes it possible to realize the individual maximum power point tracking (MPPT) of each PV panel. While a large number of studies have been done on the topology and control strategy for the micro-inverters [4–6], the flyback topology has attracted significant interest due to its simple structure, low cost and high reliability. It not only enables individual operation of each module, but also reduces the power loss caused by the mismatch between modules [7–9]. Furthermore, the parallel and interleaved structure of the flyback can reduce the system loss, decrease current ripple, prevent the single point failure, and offer 'plug and play' feature to the system [10–13].

Flyback converters operating in discontinuous conduction mode (DCM) are widely used because their output current is easy to control [14]. Despite much research [14–16] having been conducted on the DCM converter with regard to conversion efficiency enhancement, harmonic reduction and compensation of output current phase-leg, the current sharing between dual-flyback converters has been ignored under the assumption that the design parameters of the two flyback converters are the same. However, such an assumption is not generally true in the industrial field, as the precise parameters may not be guaranteed under a cost-effective manufacturing procedure. Additionally,

their couplings.

the two converters would not be operating under the exact same conditions in practice due to parameter variations caused by device ageing and other uncertainties. Together with the coupling between the two flyback converters, the parameter mismatch could cause the output current imbalance, which potentially overloads one of the converters. Consequently, the efficiency and reliability of the micro-converter system drops. In the worst case, the output current imbalance may cause the overloaded converter to suffer from thermal runaway [17,18]. Although there has been a great deal of research into dynamic performances such as conversion efficiency and output current ripple, current sharing has not been completely studied. To address the output imbalance issue, current sharing control strategies for the dual-flyback converters should be studied, the aim of which would be to build up an accurate model describing the dynamic characteristic of the converters and

When converters such as buck, boost and flyback operate in DCM, there are two modeling methods, namely, the average small-signal [16,19–21] and the duty-ratio constraint method [22–28]. The average small-signal modeling method has mainly been employed to study system conversion efficiency [16,20] and harmonic distortion [19,21]. The average small-signal modeling method is over-simplified through ignoring the equivalent series resistances of the inductances in primary/secondary side of transformer, which could result in large differences and discrepancies at high frequencies due to the high-frequency pole. The duty-radio constraint method is a modified average-state-space method, in which the parasitic parameters of the converter can be taken into consideration. Consequently, the resulting models have good dynamic performance at low frequency and at high frequency [22]. Accordingly, the models based on the duty-ratio constraint method have been widely employed in application fields that call for high accuracy models, such as studying the relationship of the transfer function of the duty-cycle to the weighted-output-voltage of single-input multiple-output flyback converters [23], analyzing the influence of parasitic parameters on input current distortion with a boost power factor correction converter [24], and discussing power flow between two inputs of an interleaved-boost full-bridge three-port converter [25]. Considering that current sharing requires a precise model of flyback micro-inverter, the duty-ratio constraint method is a good candidate for modeling, but the modeling issue has not been covered in the existing literature.

The available methods of achieving current sharing can be categorized into two types, hardware-added type [29-31] and hardware-free type. The first type requires additional hardware, such as equivalent resonant capacitor and series bus capacitors, which would increase the system cost directly. The second type uses a current sharing control method, which includes the droop control method [32–34], distributed control method [35] and PI control [17,18]. The droop control and distributed control have been applied to power supply modules. The droop control method noted for being simple, inexpensive and efficient, since there is no communication connection among power supply modules [33]. The distributed control method uses the consensus algorithm, in which information such as current and voltage of the distributed modules is required [35]. The current sharing is achieved through feedback of output signals to the control duty cycle, which could cause a control delay compared to the method of controlling the primary side current directly [19]. Meanwhile, these methods are inapplicable to the dual flyback converters when the duty cycles of two flyback converters are unfixed. For the CCM interleaved flyback micro-inverter, the multiple PI controllers are developed in [17,18] to achieve the current sharing, but they could not track the sinusoidal reference current without static error [36] and may increase the total harmonic distortions (THDs) of system. Besides, there are several parameters require to design for PI controllers, which increase the design complexity. Therefore, the current sharing control for DCM flyback micro-inverter needs to be studied.

To solve the aforementioned issue, a model-based current sharing approach for the DCM Interleaved Flyback micro-inverter is proposed in this paper. On one hand, the accurate and novel full-fourth-order model is established in the DCM interleaved flyback micro-inverter, showing the imbalance of the system parameters and coupling of the two flyback converters. On the other hand, to solve the load imbalance caused by parameter imbalance and disturbance, a current controller based on sliding mode control (SMC) for dual-flyback converter is designed, which ensures equal sharing of the injected current, and improves the dynamic performance of the system. In the proposed SMC, the primary currents of the presented model are used as controlled variables to achieve current sharing. This ensures equal sharing of the injected current and improves the dynamic performance of the system. The remaining sections are organized as follows: the accurate fourth-order model operating in DCM is introduced and analyzed in Section 2. The SMC controller is designed in Section 3, and Section 4 presents the simulations in MATLAB/SIMULINK. Finally, Section 5 concludes the study.

2. Working Principle and Dynamic Modeling

2.1. Working Principle

The DCM interleaved flyback has been widely used in AC-PV modules, because its output current is easy to control and it can achieve high efficiency [16]. Figure 1 illustrates the circuit of the interleaved flyback micro-inverter, which is comprised of a PV module, an input capacitor C_{pv} , the Flyback 1 converter, the Flyback 2 converter, an unfolding H-bridge inverter (M₁, M₂, M₃, M₄) and an output CL filter. V_{pv} is the PV voltage. *n* is the transformer turns ratio. L_{m1} , L_{m2} are the primary magnetizing inductances of the transformer, respectively. i_{m1} , i_{m2} are the primary currents of the transformer, respectively. i_{s1} , i_{s2} are the secondary currents of the transformer, respectively. L_f is the filter inductor. C_f is the filter capacitor. R_{p1} , R_{p2} are the equivalent series resistances of the primary magnetizing inductance, respectively. R_{s1} , R_{s2} are the equivalent series resistances of the secondary magnetizing inductance, respectively. R_f is the equivalent series resistance of the output filter inductor. D is main MOSFET duty cycle.



Figure 1. The interleaved flyback mirco-inverter topology.

As shown in Figure 1, the circuit diagram is a double-stage topology. The preceding stage is made up of a PV module and two flyback converters. This stage is controlled to extract maximum power from the PV module and to provide a semi-sinusoidal output current. The post-stage circuit consists of an unfolding H-bridge circuit, which forms a current-unfolding circuit for injecting sinusoidal AC current into the grid. How to control switches Q_1 and Q_2 in the preceding stage is studied in this paper.

There are two circumstances (D > 0.5 or D < 0.5) for the PWM singles, which are used to control switches Q_1 and Q_2 during one switching cycle [14]; the PWM waveforms are shown in detail in Figure 2. Meanwhile, the corresponding equivalent circuits of steady-state operation are illustrated in Figure 3, and Table 1 identifies the operating phases for switches and diodes (Q_1 , Q_2 , Diode 1 and Diode 2) with respect to the equivalent circuits. When D < 0.5, the primary and secondary current waveforms of the interleaved flyback micro-inverter are shown in Figure 2b. The matching current flowing paths in one switching cycle for steady-state path (1) are: Interval 1 \rightarrow Interval 2

 \rightarrow Interval 3 \rightarrow Interval 4 \rightarrow Interval 5 \rightarrow Interval 3. When the next cycle arrives, the secondary inductor currents i_{s1} and i_{s2} are reduced to zero, and the steady-state path (2) of matching the current flow is Interval 1 \rightarrow Interval 2 \rightarrow Interval 3 \rightarrow Interval 6 \rightarrow Interval 4 \rightarrow Interval 5 \rightarrow Interval 3 \rightarrow Interval 6. Additionally, when D > 0.5, there are six current flowing paths during one switching cycle, and the steady-state operation path is Interval 1 \rightarrow Interval 2 \rightarrow Interval 5 \rightarrow Interval 5 \rightarrow Interval 7. The model operating in steady-state path (1) is established and explicitly described below.

Interval	Switch Q1	Switch Q2	Diode 1	Diode 2
1	ON	OFF	OFF	ON
2	ON	OFF	OFF	OFF
3	OFF	OFF	ON	ON
4	OFF	ON	ON	OFF
5	OFF	ON	OFF	OFF
6	OFF	OFF	OFF	OFF
7	ON	ON	OFF	OFF

Table 1. Circuit steady-state operations.



Figure 2. PWM and duty cycle for one switching period and the waveforms of i_{m1} , i_{s1} , i_{m2} , and i_{s2} corresponding to switch Q1, Q2 and Duty cycle state.



Figure 3. Equivalent circuits for different intervals in steady-state operation.

2.2. Accurate Dynamic Model in DCM Operation

In order to facilitate modeling and analysis, the following assumptions are made in this paper:

- 1. The DC side decoupling capacitor C_{pv} is large enough to neglect the current ripple across the C_{pv} ;
- 2. The equivalent series resistances (ESR) of the inductances in primary/secondary side of transformer and the output filter are considered. The transformer leakage inductance is ignored.

As shown in Figure 2b, one switching interval is divided into six subintervals. Accurately representing the dynamic of the converter circuit, a precise full-order averaged model [22,23,25] of the converter circuit is used.

$$\dot{\overline{x}} = \left(\sum_{k=1}^{H} \left(d_k A_k\right)\right) \Phi \overline{x} + \left(\sum_{k=1}^{H} \left(d_k B_k\right)\right)$$
(1)

where \overline{x} represents the average of x in one switching period. H denotes the number of steady-state subintervals in the DCM interleaved flyback (H = 6). A_k and B_k are presented as steady-state equations in k-interval. Equation (1) revises the conventional state-space average equation through matrix Φ , which can accurately predict performance at high frequency (above one-tenth of the switching frequency), particularly in the phase response [22].

$$\Phi = diag \left[\underbrace{\frac{1}{d_{off_{-1}} + d_{on_{-1}}}}_{n_L} \frac{1}{d_{off_{-2}} + d_{on_{-2}}} \dots 1 \right]$$
(2)

where $d_{off_1} = d_3 + d_4$, $d_{off_2} = d_6 + d_1$, $d_{on_1} = d_1 + d_2$ and $d_{on_2} = d_4 + d_5$. n_L denotes the number of inductor currents of the transformer. d_{on_1} and d_{on_2} denote duty cycles of switches Q₁ and Q₂ when

they are on, respectively. d_{off_1} and d_{off_2} represent the time intervals from turn-off of switches Q_1 and Q_2 to the decreasing to zero of the transformer inductor currents, respectively.

From Equations (1) and (2), the state-space averaged large signal model of the interleaved flyback micro-inverter is derived as

$$\begin{bmatrix} \frac{d\bar{i}_{m1}}{dt} \\ \frac{d\bar{i}_{m2}}{dt} \\ \frac{d\bar{i}_{ac}}{d\bar{t}} \\ \frac{d\bar{v}_{ac}}{d\bar{t}} \\ \frac{d\bar{v}_{ac}}{d\bar{t}} \end{bmatrix} = \begin{bmatrix} -\frac{d_{on_{-1}}R_{p1}+d_{off_{-1}}\frac{R_{s1}}{n^2}}{L_{m1}(d_{on_{-1}}+d_{off_{-1}})} & 0 & 0 & -\frac{d_{off_{-1}}}{L_{m1}} \\ 0 & -\frac{d_{on_{-2}}R_{p2}+d_{off_{-2}}\frac{R_{s2}}{n^2}}{L_{m2}(d_{on_{-2}}+d_{off_{-2}})} & 0 & -\frac{d_{off_{-2}}}{L_{m2}} \\ 0 & 0 & -\frac{R_f}{L_f} & \frac{1}{L_f} \\ \frac{d_{on_{-1}}}{\bar{v}_{ac}} \end{bmatrix} + \begin{bmatrix} \frac{d_{on_{-1}}}{L_{m1}} & 0 \\ \frac{d_{on_{-2}}}{L_{m2}} & 0 \\ 0 & -\frac{1}{L_f} \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \overline{V}_{pv} \\ |\overline{V}_g| \end{bmatrix}$$
(3)

The correlations between $d_{off_i(i=1,2)}$ and $d_{on_i(i=1,2)}$ are shown as follows

$$d_{off_{-1}} = 2L_{m1}i_{m1}/(d_{on_{-1}}V_{pv}(t)T_s) - d_{on_{-1}}$$
(4)

$$d_{off_2} = 2L_{m2}i_{m2}/\left(d_{on_2}V_{pv}(t)T_s\right) - d_{on_2}$$
(5)

According to Equations (4) and (5), and linearizing Equation (3), the small signal model is derived as follows:

$$\widetilde{x}(t) = A_{DCM}\widetilde{x} + B_{DCM}d + W_{DCM}Z$$

$$y = \begin{bmatrix} 0 & 0 & 1 & 0 \end{bmatrix} \widetilde{x}$$
(6)

where $\tilde{x} = \begin{bmatrix} \tilde{i}_{m1} & \tilde{i}_{m2} & \tilde{i}_{ac} & \tilde{v}_{ac} \end{bmatrix}^T$, $Z = \begin{bmatrix} \tilde{V}_{pv} & \tilde{V}_g \end{bmatrix}^T$, and the parameters A_{DCM} , B_{DCM} and W_{DCM} are derived in Appendix A.

2.3. Comparisons between the Accurate Dynamic Model and the Existing Model

To analyze the relationships between dual-flyback converters and output grid current of the accurate dynamic model and existing models, the output current to duty cycle transfer functions of these models are established. The analysis and comparisons between the accurate model and the existing models are shown as follows.

2.3.1. The Proposed Fourth-Order Model

Assume the difference between \tilde{d}_{on_1} and \tilde{d}_{on_2} is ignored, then the condition of $\tilde{d} = \tilde{d}_{on_1} = \tilde{d}_{on_2}$ is satisfied. From Equation (6), the output current to duty cycle transfer function of the proposed fourth-order model G_{dt4} is shown as

$$G_{dt4}(s) = \frac{\tilde{i}_{ac}(s)}{\tilde{d}(s)} = \frac{a_2 s^2 + a_1 s + a_0}{s^4 + b_3 s^3 + b_2 s^2 + b_1 s + b_0}$$
(7)

when the two flyback converters' parameters are identical, the fourth-order model G_{dt4} is reduced to a third-order model G_{dt3_1} .

$$G_{dt3_{-1}}(s) = \frac{\tilde{i}_{ac}(s)}{\tilde{d}(s)} = \frac{c_1 s + c_0 + c'_0}{s^3 + (j_2 + f_2)s^2 + (j_1 + f_1)s + (j_0 + f_0)}$$
(8)

2.3.2. The Third-Order Model Based on Single-Phase Flyback Converter

For the sake of simplification, by assuming the two flyback converters identical and connected in parallel, some researchers have treated the dual-flyback micro-inverter as a single-phase flyback

micro-inverter [23]. The third-order model G_{dt3_2} based on a single-phase flyback converter is studied instead of G_{dt3_1} , which is expressed as

$$G_{dt3_2}(s) = \frac{\tilde{i}_{ac}(s)}{\tilde{d}(s)} = \frac{c_1 s + c_0}{s^3 + j_2 s^2 + j_1 s + j_0}$$
(9)

2.3.3. The Second-Order Model Based on Average Small Signal

The second-order model based on a voltage-controlled current source is studied in [16,19,37–41]. This model is applied to reduce current stress and THDs and improve system efficiency. The second-order model is expressed as

$$G_{d2} = \frac{\tilde{i}_{ac}}{\tilde{d}} = \frac{h_2}{s^2 + h_1 s + h_0}$$
(10)

where the parameters $a_2 \sim a_0$, $b_3 \sim b_0$, $c_1 \sim c_0$, c'_0 , $j_2 \sim j_0$, $f_2 \sim f_0$ and $h_2 \sim h_0$ are given in Appendix A.

Comparing Equations (9) and (10), the second-order model is further simplified. The equivalent parasitic resistances R_{p1} , R_{p2} , R_{s1} and R_{s2} of transformer magnetizing inductance are ignored. The model G_{d2} can only accurately predict the low-frequency behavior of the converter, but the model G_{dt3_2} can precisely predict the behavior of the converter at high and low frequencies [22], so the model G_{dt3_2} has more accuracy. On the other hand, G_{dt3_1} is much more accurate than G_{dt3_2} since the coupling terms c'_0, f_2, f_1 and f_0 between dual-flyback are considered. In addition, G_{dt4} is better than G_{dt3_1} since parameter mismatch is taken into account. Therefore, with all that analysis, the sequence of models' accuracy is $G_{dt4} > G_{dt3_1} > G_{dt3_2} > G_{d2}$.

On the other hand, in the second-order model G_{d2} and in the third-order model G_{dt3_2} , the primary magnetizing inductance currents i_{m1} and i_{m2} are not used as state variables. Therefore, these two models cannot be used to control the primary current to achieve current sharing. The proposed model G_{dt4} in this paper shows the parameter inconsistency and coupling between the two flybacks, which are the major sources of the current imbalance between the two flybacks. Meanwhile, the model takes i_{m1} and i_{m2} into account. Thus, it should be able to supply a solid basis to meet the current sharing requirement. Plus, a model-based current balance control strategy could be developed based on the proposed model.

3. Current Controller Using Sliding Mode Control

In this section, to solve the output imbalance between the two flybacks caused by coupling, parameter inconsistency and disturbance, a novel sliding mode control (SMC) current controller is designed, since the SMC would realize good dynamic response, strong robustness and good regulation performance [42–45]. In this paper, the SMC current sharing controller is constructed based on the presented accurate fourth-order mathematical model, and the target of current sharing is realized by controlling the primary currents of dual-flyback converters to regulate the duty cycles of switches Q_1 and Q_2 . The flow chart for designing the sliding mode current sharing controller are shown in Figure 4.



Figure 4. Flow chart for designing sliding mode current sharing controller.

According to Figure 4, the design steps of SMC are as follows: **Step 1:** *A stable sliding surface is selected.* Equation (6) can be written in matrix form as

$$u = A\dot{x} + Bx + r \tag{11}$$

where $x = [i_{m1} \ i_{m2}]^T$, $u = [d_{on_1} \ d_{on_2}]^T$, r is the external disturbances of the input and output voltages. The parameters of matrix A, B and r are given in Appendix A.

 e_1 and e_2 are defined as the current tracking errors of Flyback 1 and Flyback 2, respectively. Meanwhile, *e* is defined as the error vector, $e = [e_1 e_2]^T$. The tracking error dynamics are

$$e_1 = I_{1ref} - i_{m1} e_2 = I_{2ref} - i_{m2}$$
(12)

where, I_{1ref} and I_{2ref} are the primary currents references of the Flyback 1 and Flyback 2 converters, respectively, which are given in [16,19].

According to Equations (11) and (12), the current tracking errors dynamic can be written as:

$$\dot{e} = \dot{I}_{ref} - A^{-1}(u - r - Bx)$$
(13)

where, the reference current I_{ref} is denoted as $[I_{1ref} \ I_{2ref}]^T$. Let current sharing error α between two circuits be presented as $(e_1 - e_2)$. Then the error matrix $[e_1 \ e_2 \ \alpha]^T$ would be calculated from individual tracking error by using the following transformation *F*:

$$\begin{bmatrix} e_1 & e_2 & \alpha \end{bmatrix}^T = Fe \tag{14}$$

where $F = \begin{bmatrix} 1 & 0 & 1 \\ 0 & 1 & -1 \end{bmatrix}^T$.

The overall objective of the controller is to minimize all the errors presented in Equation (14), namely the current tracking error and the current sharing error of the interleaved flyback circuit. Then a 3×1 dimensional sliding surface *S* is defined as:

$$S = \begin{bmatrix} S_1 & S_2 & S_3 \end{bmatrix}^T = \lambda Fe$$
(15)

where $\lambda = diag(\lambda_1, \lambda_2, \lambda_3)$ is the sliding coefficient, it defines the convergence speed of the errors on the sliding surface.

Step 2: The disturbance observer is designed. To ensure the controller has good robustness and the system has zero steady-state error, the disturbance observer is designed by integrating the sliding surface. The disturbance observer is defined as

$$\hat{r} = \begin{bmatrix} \hat{r}_1 \\ \hat{r}_2 \end{bmatrix} = \rho\beta \int Sd\tau = \rho\beta\lambda F \int ed\tau$$
(16)

where ρ is the observer gain matrix, which is a tuning variable for the sliding mode controller. β is coefficient matrix for limiting integral gain. Additionally, only when the transfer matrix $\rho\beta$ satisfies the following conditions is the integration performed.

$$\rho\beta = \begin{bmatrix} \rho_1\beta_1 & 0 & 0\\ 0 & \rho_2\beta_2 & 0 \end{bmatrix}$$
(17)

Step 3. A stable SMC control law is derived (Barbalat Lemma [46]).

The stable SMC control law is presented as

$$u = A\dot{I}_{ref} + Bx + \hat{r} + F^{-1}\lambda^{-1}KS$$
(18)

where the matrix $K = diag(K_1, K_2, K_3)$ is a positive definite feedback gain matrix. The stability of the system and errors asymptotically converge to the origin point, as proved in Appendix B.

Substituting Equation (16) into Equation (18), the synchronization sliding control law shown in Equation (18) is revised. The modified sliding control law is:

$$u = A\dot{I}_{ref} + Bx + \rho\beta \int Sd\tau + F^{-1}\lambda^{-1}KS$$

$$= A\dot{I}_{ref} + Bx + S(kp, smc + ki, smc/s)$$
(19)

where, F^{-1} is the pseudo-inverse of *F*.

Equation (19) indicates the sliding mode control law u is made up of two items, $(AI_{ref} + Bx)$ and (kp, smc + ki, smc/s). The first item presents as feed-forward, which can improve the tracking bandwidth and reject the measured disturbance, the parameters *A* and *B* of $(AI_{ref} + Bx)$ can be obtained from system parameters. The second item can present as PI gain, so there are only two parameters $k_{p,smc}$ and $k_{i,smc}$ need to design. In other words, the proposed sliding mode current sharing controller converts the control problem of multiple PI controllers into a single PI controller, reducing complexity of design.

4. Simulation

To prove the validity and feasibility of the proposed model and controller, the dynamic performance is compared with the PI current control by MATLAB/Simulink (2016b, MathWorks, Natick, MA, USA). A 250 W digitally controlled DCM interleaved flyback micro-inverter prototype is designed, where each flyback circuit is 125 W, and the PV output voltage is 20 V–55 V. The system parameters of the interleaved flyback micro-inverter are shown in Table 2, where the inductances of the transformer secondary side are 216 uH, and P_0 is the rated power of the interleaved flyback micro-inverter.

Parameters	Value	Parameters	Value
C_{PV}	11 mF	R_{p1}, R_{p2}	0.15 Ω
L_{m1}, L_{m2}	6 μΗ	R_{s1}, R_{s2}	$0.05 \ \Omega$
C_{f}	0.68 μF	R_f	0.29 Ω
L_{f}	600 µH	$\dot{V_g}$	220 V
n	6	f_{g}	50 Hz
f_s	100 kHz	P_0	250 W

Table 2. System parameters.

The control diagrams of SMC and PI current controller are illustrated in Figures 5 and 6, respectively. Figure 5 demonstrates the proposed sliding mode current sharing control adopts an open-loop control. The current sharing is achieved through sensing and controlling the primary switch currents. In order to meet the consistency of the comparison conditions, the current open-loop control mode is adopted in the conventional PI current sharing controller; the PI control diagram is indicated in Figure 6. It shows the PI current sharing controller consists of 3 PI controllers, which increases the design complexity. The designed sliding mode control law u is presented as Equation (17), the parameters matrix *A*, *B* can be obtained from the system parameters. From Equation (19), the coefficients of $k_{p,snc}$ and $k_{i,snc}$ refer to $F^{-1}\lambda^{-1}K$ and $\rho\beta$, respectively. In order to meet the condition

of asymptotic convergence, the parameters of $F^{-1}\lambda^{-1}K$ and $\rho\beta$ are obtained from $\rho = I$, $\beta_1 = 0.08$, $\beta_2 = 0.08$, $\lambda = I$ and K = I, where I is the unit matrix.



Figure 5. The SMC control diagram of interleaved flyback micro-inverter in DCM.



Figure 6. The PI control diagram of interleaved flyback micro-inverter in DCM.

In order to compare dynamic performances between the PI current sharing and the proposed sliding mode current sharing controller, the two cases are designed as follows. The output voltage V_{rms} is 220 V:

Case 1: The two converters, Flyback 1 and Flyback 2, have the same parameters in the flyback micro-inverter. The output power P_{out} is changed from 200 W (80% P_0) to 125 W (50% P_0) at 0.06 s.

Figure 7 shows the waveform of the output grid current i_{ac} and reference signal i_{ac_ref} of the interleaved flyback micro-inverter with the PI current sharing controller. There are large current ripples in simulation's waveform. When the output power decreases to 50% P_0 , the PI controller regulates the output current to track the desired output current, which shows the ability to reject the disturbance, but oscillation still exists. Through the Fast Fourier Transformation (FFT) analysis, when $0.02 \text{ s} \le t \le 0.06 \text{ s}$, the THDs of output current is 6.43%; when $0.06 \text{ s} < t \le 0.1 \text{ s}$, the THDs of output current is 6.58%.



Figure 7. Simulation results of case 1: output grid current response of the interleaved flyback micro-inverter controlled by a PI controller.

Figure 8 shows the waveforms of the output grid current i_{ac} of the interleaved flyback micro-inverter and reference signal i_{ac_ref} with the sliding mode current sharing controller. When the output power P_{out} changes from 80% P_0 to 50% P_0 , the effective value of the output current i_{ac} is from 0.908 A to 0.565 A, where only a small transient current is observed in the current waveforms. Through the FFT analysis, when 0.02 s $\leq t \leq 0.06$ s, the output current THDs of the SMC controller is 2.97%; when 0.06 s < $t \leq 0.1$ s, the output current THDs of the SMC controller is 2.45%.



Figure 8. Simulation results of case 1: output grid current response of the interleaved flyback micro-inverter controlled by a SMC controller.

The simulation results show that the proposed SMC controller has stronger robustness to solve the output imbalance caused by disturbance when the output power decreases. Moreover, compared with the PI controller, the output current i_{ac} of the proposed SMC controller has lower THDs.

Case 2: The two converters, Flyback 1 and Flyback 2, have different parameters in the flyback micro-inverter. The output power P_{out} is also changed from 200 W (80% P_0) to 125 W (50% P_0) at 0.06 s. The different parameters are given in Table 3.

Parameters	Value	Parameters	Value	Parameters	Value
L_{m1}	5.8 µH	R_{p1}	0.15 Ω	R_{s1}	0.051 Ω
L_{m2}	6.2 μH	R_{p2}	$0.18 \ \Omega$	R_{s2}	$0.085 \ \Omega$
L_{s1}	216 µH	L_{s2}	223 µH	-	-

Table 3. Different system parameters.

Figure 9 describes the waveform of the output grid current i_{ac} of the interleaved flyback micro-inverter using the PI current sharing controller, which tracks the reference current I_{ref} well. The output current ripple becomes large when the output current is biggest. As the output power reduces to 50% P_0 , the output current stays away from the balance point and then converges in short time. Through the FFT analysis, when 0.02 < t < 0.06, the output current THDs of the PI controller are 6.85%; when $0.06 \le t \le 0.1$ s, the output current THDs of the PI controller are 6.87%.



Figure 9. Simulation results of Case 2: output gird current response of interleaved flyback micro-inverter by a PI controller.

Figure 10 shows the waveform of the output grid current i_{ac} of interleaved flyback micro-inverter using the proposed sliding mode current sharing controller. There is a small current transient when output power decreases to 125 W. Through the FFT analysis, when 0.02 < t < 0.06, the THD of output current is 3.44%; when $0.06 \le t \le 0.1$ s, the THD of output current is 2.83%. The results of Figures 9 and 10 confirm that SMC has better robustness than the PI controller when a parameter imbalance exists.



Figure 10. Simulation results of Case 2: output gird current response of interleaved flyback micro-inverter by a SMC controller.

Figure 11a,b demonstrates the waveforms of primary currents i_{m1} and i_{m2} with the PI controller, and those with the sliding mode controller, respectively. It is very clear that the peak current difference between the two primary currents is about 0.318 A with the PI current sharing control, while that with the sliding mode controller is much less, and its value is approximate 0.05 A. From the results, it is found that the proposed controller achieved better current sharing in comparison to the PI controller, which would avoid one of the converters being overloaded, resulting in lower efficiency and reliability of the system, and even reducing the life of hardware. Figure 12 shows the current sharing errors after two current sharing controllers, and the results indicate that the current sharing errors with both controllers are almost negligible, but the THDs of the output current with the PI controller are bigger.



Figure 11. Waveforms for primary currents by two current sharing controllers: (**a**) PI current sharing control; (**b**) sliding mode current sharing control.



Figure 12. The difference of the two primary currents after the two current sharing controllers: (**a**) PI current sharing control; (**b**) sliding mode current sharing control.

In summary, the simulation results show that the proposed SMC has good robustness to solve output imbalance caused by coupling, parameter inconsistency and disturbance. No matter whether the parameters are identical or not, compared with the PI current sharing controller, the proposed SMC tracks a sinusoidal reference effectively and achieves current sharing. Simultaneously, it has stronger anti-interference ability when the load power changes. In addition to that, there is a lower THD of i_{ac} when the proposed SMC is applied. The detailed simulation results are presented in Table 4.

Controller	Pout	Case 1		Case 2	
controller	1040 -	α	THDs	α	THDs
the proposed SMC	200 W	0.001	2.97%	0.01	3.44%
PI controller	200 W	0.01	6.43%	0.32	6.85%
the proposed SMC	150 W	0.001	2.45%	0.05	2.83%
PI controller	150 W	0.005	6.58%	0.318	6.87%

Table	4	Simu	link	results
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5. Conclusions

This paper has proposed a current sharing control approach for the DCM interleaved flyback micro-inverter. To do this, an accurate fourth-order model has firstly been built up, which is used to analyze the output imbalance problem caused by coupling and mismatched parameters between two flyback converters, such as the equivalent series resistances and magnetizing inductances of the primary/secondary magnetizing inductance. The accuracy of the proposed model has been analyzed through comparing it to the third-order and second-order models, and the analysis results have shown that the proposed model is more accurate than the existing models.

Then, a sliding mode current sharing controller based on the full-fourth-order model has been developed, which solves the output imbalance caused by parameter imbalance and disturbance. The developed sliding mode controller comprises two parts: the feed-forward part, which can improve the tracking bandwidth and reject the measured disturbance, and the feedback part, which is in a form of PI. Thus, the proposed sliding mode controller can use the conventional PI blocks for current sharing. Since the parameters of the sliding control law can be designed by the proposed model and Lyapunov energy function, it is convenient to design compared to the PI current sharing controllers with four gains.

Finally, simulation studies were carried out in MATLAB/Simulink to validate the model accuracy, and the sliding mode current sharing controller. Results showed that the sliding mode controller tracks sinusoidal reference signals smoothly, and controls the current sharing between two flyback converters effectively when compared to the PI controllers.

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Nomenclature

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R_{p1}, R_{p2}	ESRs of the primary magnetizing inductance	Ω
R_{s1}, R_{s2}	ESRs of the secondary magnetizing inductance	Ω
R _f	ESR of the output filter inductor	Ω
i _{ac}	output current	А
i _{ac_ref}	the reference of i_{ac}	А
fs	switching frequency	kHZ
fg	frequency of gird	HZ
Vg	grid voltage	V
P_0	rated power	W
D	main MOSFET duty cycle	-
- ~	the average and small signal of x in one	
<i>x</i> , <i>x</i>	switching period	-
Н	the number of steady-state subinterval	-
A_k and B_k	steady state equations in k-interval	-
$d_{i(i=1,\ldots,k)}$	k-interval duty cycle	-
Φ	revised matrix	-
n_L	the number of inductor	-
	duty cycles of switches Q_1 and Q_2 when they	
$u_{on_{1}}, u_{on_{2}}$	are on	-
	the time intervals from turn-off of switches Q1	
d_{off_1} , d_{off_2}	and Q2 to the decreasing to zero of transformer	-
	inductor currents	
$A_{DCM}, B_{DCM}, W_{DCM}$	The coefficient matrixes of the proposed model	-
	The transfer functions of the proposed fourth-	
Guy Guay Gua Gu	order model, the reduced to third-order model,	_
$O_{dt4}, O_{dt3_1}, O_{dt3_2}, O_{d2}$	the third-order model based on single-phase	
	flyback converter and second-order model	
$\begin{array}{c} a_2 \sim a_0, b_3 \sim b_0, c_1 \sim c_0, c_0', j_2 \\ \sim j_0, f_2 \sim f_0, h_2 \sim h_0 \end{array}$	the coefficients of G_{dt4} , G_{dt3_1} , G_{dt_2} and G_{d2}	-
$e_1 e_2$	tracking errors of Flyback1 and Flyabck2	А
α	current sharing error	А
I.I.	the primary current references of Flyback1 and	٨
¹ 1ref , ¹ 2ref	Flyback2 converters	A
S	the sliding surface	-
λ	sliding coefficient	-
ρ	observer gain matrix	-
β	coefficient matrix for limiting integral gain	-
Κ	positive definite feedback gain matrix	-
k _{p.smc} , k _{i.smc}	proportional and integral of sliding mode law	-

Appendix A.

The parameters A_{DCM} , B_{DCM} and W_{DCM} of Equation (6) are given

$$A_{DCM} = \begin{bmatrix} -\left(\frac{R_{s1}}{n^2 L_{m1}} + \frac{2V_{ac}}{nD_{on_{-}1}T_s V_{pv}}\right) & 0 & 0 & -\frac{2I_{m1}}{nD_{on_{-}1}T_s V_{pv}} + \frac{D_{on_{-}1}}{nL_{m1}} \\ 0 & -\left(\frac{R_{s2}}{n^2 L_{m2}} + \frac{2V_{ac}}{nD_{on_{-}2}T_s V_{pv}}\right) & 0 & -\frac{2I_{m2}}{nD_{on_{-}2}T_s V_{pv}} + \frac{D_{on_{-}2}}{nL_{m2}} \\ 0 & 0 & -\frac{R_f}{L_f} & \frac{1}{L_f} \\ \frac{1}{nC_f} & \frac{1}{nC_f} & -\frac{1}{C_f} & 0 \end{bmatrix}$$
(A1)

$$B_{DCM} = \begin{bmatrix} \frac{2I_{m1}V_{ac}}{nD_{on_{-1}}^{2}T_{s}V_{pv}} - \frac{D_{on_{-1}}T_{s}V_{pv}(R_{\rho 1} - \frac{R_{s1}}{n^{2}})}{L_{m1}^{2}} + \frac{V_{ac}}{nL_{m1}} + \frac{V_{pv}}{L_{m1}} \\ \frac{2I_{m2}V_{ac}}{nD_{on_{-2}}^{2}T_{s}V_{pv}} - \frac{D_{on_{-2}}T_{s}V_{pv}(R_{\rho 2} - \frac{R_{s2}}{n^{2}})}{L_{m2}^{2}} + \frac{V_{ac}}{nL_{m2}} + \frac{V_{pv}}{L_{m2}} \\ 0 \\ - \frac{T_{s}V_{pv}}{nC_{f}} \left(\frac{D_{on_{-1}}}{L_{m1}} + \frac{D_{on_{-2}}}{L_{m2}}\right) \end{bmatrix}$$
(A2)
$$W_{DCM} = \begin{bmatrix} \frac{2I_{m1}V_{ac}}{nD_{on_{-1}}T_{s}V_{pv}^{2}} - \frac{D_{on_{-1}}T_{s}(R_{\rho 1} - \frac{R_{s1}}{n^{2}})}{2L_{m1}^{2}} + \frac{D_{on_{-1}}}{L_{m1}} & 0 \\ \frac{2I_{m2}V_{ac}}{nD_{on_{-2}}T_{s}V_{pv}^{2}} - \frac{D_{on_{-2}}T_{s}(R_{\rho 1} - \frac{R_{s1}}{n^{2}})}{2L_{m2}^{2}} + \frac{D_{on_{-1}}}{L_{m2}} & 0 \\ 0 & -\frac{1}{L_{f}} \\ -\frac{T_{s}}{2nC_{f}} \left(\frac{D_{on_{-1}}^{2}}{L_{m1}} + \frac{D_{on_{-2}}^{2}}{L_{m2}^{2}}\right) & 0 \end{bmatrix}$$

The coefficient parameters $a_2 \sim a_0$, $b_3 \sim b_0$ of G_{dt4} , $c_1 \sim c_0$, $j_2 \sim j_0$ and $f_2 \sim f_0$ of G_{dt3_1} , and $h_2 \sim h_0$ of G_{d2} are given as.

$$\eta_1 = \frac{R_{s1}}{n^2 L_{m1}} + \frac{2V_{ac}}{n D T_s V_{pv}}$$
(A5)

$$\eta_2 = \frac{R_{s2}}{n^2 L_{m2}} + \frac{2V_{ac}}{nDT_s V_{pv}}$$
(A6)

$$\eta_3 = -\frac{2I_{m1}}{nDT_sV_{pv}} + \frac{D}{nL_{m1}}$$
(A7)

$$\eta_4 = -\frac{2I_{m2}}{nDT_sV_{pv}} + \frac{D}{nL_{m2}}$$
(A8)

• $a_2 \sim a_0$:

$$a_{2} = \frac{DT_{s}V_{pv}}{nC_{f}L_{f}} \left(\frac{L_{m1} + L_{m2}}{L_{m1}L_{m2}}\right)$$
(A9)

$$a_{1} = \frac{-DT_{s}V_{pv}L_{m2}(R_{\rho1} - \frac{R_{s1}}{n^{2}}) + DT_{s}V_{pv}L_{m1}(R_{\rho2} - \frac{R_{s2}}{n^{2}})}{nC_{f}L_{f}L_{m1}L_{m2}} + \frac{DT_{s}V_{pv}}{nC_{f}L_{f}} (\frac{L_{m1} + L_{m2}}{L_{m1}L_{m2}}) (\frac{R_{s1}}{n^{2}L_{m1}} + \frac{R_{s2}}{n^{2}L_{m2}} + \frac{4V_{ac}}{nDT_{s}V_{pv}}) + \frac{2(I_{m1} - I_{m2})V_{ac}}{n^{2}D^{2}T_{s}V_{pv}C_{f}L_{f}} + \frac{(\frac{V_{ac}}{n} + V_{pv})(L_{m2} - L_{m1})}{nC_{f}L_{f}L_{m1}L_{m2}}$$
(A10)

$$a_{0} = \frac{2\eta_{2}I_{m1}V_{ac}}{n^{2}D^{2}T_{s}V_{pv}C_{f}L_{f}} - \frac{DT_{s}V_{pv}\eta_{2}(R_{p1} - \frac{R_{s1}}{n^{2}})}{nL_{m1}C_{f}L_{f}} + \frac{\eta_{2}V_{ac} + n\eta_{2}V_{pv}}{n^{2}L_{m1}C_{f}L_{f}} + \frac{\eta_{1} + \eta_{2}}{L_{f}}(\frac{2I_{m2}V_{ac}}{nD^{2}T_{s}V_{pv}} - \frac{DT_{s}V_{pv}(R_{p2} - \frac{R_{s2}}{n^{2}})}{L_{m2}} + \frac{V_{ac}}{nL_{m2}} + \frac{V_{pv}}{L_{m2}} + \frac{DT_{s}V_{pv}}{nC_{f}}(\frac{L_{m1} + L_{m2}}{L_{m1}L_{m2}}))$$
(A11)

• $b_3 \sim b_0$:

$$b_3 = \eta_1 + \eta_2 + \frac{R_f}{L_f}$$
(A12)

$$b_2 = -\frac{(\eta_3 + \eta_4)L_f}{nC_f} + \frac{1}{L_fC_f} + \eta_1\eta_2 + \frac{R_f(\eta_1 + \eta_2)}{L_f}$$
(A13)

$$b_1 = -\frac{(\eta_1\eta_4 + \eta_2\eta_3)L_f + (\eta_3 + \eta_4)R_f - n(\eta_1 + \eta_2) - n\eta_1\eta_2R_fC_f}{nC_fL_f}$$
(A14)

$$b_0 = -\frac{(\eta_2 \eta_3 + \eta_1 \eta_4)R_f - n(\eta_1 \eta_2)}{nC_f L_f}$$
(A15)

• $c_1 \sim c_0$:

$$c_1 = -\frac{\mathrm{D}T_{\mathrm{s}}V_{pv}}{\mathrm{n}L_{m1}C_fL_f} \tag{A16}$$

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$$c_{0} = -\frac{\frac{\eta_{1}}{L_{m1}} - \frac{V_{pv}}{L_{m1}} - \frac{V_{ac}}{nL_{m1}} - \frac{DT_{s}(R_{p1} - \frac{R_{s1}}{n^{2}})}{L_{m1}^{2}} - \frac{2I_{m1}V_{ac}}{nD^{2}T_{s}V_{pv}}}{nL_{f}C_{f}}$$
(A17)

• $j_2 \sim j_0$:

$$j_{2} = \frac{R_{f}}{C_{f}} - \eta 3 - \eta 1 + \frac{R_{p1}}{2} + \frac{R_{s1}}{2n^{2}L_{m1}} + \frac{(-D^{2}L_{m1}T_{s}^{2}V_{pv}^{2}n^{2} + 3nV_{ac}D^{2}L_{m1}T_{s}V_{pv} - 2nI_{m1}V_{ac}L_{m1})}{2D^{3}L_{m1}T_{s}^{2}V_{pv}^{2}n^{2}}$$
(A18)

$$j_1 = \eta_1 \eta_3 + \frac{1}{C_f L_f} - \frac{(1 + nR_f)(\eta_1 + \eta_3)}{nC_f} - {d_0}^2 + \frac{d_0(\eta_1 + \eta_3 - \frac{R_f}{C_f})}{2D^3 L_{m1} T_s^2 V_{pv}^2 n^2}$$
(A19)

$$j_0 = \frac{R_f \eta_1 \eta_3}{L_f} - \frac{\eta_1 + \eta_3}{C_f L_f} + \frac{2\eta_1 \eta_3 + \frac{R_f}{C_f} (\eta_1 + \eta_3)}{nC_f} - d_0 d_1 + \frac{d_1 (\eta_1 + \eta_3 - \frac{R_f}{C_f})}{2D^3 L_{m1} T_s^2 V_{pv}^2 n^2}$$
(A20)

• $f_2 \sim f_0$:

$$f_2 = \eta 3 - \frac{R_{p1}}{2} - \frac{R_{s1}}{2n^2 L_{m1}} - \frac{(-D^2 L_{m1} T_s^2 V_{pv}^2 n^2 + 3n V_{ac} D^2 L_{m1} T_s V_{pv} - 2n I_{m1} V_{ac} L_{m1})}{2D^3 L_{m1} T_s^2 V_{pv}^2 n^2}$$
(A21)

$$f_1 = -\eta_1 \eta_3 + \frac{\eta_1 + \eta_3 + nR_f(\eta_3 - \eta_1)}{nC_f} + {d_0}^2 - \frac{d_0(\eta_1 + \eta_3 - \frac{R_f}{C_f})}{2D^3 L_{m1} T_s^2 V_{pv}^2 n^2}$$
(A22)

$$f_0 = -\frac{R_f \eta_1 \eta_3}{L_f} + \frac{\eta_1 + \eta_3}{C_f L_f} - \frac{2\eta_1 \eta_3 + \frac{R_f}{C_f} (\eta_1 + \eta_3)}{nC_f} + d_0 d_1 - \frac{a_1 (\eta_1 + \eta_3 - \frac{R_f}{C_f})}{2D^3 L_{m1} T_s^2 V_{pv}^2 n^2}$$
(A23)

• $h_2 \sim h_0$:

$$h_2 = D'^2 V_{pv}^2 T_s / (C_f L_f L_{m1} V_{ac})$$
(A24)

$$h_1 = (L_{m1}V_0C_fR_f + \frac{D'^2V_{pv}^2T_sL_f}{2V_{ac}})/(C_fL_fL_{m1}V_{ac})$$
(A25)

$$h_0 = (L_{m1}V_{ac} + \frac{D'^2 V_{pv}^2 T_s R_f}{2V_{ac}}) / (C_f L_f L_{m1} V_{ac})$$
(A26)

The parameters of matrix *A*, *B* shown in Equation (11) are given as

$$A = \begin{bmatrix} a_{11} & 0\\ 0 & a_{12} \end{bmatrix}$$
(A27)

$$a_{11} = \frac{2I_{m1}L_{m1}^2 V_{ac} - nD^3 T_s^2 V_{pv}^2 (R_{\rho 1} - \frac{R_{s1}}{n^2}) + V_{ac}D^2 T_s V_{pv}L_{m1} + nD^2 T_s V_{pv}L_{m1}}{nD^2 T_s V_{pv}L_{m1}^2}$$
(A28)

$$a_{12} = \frac{2I_{m2}L_{m2}^2 V_{ac} - nD^3 T_s^2 V_{pv}^2 (R_{\rho 2} - \frac{R_{s2}}{n^2}) + V_{ac}D^2 T_s V_{pv}L_{m2} + nD^2 T_s V_{pv}^2 L_{m2}}{nD^2 T_s V_{pv}L_{m2}^2}$$
(A29)

$$B = \begin{bmatrix} \frac{a_{11}(DT_s V_{pv} R_{s1} + 2nL_{m1} V_{ac})}{n^2 DT_s V_{pv} L_{m1}} & 0\\ 0 & \frac{a_{12}(DT_s V_{pv} R_{s2} + 2nL_{m2} V_{ac})}{n^2 DT_s V_{pv} L_{m2}} \end{bmatrix}$$
(A30)

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Appendix B. Robustness of the Proposed SMC

Remark 1. The Lyapunov energy function V satisfies the condition of being lower bounded, so the V is constructed as

$$V = \frac{1}{2} [S^T S + (r - \hat{r})^T (M\rho)^{-1} (r - \hat{r})] > 0$$
(A31)

It is assumed that the SMC controller for current sharing is stable in the interleaved flyback micro-inverter. *V* represents the total energy of the system, which depends on the sliding surface dynamics by the S^TS term, also depends on the adaptive disturbance error by the $(r - \hat{r})^T (M\rho)^{-1} (r - \hat{r})$ term.

Remark 2. *The derivative of the Lyapunov function V must be negative semi-definite to generate a stable SMC control law. As a result, asymptotic stability of the overall system may be achieved.*

It should be noted that the disturbance is regarded as a slowly varying quantity, so that the derivative of the actual disturbance is considered to be zero, $\dot{r} = 0$. Then, the derivative of the Lyapunov energy function is obtained from Equation (A31) and computed as:

$$\dot{V} = \frac{1}{2} [\dot{S}^{T} S + S^{T} \dot{S} + (\dot{r} - \dot{\hat{r}})^{T} (M\rho)^{-1} (r - \hat{r}) + (r - \hat{r})^{T} (M\rho)^{-1} (\dot{r} - \dot{\hat{r}})]$$

$$= S^{T} \dot{S} - (r - \hat{r})^{T} (M\rho)^{-1} \dot{\hat{r}}$$
(A32)

According to Equations (15) and (16), Equation (A32) can be expressed as

$$\dot{V} = S^T [\lambda F \dot{I}_{ref} - \lambda F A^{-1} (u - Bx)] + S^T \lambda F A^{-1} \hat{r} + S^T (\lambda F - \beta^T) A^{-1} (r - \hat{r})$$
(A33)

In order to meet the requirement of $\dot{V} < 0$, the matrix β should be designed to satisfy the condition of $S^T(\lambda F - \beta^T)A^{-1}(r - \hat{r}) < 0$, which makes the disturbance observable. Next, to ensure the system asymptotically converges (\dot{V} satisfies the condition of negative semi-definite). Then the derivative of the Lyapunov energy function is designed as follows:

$$\dot{V} = S^T [\lambda F \dot{I}_{ref} - \lambda F A^{-1} (u - Bx)] + S^T \lambda F A^{-1} \hat{r} = -S^T K S$$
(A34)

Substituting Equation (16) into Equation (A34), the SMC control law *u* is obtained, which is given in Equation (18).

Remark 3. The \dot{V} should satisfy the condition of being uniformly continuous in time, or \ddot{V} should satisfy the condition of being bounded.

According to Equation (A34), the differential of V is given as

$$\ddot{V} = -2S^T \dot{KS} \tag{A35}$$

In order to satisfy the boundary condition, the *S* and *S* should be bounded. Next, let Equations (13) and (15) be substituted into Equation (A34), then

$$\dot{S} + KS = \lambda F A^{-1} (r - \hat{r}) \tag{A36}$$

From Equation (A36), it is shown that the sliding surface *S* and differential of S are bounded, since the term of $(r - \hat{r})$ is abounded. Therefore, it is proved that \ddot{V} satisfies the condition of being

bounded, since *S* and *S* are bounded. So V satisfies the condition of being uniformly continuous in time.

According to Barbalat's lemma [46], when three remarks are satisfied, it ensures that all the errors can converge to the sliding surface. Finally, it is proved that the errors e_1 , e_2 and α can converge to a zero point and the designed controller is stable.

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