



Article DLI-Based DP-QPSK Reception Scheme for Short-Range Optical Communication

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Abstract: The unacceptably high cost of digital coherent receivers for short-range optical communication has in recent years fueled the study of alternative transmission schemes that are simultaneously highly performing and cost-effective. However, the proposed solutions (e.g., Kramers–Kronig receiver) usually require the cooperation of a transmitting end, which is unachievable in the context of non-cooperative communication. In this work, we mainly studied a dual-polarization quadrature phase-shift-keying (DP-QPSK) non-coherent reception scheme based on a delay-line interferometer (DLI) and measures to improve the reception performance. A data recovery algorithm was proposed for DLI-based DP-QPSK demodulation. The simulation results demonstrated that the algorithm could accurately recover the raw data from the transmitter. Moreover, decreasing the differential delay of the DLI could effectively increase the chromatic dispersion tolerance of the receiver and the optimal delay scaled inversely with the chromatic dispersion. It was found that a DLI-based DP-QPSK reception scheme is a better choice for short-range, non-cooperative communication with less severe transmission impairments.

Keywords: dual-polarization quadrature phase-shift keying; non-coherent detection; delay line interferometer; recovery algorithm; free spectral range optimization

1. Introduction

Digital coherent optical transmission is currently the mainstream scheme used for medium-to-long-distance optical communication, where it is optimal from the standpoint of spectral efficiency as it allows for the encoding of information in both quadrants and polarization of the electric field [1,2]. However, the cost of a coherent receiver, which requires highly customized hardware and complex compensation algorithms, is unacceptably high for short-range links, such as intra-datacenter interconnections [3–5].

In recent years, the requirement for alternatives to digital coherent transmission in short-reach applications has spawned a series of studies on direct detection (DD) systems due to their low cost, high power efficiency, and easy implementation [6–19]. Some schemes have been proposed in the published literature. The earliest is the on–off keying (OOK) scheme for low-speed communications [6], which is also the simplest but has high bandwidth and fiber dispersion matching requirements. To improve the spectral efficiency, high-order modulation formats are adopted. The most common is the pulse-amplitude modulation (PAM) scheme, which encodes information at multiple amplitude levels, such as PAM-4 [7] and PAM-8 [8]. However, the reception sensitivity will be significantly reduced as the level increases. Two other popular approaches are carrier-less amplitude and phase (CAP) modulation and discrete multi-tone (DMT) modulation [9–11]. CAP is relatively simple to implement in real time, while DMT offers lower computational complexity. However, both of them have a low tolerance for linear propagation effects. To this end, a self-coherent heterodyne scheme was proposed

by Lowery and Armstrong [12,13], which requires transmitting a frequency offset local oscillator (LO), together with the signal, thereby allowing for a complete reconstruction of the complex-valued field. More recently, some other schemes that are capable of full-field reconstruction, including Stokes vector direct detection (SV-DD) [14,15], single-sideband modulation/direct-detection (SSB-DD) [16,17], and Kramers–Kronig (KK) receivers [18,19], have been proposed. It is noteworthy that these schemes all require an auxiliary signal from the transmitter to enable full-field reconstruction at the receiver.

In this study, a dual-polarization quadrature phase-shift keying modulation/direct detection (DP-QPSK-DD) scheme for short-range optical communications was investigated. Compared with traditional binary modulation formats (OOK, BPSK (Binary Phase Shift Keying), etc.), DP-QPSK is modulated in multiple dimensions of polarization, phase, and amplitude at the same time, with greater degrees of freedom and lower complexity in each dimension, which gives it the advantage of a narrower spectral width, higher spectral efficiency, and higher tolerance to various fiber-based transmission impairments [20]. Hence, DP-QPSK is one of the mainstream modulation formats in the current 100 Gbps optical communication systems and is one of the solutions used for 400 Gbps optical transmission [21,22]. The DP-DQPSK-DD scheme is implemented based on a delay-line interferometer (DLI), which has simple and low-cost hardware implementation and is an ideal alternative to digital coherent transmission for short-range, non-cooperative communication. We propose an algorithm for recovering the raw data from the transmitting end. Based on this, we also studied measures used to improve the performance of the DP-QPSK reception and it was found that appropriately decreasing the free spectral range (FSR) of the DLI could increase the tolerance to transmission impairments to a certain extent.

The remainder of this paper is organized as follows. Section 2 briefly introduces the structure and basic principle of the DLI-based DP-QPSK-DD system. The data recovery algorithm is proposed in Section 3. In Section 4, the impact of FSR adjustment is analyzed theoretically. Simulations are conducted in Section 5 to verify the feasibility of the algorithm and the effect of decreasing the FSR of the DLI. Finally, conclusions are given in Section 6.

2. Structure and Basic Principle

DP-QPSK is most commonly modulated at the transmitter through electro-optic modulators based on a Mach–Zehnder interferometer structure, which was first proposed by Zehnder in 1891 and improved by Mach in 1892 [23,24]. Figure 1 gives the detailed structure of the DP-QPSK modulator and shows how it works.



Figure 1. A diagram of the detailed structure of the dual-polarization quadrature phase-shift keying (DP-QPSK) modulator and how it works. DP-MZM: dual-parallel Mach–Zehnder modulator. $\exp(j\beta L)$ is the phase shift due to the electrode length. The baseband signal to be modulated is applied to the electrode of the MZM. Each MZM is biased at the minimum transmission point and each DP-MZM is biased on one arm to introduce a 90° phase shift.

A linearly polarized continuous-wave (CW) lightwave emitted from a laser diode (LD) is injected into a DP-QPSK modulator [25]. It was assumed that the optical signal at the input of the modulator was:

$$E_{LD}(t) = \sqrt{P_t} \exp[j(\omega_c t + \varphi)], \qquad (1)$$

where P_t is the optical power, ω_c is the optical frequency, and φ is the initial phase.

A DP-QPSK modulator mainly consists of four Mach–Zehnder modulators (MZM) and a 90° polarization rotator. Every two MZMs are connected in parallel or in series to form a DP-MZM. The transfer equation of an MZM biased at the minimum transmission point is [25,26]:

$$E_{MZM}(t) = jE_{in}(t) \, \exp(j\beta L) \cos[\pi \frac{V_1(t) - V_2(t)}{2V_{\pi}}],\tag{2}$$

where $E_{in}(t)$ is the input signal into the MZM, β is the modulation coefficient of the MZM, L is the length of the electrode, V_{π} is the half-wave voltage of the MZM, and $V_1(t)$ and $V_2(t)$ are the drive voltages of the MZM. In Figure 1, $\varphi_{I,Q} = \pi \frac{V_1(t) - V_2(t)}{2V_{\pi}}$, which is the baseband signal to be modulated. Each DP-MZM is biased on one arm to introduce a 90° phase shift. Hence, the output of the DP-MZM is:

$$E(t) = jE_{LD}(t)\exp(j\beta L)[\cos(\varphi_{I}t) + \cos(\varphi_{Q}t)\exp(j\pi/2)]$$

$$\triangleq jE_{LD}(t)\exp(j\beta L)\exp(j\Phi),$$
(3)

where Φ is the modulated phase. Then, one of the DP-MZM output signals is applied to a polarizer and the two DP-MZM outputs give the two orthogonal polarization states. The output DP-QPSK signal can be written as follows:

$$E_{s}(t) = \begin{bmatrix} jE_{LD}(t) \exp(j\beta L) \exp(j\Phi_{\parallel}) \\ jE_{LD}(t) \exp(j\beta L) \exp(j\Phi_{\perp}) \end{bmatrix}.$$
(4)

The optical signal from the transmitter is transmitted through a fiber-optic link and reaches the receiver for demodulation. The structure of a DLI-based DP-QPSK receiver is schematically shown in Figure 2. A DP-QPSK demodulator is mainly composed of two DP-DLIs and four pairs of balanced photodiodes. Each DP-DLI consists of two Mach–Zehnder DLIs and every DLI is traditionally tuned with an exact one-symbol-period differential delay on one arm and a $+45^{\circ}$ (-45°) phase shift on the other, which is the most efficient configuration in back-to-back systems [27,28].



Figure 2. A schematic diagram of the structure of a delay-line interferometer (DLI)-based DP-QPSK receiver. OS: optical signal; DPC: dynamic polarization alignment controller; PBS: polarization beam splitter; LPF: low-pass filter; CDR: clock data recovery.

The input optical signal is separated through a dynamic polarization alignment controller (DPC) and a polarization beam splitter (PBS) into two signals with mutually orthogonal polarization states, realizing polarization demultiplexing. The separated single-polarized signals are then injected into a DP-DLI each, which converts the phase-difference information into intensity information in the optical domain. Finally, signals containing the intensity information are directly detected through balanced photodiodes and high-frequency noise is eliminated after filtering. The raw data is recovered using subsequent devices. It is noteworthy that the LPF used here is a manifestation of the low-pass characteristics of the hardware, which result from the limitations of the hardware itself. For example, limitations in the frequency response of the photodiodes prevent high-frequency components from passing through it.

Assuming that the responsibilities of photodiodes are the same, all being equal to α , the differential delay in the time domain is τ , and the signal at the input of DP-DLI is $E(t) = A \exp[j(\varphi(t) + \varphi_n)]$, where A is the amplitude, $\varphi(t)$ is the carrier phase, and φ_n is the signal phase. The optical signal at the output of the DP-DLI can be expressed as:

$$E_{1u,2u,1v,2v} = \frac{j}{2} [E(t)e^{\pm j\frac{\pi}{4}} \pm E(t-\tau)].$$
(5)

According to the square-law characteristic of photodiodes, the converted photocurrent is (taking $E_{1u}(t)$ as an example here):

$$i_{PD}(t) = \frac{\sqrt{2}\alpha A^2}{4} (\sqrt{2} + \cos\Delta\varphi_n - \sin\Delta\varphi_n).$$
(6)

After the balanced photodetectors, the two electrical signals can be written as:

$$i_u(t) = \frac{\sqrt{2}}{2} \alpha A^2 (\cos \Delta \varphi_n - \sin \Delta \varphi_n),$$

$$i_v(t) = \frac{\sqrt{2}}{2} \alpha A^2 (\cos \Delta \varphi_n + \sin \Delta \varphi_n).$$
(7)

where $\Delta \varphi_n$ is the phase difference between adjacent symbols.

3. The Data Recovery Algorithm

In this section, the data recovery algorithm for DLI-based DP-QPSK reception is presented. Considering the limitations of the algorithm, assumptions were made first. Signals are usually transmitted in the form of frames in the channel and frame synchronization should be done first at the receiver before subsequent processing. There are two main types of frame synchronization techniques: one is data-aided frame synchronization, in which a predetermined synchronization word is periodically inserted in a frame, and the other is code-property-aided frame synchronization, in which the data is encoded with an error correction encoder. For the former, synchronization is attained through recognizing the synchronization word, while for the latter, synchronization is attained through exploiting the properties of the code. In this study, we only considered the data-aided frame synchronization code was periodically inserted in the raw data before modulation for the receiver to obtain data synchronization. Moreover, the payload in the raw data was random. Based on these two assumptions, the data recovery algorithm was proposed as follows.

DP-QPSK modulation lacks the process of differential precoding, while the output of a DLI-based demodulator is the information of phase differences between adjacent symbols. Hence, further data processing is required to recover the raw data.

Figure 3 shows the conversion of data flow in a single polarization state in the DLI-based DP-QPSK transmission system. The raw bitstream is modulated to a QPSK signal by the MZM at the transmitting end and the QPSK signal is processed through a DLI and CDR into a demodulated bitstream at the

receiving end. It is a reverse engineering problem to recover the raw data from the demodulated bitstream and a three-step inverse mapping is required.



Figure 3. The conversion diagram of the signal flow. The phases of the QPSK signal correspond to the dual-bit symbols of a raw bitstream after modulation. The output of the DLI is the phase difference between adjacent symbols of the QPSK signal. The dual-bit symbols of a demodulated bitstream correspond to the differential phases of a demodulated signal.

• Inverse mapping 1 from the demodulated bitstream to the demodulated signal: From Equation (6), we can find the correspondence between them, which is shown in Table 1. Therefore, the inverse mapping can be completed by just referring to Table 1.

Table 1. Correspondence between the phase difference and the demodulated symbols.

$\Delta \varphi_n$	0	π/2	π	3π/2
(i_u, i_v) IQ	$(\sqrt{2}/2,\sqrt{2}/2)$	$(-\sqrt{2}/2,\sqrt{2}/2)$	$(-\sqrt{2}/2, -\sqrt{2}/2)$	$(\sqrt{2}/2, -\sqrt{2}/2)$
	11	01	00	10

 Inverse mapping 2 from the demodulated signal to the QPSK signal: The demodulated signal is the phase difference information and the QPSK signal is the phase information, which means the initial phase φ₁ is required to complete the inverse mapping. QPSK characterizes different dual-bit symbols with four different phases in two modes, namely, mode A and mode B, corresponding to the parallel modulation and series modulation, respectively. The correspondence is shown in Table 2. Therefore, there are eight possible values for φ₁ and the eight cases need to be verified one by one in the next step.

Dual-Bit Symbols	00	01	11	10
Phases of Mode A	0	$\pi/2$	π	$3\pi/2$
Phases of Mode B	$\pi/4$	$3\pi/4$	$5\pi/4$	$7\pi/4$

Table 2. Coded relationship between the input dual-bit symbols and the modulated phases.

• Inverse mapping 3 from the QPSK signal to the raw bitstream: The inverse mapping of this step can be completed by referring to Table 2, while for the eight cases caused due to the unknown initial phase, we need to search every bitstream for the frame synchronization code, which only appears in the raw data.

Based on the three-step inverse mapping above, the data recovery algorithm can be summarized as follows.

Algorithm 1 Recover the raw data from the demodulated bitstream

Notations: *B*, *S*, *S'*, *D*, *D'* and θ are all row vectors. *N* is the length of *S*, $S = (s_i)_{N'}$ and $S' = (s'_i)_{N+1}$. $f_1(X)$ is the inverse mapping 1 (see Table 1) and $f_2(X)$ is inverse mapping 3 (see Table 2). $\theta = (\vartheta_i)_8$ is the initial phase storage matrix.

Assumption: The length of *B* is 2*N*.

Input: The demodulated bitstream storage matrix *B*.

```
Output: The raw data D.
1: Initialization, parameter setting: i = 0, j = 0.
2: S = f_1(B);
3: while i < 8 do
     i = i + 1;
     s_1 = \vartheta_i
     while j < N do
       j = j +1;
       s_{j+1} = s_j + s_j;
     end
     D' = f_2(S');
     Search D^{'} for the frame synchronization code;
     if a fixed code exists in D' then
       D = D';
     end
end
4: return D
```

4. FSR Optimization

Traditionally, DLI is configured with an exactly one-symbol-period differential delay on one arm and a 45° (-45°) phase shift on the other, which means the FSR of the DLI is equal to the transmission symbol rate. It has already been shown through simulations that decreasing the differential delay led to increased chromatic dispersion tolerances in DLI-based BPSK demodulation [28–30]. This study first qualitatively analyzed the effect of FSR adjustment on DP-QPSK demodulation performance through a theoretical derivation. The two cases of FSR increasing and decreasing are shown in Figure 4.



Figure 4. Two cases of free spectral range (FSR) adjustment. R_{sym} is the symbol rate, t_1 is the period during which a single symbol interferes with itself or the next second one, and t_2 is the period during which a single symbol interferes with its adjacent symbol.

• $T < 1/R_{sym}$

When decreasing the differential delay of the DLI to less than one symbol period, part of a symbol interferes with itself. The signals before the photodetectors can be expressed as:

$$E_{1u,2u,1v,2v} = \begin{cases} \frac{A}{2} e^{j\varphi(t)} [e^{j(\varphi_n \pm \frac{\pi}{4})} \pm e^{j\varphi_n}], & in t_1, \\ \frac{A}{2} e^{j\varphi(t)} [e^{j(\varphi_{n+1} \pm \frac{\pi}{4})} \pm e^{j\varphi_n}], & in t_2. \end{cases}$$
(8)

The converted photocurrent is (taking $E_{1u}(t)$ as an example here):

$$i_{PD}(t) = \frac{\sqrt{2}\alpha A^2}{4} (\sqrt{2} + \cos\Delta\varphi_n - \sin\Delta\varphi_n) + \frac{\sqrt{2}\alpha A^2}{4} (1 - \cos\Delta\varphi_n + \sin\Delta\varphi_n) t_1 R_{sym}.$$
 (9)

After the balanced photodetectors, the two electrical signals can be written as (the DC component is ignored here):

$$i_{u}(t) = \frac{\sqrt{2}}{2} \alpha A^{2} (\cos \Delta \varphi_{n} - \sin \Delta \varphi_{n}) t_{2} R_{sym},$$

$$i_{v}(t) = \frac{\sqrt{2}}{2} \alpha A^{2} (\cos \Delta \varphi_{n} + \sin \Delta \varphi_{n}) t_{2} R_{sym}.$$
(10)

• $T > 1/R_{sym}$

The situation of increasing the delay to greater than one symbol period is shown in Figure 4. The signals before the photodetectors can be expressed as:

$$E_{1u,2u,1v,2v} = \begin{cases} \frac{A}{2} e^{j\varphi(t)} [e^{j(\varphi_{n+2} \pm \frac{\pi}{4})} \pm e^{j\varphi_n}], & \text{in } t_1, \\ \frac{A}{2} e^{j\varphi(t)} [e^{j(\varphi_{n+1} \pm \frac{\pi}{4})} \pm e^{j\varphi_n}], & \text{in } t_2. \end{cases}$$
(11)

The converted photocurrent is (taking $E_{1u}(t)$ as an example here):

$$i_{PD}(t) = \frac{\sqrt{2}\alpha A^2}{4} (\sqrt{2} + \cos\Delta\varphi_n - \sin\Delta\varphi_n) + \frac{\sqrt{2}\alpha A^2}{2} [\cos(\Delta\varphi_{n+1} + \Delta\varphi_n + \frac{\pi}{4}) - \cos(\Delta\varphi_n + \frac{\pi}{4})] t_1 R_{sym}.$$
(12)

After the balanced photodetectors, the two electrical signals can be written as (the DC component is ignored here):

$$i_u(t) = \alpha A^2 [\cos(\Delta\varphi_n + \frac{\pi}{4})t_2 + \cos(\Delta\varphi_{n+1} + \Delta\varphi_n + \frac{\pi}{4})t_1] R_{sym},$$

$$i_v(t) = \alpha A^2 [\cos(\Delta\varphi_n - \frac{\pi}{4})t_2 + \cos(\Delta\varphi_{n+1} + \Delta\varphi_n - \frac{\pi}{4})t_1] R_{sym}.$$
(13)

It should be noted that the above formulas were derived without considering the noise. Since $i_u(t)$ and $i_v(t)$ obtained using Equations (7), (10), and (13) are subsequently compared to the set threshold for a 0–1 judgment, the impact of noise cannot be ignored. Figure 5 gives a schematic diagram of the 0–1 judgment and the noise was introduced. It can be seen from Figure 5 that the smaller $|i_{u,v}(t)|$ was, the greater the probability of misjudging will be in the presence of the noise.



Figure 5. Schematic diagram of the 0–1 judgment. δ_1 and δ_0 here are the set thresholds for the 0–1 judgment. If $i_{u,v}(t) > \delta_1$, then $i_{u,v}(t)$ is judged to be 1. If $i_{u,v}(t) < \delta_0$, then $i_{u,v}(t)$ is judged to be 0.

Based on the formula derivations for the three situations, a qualitative analysis could be conducted. Since the possible set of values for the phase difference $\Delta \varphi_n$ was $\{0, \frac{\pi}{2}, \pi, \frac{3\pi}{2}\}$, according to Equations (6) and (9), the photodetector output of case 1 was larger than that of the situation with an exactly one-symbol delay, which created a deterministic constructive interference for every symbol period. The deterministic constructive interference could suppress the inter-symbol crosstalk that resulted from transmission impairments to a certain extent. It can also be known from Equation (10) and Figure 5 that the decrease of t_2 will increase the error rate of the final judgment. Hence, decreasing the differential delay can both increase the tolerance of transmission impairments and increase the BER for receivers. As for case 2, it can be seen from Equations (12) and (13) that the photodetector output did not show any advantage over the situation with an exactly one-symbol delay due to the presence of $\Delta \varphi_{n+1}$. Therefore, case 2 was not taken into consideration in the following simulation.

5. Simulations

Simulations were conducted for a 112 Gbps DP-QPSK signal with a laser at 193.1 THz, modulated on two orthogonal polarization states, and the two sub-MZMs with an extinction ratio of 60 dB in each DP-MZM were configured in parallel. The receiver consisted of a 70 GHz third-order Gaussian optical filter, two tunable DP-DLIs followed by four pairs of balanced photodetectors, and four 21 GHz fourth-order Bessel electrical filters. The simulations were carried out in Optisystem 15 from company Optiwave in Ottawa, Ontaria, Canada.

5.1. Data Recovery Algorithm

In the algorithm simulation experiment, a string of a pseudo-random binary sequence (PRBS) is generated first and is divided into a series of frames with 1024-bit lengths. A fixed sequence 0xf628 is inserted between every two frames as the frame synchronization code. The new sequence is then applied to DP-MZM as the raw signal for modulation.

In fact, the searching algorithm plays an important role in our data recovery algorithm. This study applied a frame parameter recognition algorithm based on self-correlation to search for the frame synchronization code, and the recognition accuracy rate is claimed to be very high when bit error rate (BER) is less than 0.02 [31,32]. In this algorithm, the frame length is searched first within a given

range based on self-correlation and a peak-to-average ratio, and thresholds are set to obtain a credible frame length estimation value. Then, the synchronization code is recognized and thresholds are set to distinguish between 0 and 1. To simplify the simulation, we assumed that the frame length was known to the receiving end. If a clear correlation peak appears, which means a fixed frame synchronization code existed in the bitstream, the sequence is considered as the raw data. Otherwise, it is not. Figure 6 gives the frame synchronization code recognition result of one simulation. The upper and lower thresholds in Figure 6a were set to determine the frame length, and the thresholds in Figure 6b were set to recognize the synchronization code. The bitstream with a clear correlation peak appearing was highly consistent with the raw data. Given that bit errors existed during demodulation, we considered the bitstreams with a correlation peak as the raw data.



Figure 6. Result of the frame synchronization code recognition: (**a**) no peak appeared if there was no frame synchronization code, and (**b**) a clear peak appeared if there was a frame synchronization code.

5.2. FSR Optimization

First, we compared the performances of the receivers with the DLI in different differential delays in a back-to-back configuration and the results are presented in Figure 7.



Figure 7. DP-QPSK receiver performance with different delays in a back-to-back configuration: (**a**) Q factor versus DLI differential delay under a given optical signal-to-noise ratio (OSNR) of 15 dB, (**b**) the OSNR required for receivers with different DLI delays at the FEC (Forward Error Correction) threshold of BER = 3.8×10^{-3} .

The receiver's BER versus the DLI differential delay under the same optical signal-to-noise ratio (OSNR) is shown in Figure 7a and the given OSNR here is 15 dB. The Q parameter was calculated from

BER using the formula BER $=\frac{1}{2}erfc(\frac{Q}{2})$. Figure 7b shows the OSNR required for receivers with different DLI delays at the FEC threshold of BER = 3.8×10^{-3} . The simulation results were consistent with the theoretical derivation above. Eye diagrams of a one-symbol-period delay and a 0.55-symbol-period delay are given in Figure 7a and a serious eye closure was created when the differential delay of DLI was a 0.55-symbol period. From Figure 7b, we can see that the OSNR penalty increased almost exponentially with the decrease of the differential delay.

The chromatic dispersion tolerance of the receivers with different DLI delays were measured and the results are shown in Figure 8.



Figure 8. Measured OSNR penalty versus the chromatic dispersion at BER = 3.8×10^{-3} : (a) the dispersion tolerance due to the 2 dB OSNR penalty of the receiver, and (b) the OSNR penalty required for receivers with different delays to maintain a BER of 3.8×10^{-3} under the same system dispersion.

In the simulations, the result of the back-to-back configuration with an exactly one-symbol-period delay was used as a zero point for the OSNR penalty. We recorded the OSNR penalty of different differential delays at BER = 3.8×10^{-3} while gradually increasing the system's chromatic dispersion. The intersections of the curves and the *y*-axis in Figure 8 reflect the OSNR penalty of simply decreasing the differential delay without introducing dispersion. We can see that the OSNR penalty for receivers with a 0.9-symbol-period delay, a 0.8-symbol-period delay, and a 0.7-symbol-period delay were 0.3 dB, 0.9 dB, and 2.2 dB, respectively.

In Figure 8a, the points marked on the curves indicate the dispersion tolerance due to the 2 dB OSNR penalty of the receivers. The 2 dB chromatic dispersion tolerances were 70.8 ps/nm, 74 ps/nm, 81.2 ps/nm, and 84.5 ps/nm for receivers with a 1-symbol-period delay, a 0.9-symbol-period delay, a 0.8-symbol-period delay, and a 0.7-symbol-period delay, respectively. It can be seen that the 2 dB dispersion tolerance was increased by nearly 20% when the DLI differential delay was a 0.7-symbol period. The OSNR penalty required for receivers with different delays to maintain a BER of 3.8×10^{-3} under the same system dispersion are compared in Figure 8b. The results suggest that by considering the OSNR penalty introduced by only decreasing the differential delay, the optimal delay for a receiver with a system chromatic dispersion of 50 ps/nm was one symbol period, while the optimal delay was a 0.8-symbol period for a receiver with a dispersion of 95 ps/nm. It can be drawn from Figure 8b that the optimal delay of the DLI was inversely proportional to the chromatic dispersion, which is consistent with the previously reported results on BPSK demodulation [15].

6. Conclusions

In this study, the structure and basic principles of a DLI-based DP-QPSK transmission system were briefly introduced. A data recovery algorithm was proposed for a receiver based on DLI that could recover raw data due to the lack of a differential precoding process in the DP-QPSK modulation.

Measures to improve the demodulation performance were further studied and it was shown through theoretical derivation that appropriately increasing the FSR of DLI could achieve the goal. The simulation results showed that the algorithm was effective for recovering the raw data in a DLI-based DP-QPSK receiver and the 2 dB dispersion tolerance was increased by nearly 20% when decreasing the delay to a 0.7-symbol period under the simulation configuration set in this study. Hence, the DLI-based receiving scheme for a DP-QPSK is an ideal solution for short-range, non-cooperative communication. It is noteworthy that the performance of the data recovery algorithm depends to a certain extent on the frame synchronization algorithm, which will need to be optimized in our future work.

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