

Article

# CuO and CuO/Graphene Nanostructured Thin Films as Counter Electrodes for Pt-Free Dye-Sensitized Solar Cells

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**Abstract:** Copper oxide (CuO) and CuO/graphene nanostructured thin films were used as counter electrodes (CEs) for dye-sensitized solar cells (DSSCs). CuO and CuO/graphene pastes were prepared and coated on fluorine-doped tin oxide (FTO) glass substrates using a doctor-blade coating method. The substrates were then sintered at 350 °C for 30 min to form CuO and CuO/graphene nanostructures. The material properties of the CuO and CuO/graphene CEs were analyzed using a scanning electron microscope, transmission electron microscope, energy-dispersive spectrometer, thermogravimetric analysis instrument, X-ray diffractometer, Raman spectroscopy, X-ray photoelectron spectrometer, ultraviolet-visible spectrophotometer, and cyclic voltammetry instrument. The CuO and CuO/graphene CEs were used to fabricate DSSCs, and the device characteristics were analyzed using current density–voltage, incident photo-to-current conversion efficiency, and electrochemical impedance spectroscopy measurements. The results showed that when CuO and CuO/graphene were used as the CEs, the device conversion efficiencies were 2.73% and 3.40%, respectively. CuO is a favorable replacement for expensive platinum (Pt) because it features a simple fabrication process and is inexpensive and abundant. Furthermore, graphene, which exhibits high carrier mobility, may be added to enhance the electrical and catalytic abilities of CuO/graphene CEs. This is the first study to examine the use of CuO and CuO/graphene for developing Pt-free CEs in DSSCs.

**Keywords:** dye-sensitized solar cells; CuO; graphene; counter electrode; nanostructures

## 1. Introduction

Energy shortages and environmental pollution are two major problems for humans this century. Considerable research and development of alternative and renewable energies as substitutes for conventional energy sources has been conducted in recent years. Of the renewable energy sources, solar energy is one of the most advantageous because it is inexhaustible and is environmentally friendly [1]. In 1991, Grätzel et al. [2] developed a new type of solar cell, namely the dye-sensitized solar cell (DSSC), based on the high-surface-area TiO<sub>2</sub> nanoparticles. DSSCs feature the advantages of high efficiency, low cost, and simple fabrication, which make them a strong potential competitor for conventional silicon-based solar cells [3–12]. Structurally, DSSCs contain TiO<sub>2</sub>, dyes, and electrolytes in addition to platinum (Pt) counter electrodes (CEs), which play the critical role of catalyzing iodide ions in redox reactions [13]. However, Pt CEs are expensive; hence, seeking an alternative to such electrodes to reduce fabrication costs has become a prominent research topic [14,15]. According to the literature, materials that can be used for making DSSC CEs include graphite, carbon nanotubes, graphene, conductive polymers, and metal oxide semiconductors [16–23].

Copper oxide (CuO) is a slightly water-absorbing black oxide of copper with a relative molecular mass of 79.545, a density of 6.3–6.9 g·cm<sup>-3</sup>, and a melting point of 1326 °C. In addition, CuO is a *p*-type semiconductor having an energy gap width of approximately 1.2 eV. CuO is a secondary copper mineral, which is the most stable form of oxidized copper. It has a monoclinic structure and many remarkable properties such as superconductivity, catalytic activity, optoelectronic properties, high stability, and antibacterial activity. CuO can be used to fabricate various high-performance electronic and optoelectronic devices such as high-temperature superconductors, gas detectors, and giant magnetoresistive devices [24,25]. CuO can be fabricated using numerous methods including sonochemical, microwave irradiation, hydrothermal, precursor thermal decomposition, and electrochemical approaches [26–28]. Since CuO fabrication is easy, the number of studies using CuO to fabricate solar cells has increased in recent years. Habibi et al. fabricated DSSC working electrodes by depositing CuO–ZnO thin films using a hydrothermal process and combined cobalt electrolytes with Pt CEs; their device demonstrated an efficiency of 0.01%–0.03%, which, although inadequate, showed considerable potential for improvement [29].

Graphene, a new carbon material, has been extensively applied in recent years. Graphene has become the focus of recent research because of its unique properties such as high mechanical strength (1100 GPa), high thermal conductivity (5000 W·m<sup>-1</sup>·K<sup>-1</sup>), and high carrier mobility (200,000 cm<sup>2</sup>·V<sup>-1</sup>·s<sup>-1</sup>) [30,31]. Some researchers have recently applied graphene to the preparation of DSSC devices [32–34]. Zhang et al. [35] scattered graphene nanosheets (GNs) in a terpeneol and ethyl cellulose ethoxy solution, coated the GNs on the surface of fluorine-doped tin oxide (FTO) glass plates, and examined the effect of annealing temperature on the performance of DSSC devices. The results showed that the GN CE obtained at an annealing temperature of 450 °C achieved the highest conversion efficiency of 2.94% for DSSCs. Roy-Mayhew et al. [36] used functionalized graphene sheets as DSSC CEs. Using cyclic voltammetry (CV) measurements, they found that the addition of oxygen-containing functional groups increase the surface catalytic activity of graphene.

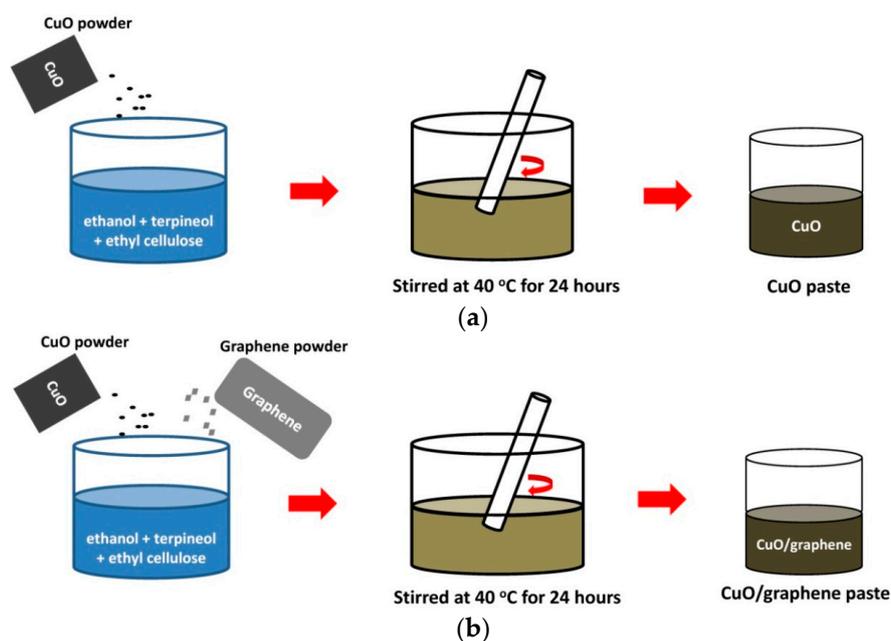
CuO has been extensively investigated for photovoltaic and photoelectrochemical proton reduction applications. Two valuable advantages of using CuO are that it is nontoxic and its constituents are abundantly available. The work function of CuO is approximately 5.3 eV, which is appropriate for use as a CE in DSSCs. In this study, CuO and CuO/graphene nanostructures were fabricated as CEs for DSSCs. The electrode properties of CuO and CuO/graphene CEs were comprehensively analyzed, and the effects of CuO and CuO/graphene CEs on the performance of DSSCs were investigated.

## 2. Experimental

### 2.1. Preparation of the CuO and CuO/Graphene Pastes

CuO and CuO/graphene pastes were prepared to fabricate CuO and CuO/graphene CEs. Figure 1 shows the process of preparing the CuO and CuO/graphene pastes. The CuO nanopowders/nanoparticles (CuO, 99%, 80 nm in diameter) were purchased from a commercial source (US Research Nanomaterials, Inc., Houston, TX, USA). First, to prepare an ethyl cellulose solution, 10 wt % ethyl cellulose powder was added to 90 wt % ethanol and stirred at room temperature for 48 h using a magnetic stirrer. Next, 20 wt % CuO nanoparticles (approximately 80 nm in diameter), 70 wt % terpeneol, and 10 wt % ethanol were added to the ethyl cellulose solution; the mixture was stirred at 40 °C for 24 h using a magnetic stirrer. Subsequently, the paste bottle was left standing for the remaining ethanol to volatilize, and a viscous CuO paste was formed. Concurrently, a CuO/graphene paste was prepared to fabricate the CuO/graphene electrodes. The GNs (graphene P-MF10) were purchased from a commercial source (Energe Inc., Yilan County, Taiwan). To prepare the CuO/graphene paste, an ethyl cellulose solution with 10 wt % ethyl cellulose powder was supplemented with 18 wt % CuO nanoparticles (approximately 80 nm in diameter), 2 wt % GNs, 70 wt % terpeneol, and 10 wt % ethanol; similarly, the mixture was stirred at 40 °C for 24 h using a magnetic stirrer. Finally, the paste bottle was left

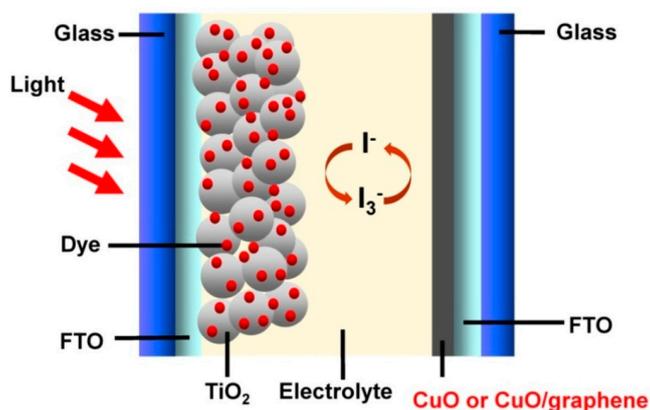
standing for the remaining ethanol to volatilize, thereby forming a viscous CuO/graphene paste. The GO/graphene paste had a CuO-to-graphene weight ratio of 9:1.



**Figure 1.** Preparation processes of CuO and CuO/graphene pastes.

## 2.2. Preparation of the Counter Electrodes

Figure 2 shows the schematic device structure of a DSSC fabricated using a CuO or CuO/graphene CE. To fabricate CEs, the following method was adopted: First, FTO transparent conductive glass substrates were prepared. Two holes were drilled on the transparent conductive glass substrates to be used for injecting electrolytes. Subsequently, the glass substrates were placed in acetone and washed for 10 min using an ultrasonic cleaner. Next, deionized water was added to the cleaner to wash the glass substrates for another 10 min. Ethanol was then added to the cleaner to wash the glass substrates for 10 min. Finally, deionized water was added to the cleaner again to wash the glass substrates for another 10 min, completing the cleaning process of the FTO glass substrates.



**Figure 2.** The schematic device structure of a DSSC using the CuO or CuO/graphene CEs.

The cleaned glass substrates with holes were dried using a nitrogen air gun. The two sides of the glass substrates were taped with a transparent tape, which was evenly pressed to ensure no air bubbles were trapped and to prevent uneven taping on the surface. Next, the CuO and CuO/graphene pastes

were coated on the glass substrates using a doctor-blade method, after which the glass substrates were placed inside a furnace to vacuumize them and produce a glass pressure of 7 mTorr. Next, nitrogen was injected into the tube furnace at a rate of  $300 \text{ cc}\cdot\text{min}^{-1}$ . The nitrogen bottle outlet pressure was maintained at a constant value while air and nitrogen were continuously sucked out and injected, separately, for 3 min, creating a nitrogen atmosphere in the tube furnace. Next, the air suction pump was turned off, whereas the nitrogen injection valve was left on until the furnace pressure rose to 300 Torr, after which the nitrogen injection valve was turned off. Finally, the glass substrates underwent high-temperature sintering to  $350 \text{ }^\circ\text{C}$  for 30 min in the nitrogen atmosphere and were left to anneal to room temperature, producing the CEs.

### 2.3. Analysis of Counter Electrode Characteristics

A complete analysis of various CEs was performed in this study. An electrode surface morphology analysis was conducted using a scanning electron microscope (SEM) (JEOL JSM-7000F, JEOL, Tokyo, Japan). The electrode morphology was observed using a transmission electron microscope (TEM) (JEOL JEM-1400, JEOL, Tokyo, Japan). In addition, an energy-dispersive spectrometer (EDS) (JEOL JSM-7000F) was used to analyze the CE element type and content. The composition of CuO/graphene CEs was investigated through a thermogravimetric analysis (TGA) instrument (SETSYS Evolution TGA-DTA/DSC, SETARAM, Inc., Cranbury, NJ, USA), and a material property analysis was completed using a Raman spectrometer. The crystal structure of various electrodes was also observed using an X-ray diffractometer (XRD) (Rigaku D/Max-2500V, Rigaku, Tokyo, Japan). Furthermore, an X-ray photoelectron spectrometer (XPS) (Thermo K-Alpha, Thermo Fisher Scientific, Waltham, MA, USA) was used to analyze the chemical element composition on the electrode surfaces. The transmittance of the electrodes was characterized using an ultraviolet-visible (UV-VIS) spectrophotometer (OPTIZEN POP UV/VIS spectrophotometer, Mecasys Co., Ltd., Daejeon, Korea), and the electrode catalytic activity was measured using a CV instrument (CH Instruments 6116D, CH Instruments, Inc., Austin, TX, USA).

### 2.4. Fabricating the Working Electrodes

The cleaned FTO transparent conductive glass substrates were dried using a nitrogen air gun. A piece of transparent tape was drilled with holes measuring  $0.126 \text{ cm}^2$  and taped onto the glass substrates. The holes were then blade coated with small  $\text{TiO}_2$  nanoparticles (approximately 25 nm in diameter, anatase phase) (Ti-Nanoxide T/SP, Solaronix SA, Aubonne, Switzerland). Next, the glass substrates were placed on a hot plate and baked at  $150 \text{ }^\circ\text{C}$  for 10 min before being left to cool at room temperature. Subsequently, the substrate was blade coated with large  $\text{TiO}_2$  nanoparticles (approximately 200 nm in diameter, anatase phase) (Ti-Nanoxide R/SP, Solaronix SA, Aubonne, Switzerland). The original tape was then removed, and the glass substrates were placed inside the furnace to sinter at  $500 \text{ }^\circ\text{C}$  for 30 min before being left to cool at room temperature. Finally, the glass substrates were soaked in a dye solution for 20 h, completing the fabrication of the working electrodes. The dye solution used in this experiment was made by dissolving 0.5 mM N719 dye and 0.5 mM chenodeoxycholic acid in acetonitrile and tert-butyl alcohol at a 1:1 ratio.

### 2.5. Fabricating the DSSCs

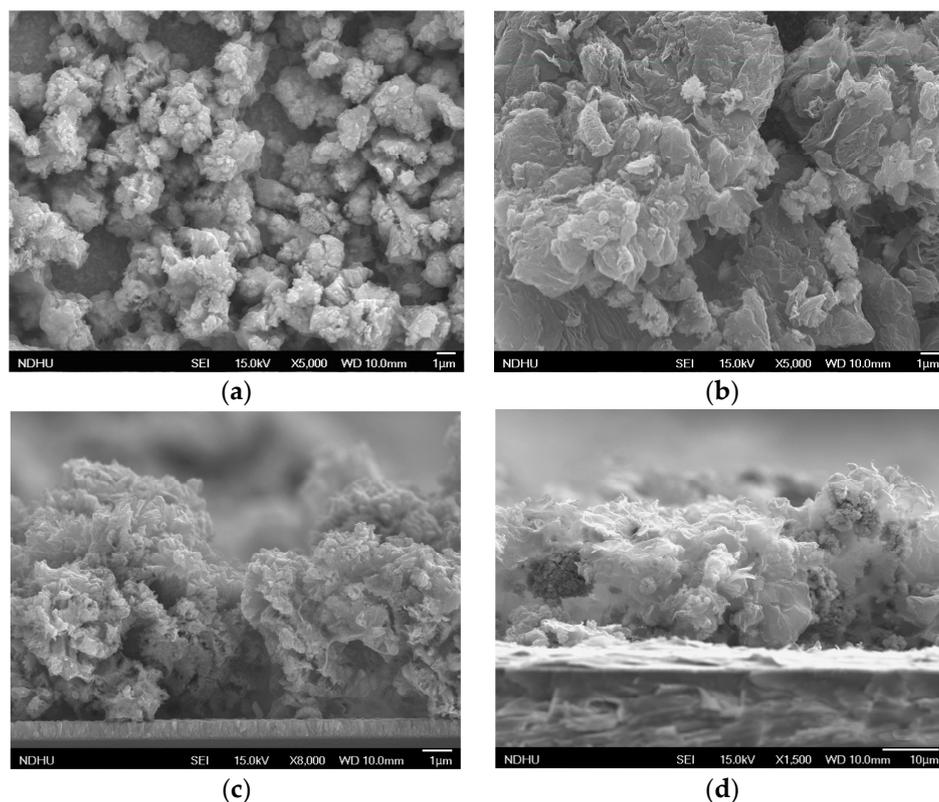
A  $60\text{-}\mu\text{m}$ -thick sealing foil was cut into a  $2.5 \text{ cm} \times 2.5 \text{ cm}$  square. Next, a  $0.8 \text{ cm} \times 0.8 \text{ cm}$  hole was made in the center of the sealing foil. The sealing foil was then placed between a working electrode and a CE, and the hole of the sealing foil was carefully adjusted before the whole arrangement was placed in an encapsulator and compressed at a temperature of  $130 \text{ }^\circ\text{C}$  and a pressure of  $3 \text{ kg}\cdot\text{cm}^{-2}$  for 3 min to completely seal the upper and lower electrodes. After the device was left to cool,  $5 \text{ }\mu\text{L}$  of electrolytic solution was injected into the device using a micropipette. The electrolytic solution consisted of 12.6 mg of  $\text{I}_2$ , 33.5 mg of LiI, 0.36 mL of 4-tert-butylpyridine, and 5 mL of acetonitrile as the electrolyte solvent. Next, the  $0.8 \text{ cm} \times 0.8 \text{ cm}$  sealing foil and a glass slide were used to quickly

seal the device at 130 °C for 10 s to prevent the electrolytic solution from leaking out and volatilizing. Finally, the surface of the glass was wiped clean with alcohol to complete the DSSC device fabrication. The characteristics of the DSSC devices were analyzed through current density–voltage ( $J$ – $V$ ), incident photo-to-current conversion efficiency (IPCE), and electrochemical impedance spectroscopy (EIS) measurements. The complete details of the measurements are described in a report of our previous work [23].

### 3. Results and Discussion

#### 3.1. Characterization of the CuO and CuO/Graphene CEs

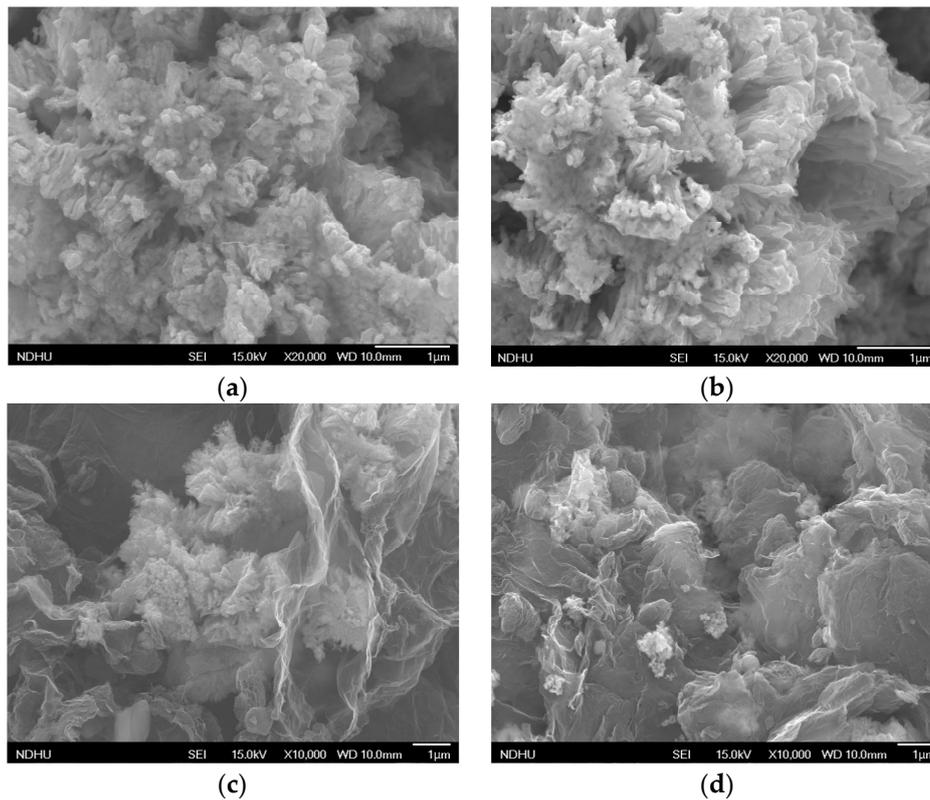
Figure 3 shows the top-view and cross-sectional SEM images of CuO and CuO/graphene CEs. As presented in Figure 3a, the CuO CE exhibited a granular structure; the size of the granular structure was approximately 1–4  $\mu\text{m}$ , and the structure was composed of CuO nanoparticles (approximately 80 nm in diameter). The solution pH, ionic strength, and presence of natural organic and inorganic colloids may have modified the surface conditions of the CuO nanoparticles, which caused the aggregation of the CuO nanoparticles. As shown in Figure 3b, the CuO/graphene CE displayed a foliated and granular structure; graphene exhibited a nanosheet structure, whereas CuO had a granular structure. The granular structure was composed of CuO nanoparticles (approximately 80 nm in diameter) and was distributed on the foliated graphene surface. Figure 3c,d depict the cross-sectional SEM images of CuO and CuO/graphene. The thicknesses of the CuO and CuO/graphene nanostructured thin films were 10.5 and 24.1  $\mu\text{m}$ , respectively.



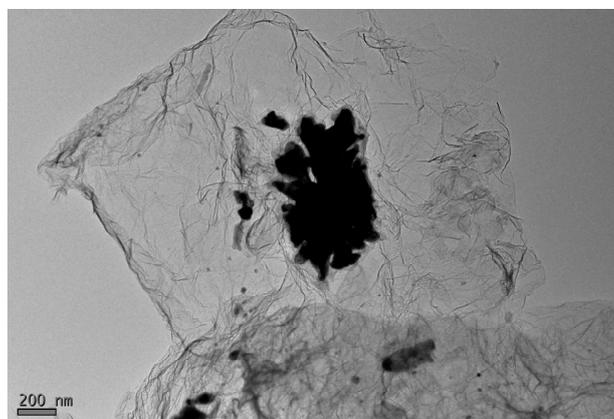
**Figure 3.** Top-view SEM images of (a) CuO and (b) CuO/graphene CEs. Cross-sectional SEM images of (c) CuO and (d) CuO/graphene CEs.

Figure 4a,b show the SEM images of the CuO CEs fabricated with 20 wt % and 30 wt % CuO concentrations, respectively. The SEM result indicated that increasing the CuO concentration

engendered an increased number of CuO nanoparticles in the granular structures. Figure 4c,d present the CuO/graphene CEs fabricated with CuO:GNs (9:1) and CuO:GNs (8:2) weight ratios in the CuO/graphene pastes. The SEM result indicated that increasing the GNs ratio in CuO/graphene CEs may have affected the surface morphology of the CuO/graphene CEs. As the concentration of the GNs increased, the number of the sheet-like GNs decreased due to the stacking effect of GNs. In addition, the morphology of the CuO/GNs was observed by a TEM (Figure 5). The GNs exhibited a crumpled morphology and a paper-like structure, and the CuO nanoparticles were aggregated and distributed on the GNs.



**Figure 4.** SEM images of CuO CEs fabricated with (a) 20 wt % and (b) 30 wt % CuO nanoparticles in CuO pastes. SEM images of CuO/graphene CEs fabricated with (c) CuO:GNs (9:1) and (d) CuO:GNs (8:2) weight ratios in CuO/graphene pastes.



**Figure 5.** The TEM image of CuO/graphene counter electrode.

Figure 6 shows the EDS analysis results of the CuO and CuO/graphene CEs. An EDS was used to analyze the elemental composition and content of the test samples. The EDS analyses of the CuO and CuO/graphene CEs showed that under the sintering condition of 350 °C in a pure nitrogen environment, the weight ratios and atomic numbers of O and Cu in the CuO electrode were 5.72:94.28 and 19.41:80.59, respectively, whereas those of C, O, and Cu in the CuO/graphene electrodes were 56.98:15.21:27.81 and 77.36:15.5:7.14, respectively. The EDS results demonstrated that the C content was significantly increased in the CuO/graphene CEs. In addition, the EDS results showed that the O and Cu content did not fit the stoichiometry of CuO in terms of atomic percentage. This is because EDS cannot be considered a quantitative method of analysis, especially for light elements (i.e., carbon); it can only be used as a qualitative analysis for various elements in an electrode. The composition of the CuO/graphene CEs was further investigated by a TGA instrument under an air atmosphere (Figure 7). CuO nanoparticles had high thermal stability with little or no significant mass loss below 700 °C. The final weight percentage of CuO in the CuO/graphene composite was 88.6 wt %, indicating a weight percent of graphene in the CuO/graphene CE of approximately 11.4 wt %.

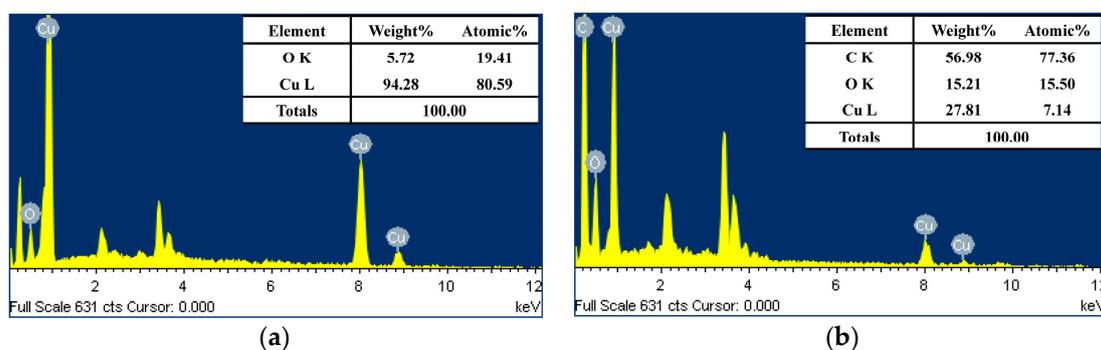


Figure 6. EDS analysis results of (a) CuO and (b) CuO/graphene CEs.

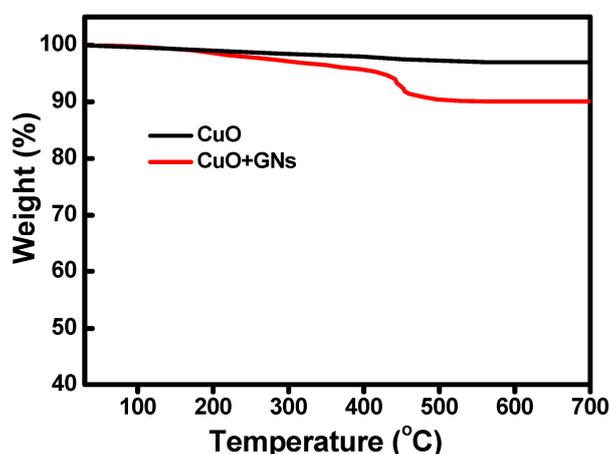


Figure 7. TGA analysis results of CuO and CuO/graphene CEs.

Raman spectroscopy involves exciting a test sample using a laser and studying the crystal lattice of the test sample as well as its molecular vibrations and rotational modes using photon–phonon interactions. The Raman spectra of the CuO and CuO/graphene electrodes sintered under a nitrogen atmosphere at a temperature of 350 °C are presented in Figure 8. The main Raman signals of the CuO electrodes, which were the CuO Raman signals, were found at 282, 330, and 616  $\text{cm}^{-1}$ . Conversely, the Raman signals of the CuO/graphene electrodes exhibited graphene Raman signals. The graphene Raman signals such as the D band, G band, and 2D band were found at 1350, 1580, and 2670  $\text{cm}^{-1}$ , respectively. In addition, the Raman results showed that the CuO Raman signals disappeared in

CuO/graphene electrodes. The reason for this phenomenon is that the CuO nanoparticles were reduced to Cu<sub>2</sub>O and/or Cu by graphene under a sintering temperature of 350 °C for 30 min.

Next, the crystal structure of CuO was analyzed using XRD, and the measured XRD results are shown in Figure 9. The XRD signal peaks for CuO were found at 35.6°, 38.9°, 48.9°, 58.4°, 61.7°, and 66.3°, corresponding to the (1, 1, -1), (2, 0, 0), (2, 0, -2), (2, 0, 2), (1, 1, -3), and (0, 2, 2) lattice planes for CuO, respectively. The diffraction peaks in the XRD pattern could be ascribed as the monoclinic (tenorite) phase of CuO and are close to reported data (Joint Committee on Powder Diffraction Standards: 48-1548;  $a = 4.688$ ,  $b = 3.422$ ,  $c = 5.131$  Å and  $\beta = 99.50^\circ$ , space group:  $C 2/c$  [No. 15]). The XRD signal peaks for FTO conductive substrates (SnO<sub>2</sub>:F) were found at 26.5°, 33.8°, 37.9°, 51.7°, 61.7°, 65.8°, and 78.4°. In addition, the XRD results showed that when graphene was added to CuO, it provided electrons to CuO. This electron transfer caused CuO to reduce to Cu<sub>2</sub>O and/or Cu. The XRD signal peaks for Cu were found at 43°, 50°, and 74°, corresponding to the (1, 1, 1), (2, 0, 0), and (2, 2, 0) lattice planes, respectively.

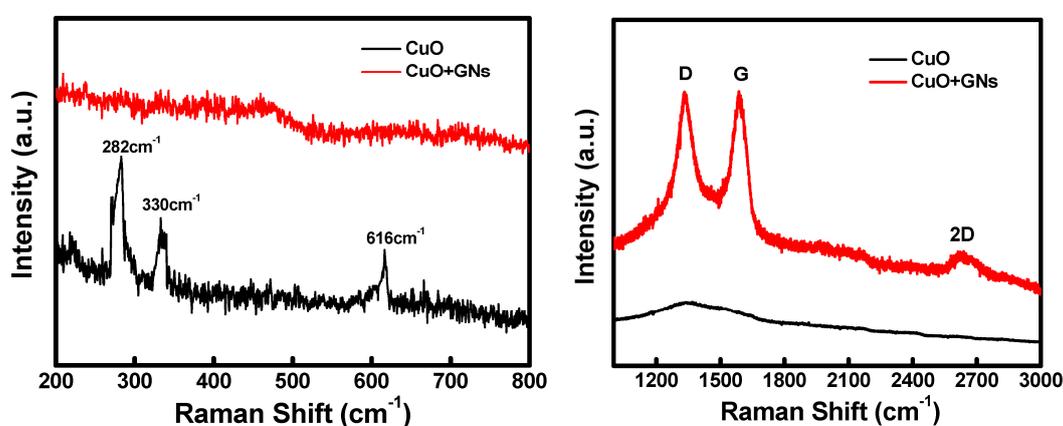


Figure 8. Raman spectra of CuO and CuO/graphene CEs.

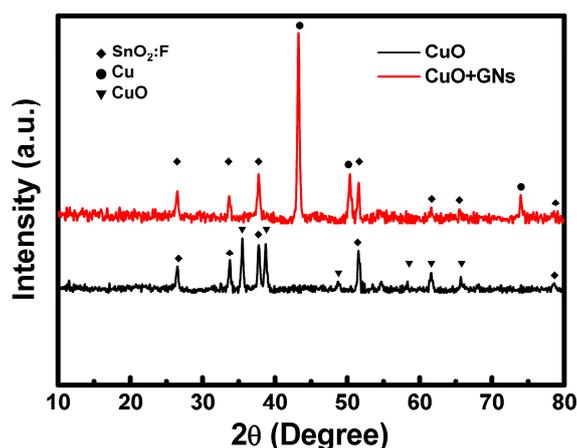


Figure 9. XRD spectra of CuO and CuO/graphene CEs.

The XPS analysis results of the CuO and CuO/graphene electrodes sintered under a nitrogen atmosphere at 350 °C are presented in Figure 10a. For the CuO electrode, main peaks and shake-up satellite peaks for both Cu 2p<sub>3/2</sub> and Cu 2p<sub>1/2</sub> were observed, and the shake-up satellite peaks appeared with a binding energy of 10 eV, higher than that of the main Cu 2p<sub>3/2</sub> and Cu 2p<sub>1/2</sub> peaks. The XPS analysis of the CuO/graphene electrodes showed that when graphene was added to CuO, it provided electrons to CuO, thereby reducing CuO to Cu<sub>2</sub>O and/or Cu. The transmittance of the CuO and

CuO/graphene CEs were characterized using a UV-Vis spectrophotometer, as shown in Figure 10b. At the wavelength of 550 nm, the transmittance of the CuO and CuO/graphene CEs were 12.9% and 11.1%, respectively. These results indicate that after the addition of graphene, the transmittance of the CEs reduced  $\sim 2\%$  of the light.

The electrochemical catalytic properties of the CuO and CuO/graphene CEs were then analyzed using CV, and the CV results are shown in Figure 11. In the CV diagram, the voltage range between +0.6 V and +1.2 V signifies an oxidation reaction, whereas the voltage range between  $-0.1$  V and  $-0.6$  V denotes a reduction reaction. The electrochemical catalytic properties of the electrodes were determined by observing the peak values of the reduction reaction. The reduction current peak of CuO was  $2.41 \text{ mA}\cdot\text{cm}^{-2}$ , and the reduction current peak of the CuO/graphene electrodes increased to  $4.12 \text{ mA}\cdot\text{cm}^{-2}$ . These experimental results reveal that when compared with the CuO electrodes, the CuO/graphene electrodes featured superior oxidation and reduction peak values, indicating that the addition of graphene elevated the electrochemical catalytic abilities of the CuO/graphene electrodes.

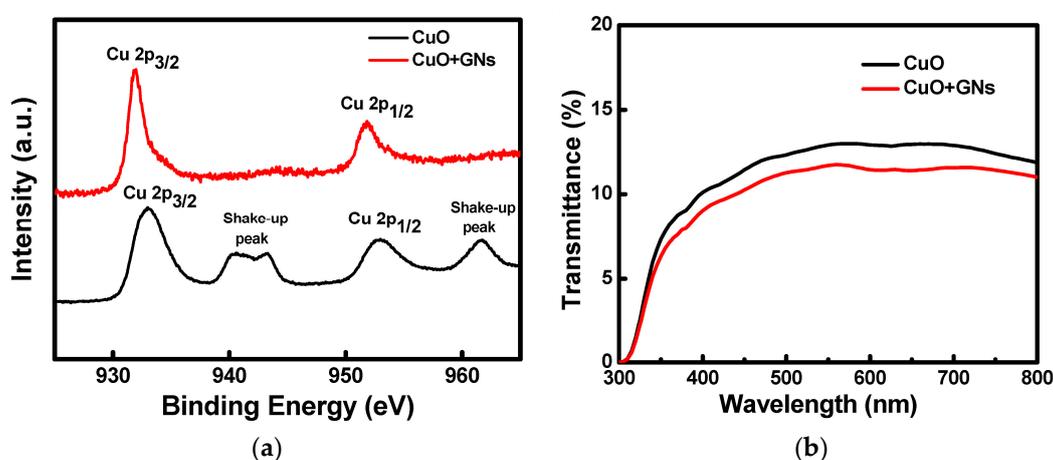


Figure 10. (a) XPS analysis results and (b) transmittance spectra of CuO and CuO/graphene CEs.

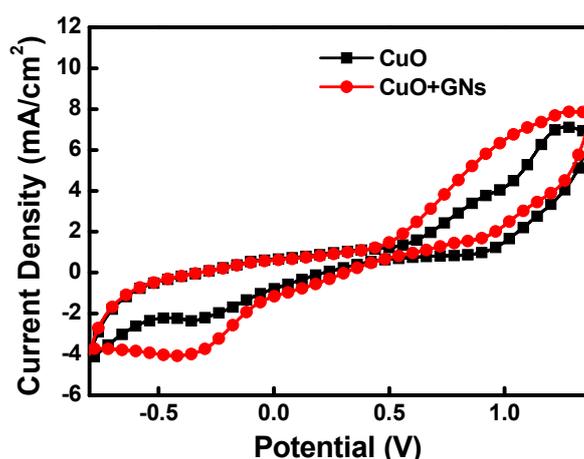


Figure 11. Cyclic voltammograms of CuO and CuO/graphene CEs.

### 3.2. Characterization of the DSSCs

We then proceeded to fabricate DSSC devices using various CEs and investigated the effect of the different CE materials on the DSSC device properties. Figure 12a shows the  $J$ - $V$  properties of the

DSSC devices fabricated using both CuO and CuO/graphene CEs. The result showed that the DSSC devices fabricated using CuO and CuO/graphene as the CEs had, respectively,  $J_{sc}$  values of 14.06 and 15.62  $\text{mA}\cdot\text{cm}^{-2}$ ,  $V_{oc}$  values of 0.67 and 0.69 V, fill factors of 0.28 and 0.31, and efficiencies of 2.73% and 3.40%. For a fair comparison, DSSCs based on Pt CEs were also fabricated and tested under similar conditions. The  $J_{sc}$ ,  $V_{oc}$ , and fill factor of the solar cells based on the Pt CEs were 15.12  $\text{mA}\cdot\text{cm}^{-2}$ , 0.69 V, and 0.61, respectively, which yielded an overall conversion efficiency of 6.36%. The measurements of each parameter are summarized in Table 1. According to the measured  $J$ - $V$  results, the DSSC fabricated using the CuO/graphene CE had a higher  $J_{sc}$  and power conversion efficiency than did the DSSC fabricated using CuO as the CE. However, compared with Pt, the electrical and catalytic abilities of CuO and CuO/graphene were still lower. Hence, the fill factor and power conversion efficiency of the devices fabricated with the CuO and CuO/graphene CEs were lower than those of the devices fabricated with the Pt CEs. Figure 12b shows the IPCE of the DSSC devices fabricated using the CuO and CuO/graphene CEs. The result revealed that the IPCE peak value of the DSSC device fabricated with the CuO CE was 66%, and the IPCE peak value of the DSSC device fabricated using the CuO/graphene CE was 78%. The DSSC device fabricated with the CuO/graphene CE exhibited a higher IPCE value. The IPCE result obtained is consistent with the  $J_{sc}$  trend obtained from the  $J$ - $V$  measurement.

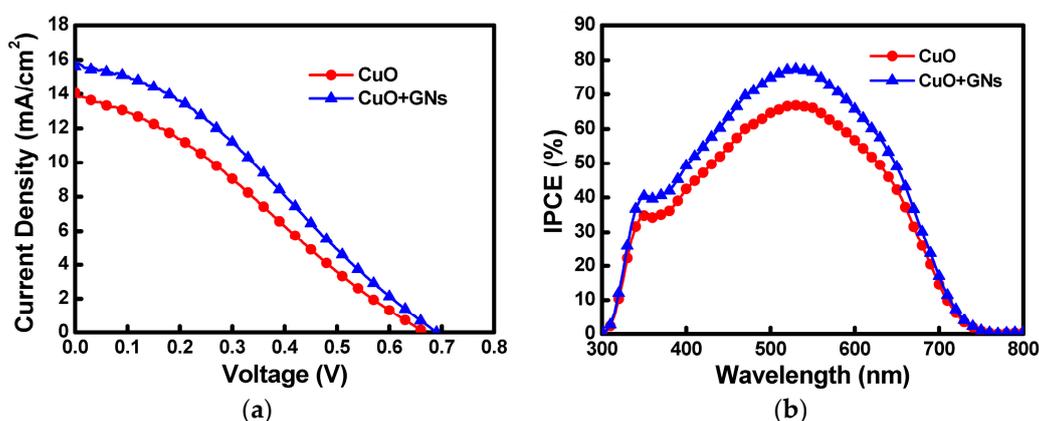


Figure 12. (a)  $J$ - $V$  curves and (b) IPCE curves of DSSCs using CuO and CuO/graphene CEs.

Table 1. Characteristics of DSSCs fabricated using various counter electrodes.

Counter Electrode	$J_{sc}$ ( $\text{mA}/\text{cm}^2$ )	$V_{oc}$ (V)	Fill Factor	Efficiency (%)
CuO	14.06	0.67	0.28	2.73
CuO + GNs	15.62	0.69	0.31	3.40
Pt	15.12	0.69	0.61	6.36

The EIS (frequency range: 0.1 Hz to 1 MHz) measurements of the DSSC devices fabricated using the CuO and CuO/graphene CEs are shown in Figure 13. During the impedance measurement, the cell was under constant AM 1.5G, 100  $\text{mW}\cdot\text{cm}^{-2}$  illumination. The impedance of the cell (from 0.1 Hz to 1 MHz) was measured by applying a bias at the  $V_{oc}$  of the cell (i.e., under no DC electric current) and by using an AC amplitude of 10 mV. In general, an impedance spectroscopy Nyquist plot contains three semicircles, which represent the interfacial impedance values of the applied CE/electrolyte (frequency range: >1 kHz), the interfacial impedance values of the applied  $\text{TiO}_2$ /dye/electrolyte (frequency range: 1 Hz–1 kHz), and the impedance values of electrolytes (frequency range: 0.1–1 Hz). However, in the current study, because the  $\text{TiO}_2$ /dye/electrolyte had considerably high interfacial impedance values that overlapped with the interfacial impedance values of the CEs/electrolytes and those of the electrolytes, only one semicircle was observed. To extract the quantitative impedance characteristics of the DSSCs, an equivalent-circuit model (in Figure 13) was used to analyze the internal

impedance of the DSSCs. The experimental results revealed that the DSSC device fabricated using the CuO CE displayed an impedance value of approximately 600  $\Omega$ , whereas the DSSC device fabricated using the CuO/graphene CE displayed an impedance value of approximately 520  $\Omega$ . These findings reveal that the DSSC device fabricated using the CuO/graphene CE had a lower impedance value, and this is because this device featured superior catalytic abilities and material properties.

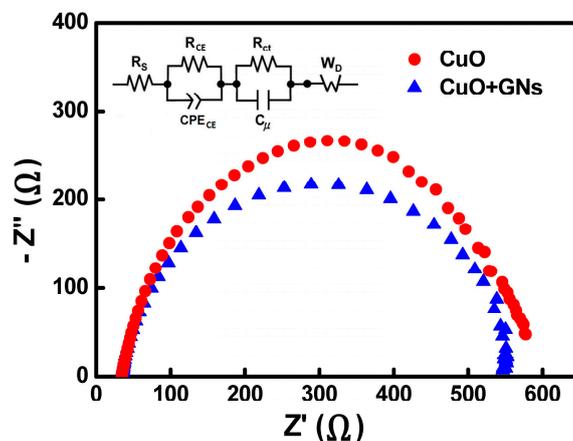


Figure 13. EIS Nyquist plots of DSSCs using CuO and CuO/graphene CEs.

#### 4. Conclusions

In this study, CuO and CuO/graphene pastes were coated on a FTO conductive substrate using a doctor-blade coating method. The substrate was then sintered at a high temperature to form a granular CuO nanostructure, which served as the DSSC CE. Concurrently, TiO<sub>2</sub> working electrodes covered in dye, CuO-based CEs, and electrolytes were combined to produce DSSCs. Ethyl cellulose and terpineol were added to CuO nanoparticles and stirred to form a paste. The same methodology was adopted to produce a paste with a CuO-to-graphene weight ratio of 9:1, which was coated on conductive glass using a doctor-blade coating method. Sintering the paste under a nitrogen atmosphere and a temperature of 350 °C resulted in CuO and CuO/graphene containing a granular nanostructure. Next, an SEM, TEM, EDS, TGA, XRD, Raman spectrometer, XPS, UV-Vis spectrophotometer, and CV instrument were employed to analyze the material properties of the CuO and CuO/graphene CEs. Using the CuO and CuO/graphene CEs, a TiO<sub>2</sub> working electrode, a dye, and an electrolyte, we fabricated DSSC devices and analyzed the device characteristics using  $J-V$ , IPCE, and EIS data. The experimental results show that when CuO and CuO/graphene were used as the CEs, the device conversion efficiencies were 2.73% and 3.40%, respectively. CuO is inexpensive, abundant, nontoxic, and easy to obtain. Therefore, it is a favorable replacement for expensive Pt as a CE. Furthermore, graphene, which features high carrier mobility, may be added to enhance the electrical and catalytic abilities of CuO/graphene CEs. This is the first study to examine the use of CuO and CuO/graphene for developing Pt-free CEs in DSSCs.

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