



Computing-in-Memory Devices and Systems

Guest Editors:

Dr. Mariagrazia Graziano

Department of Applied Science
and Technology (DISAT),
Politecnico di Torino, 10129
Torino, Italy

Dr. Fabrizio Riente

Department of Electronics and
Telecommunications,
Politecnico di Torino, 10129
Torino, Italy

Dr. Giovanna Turvani

Department of Electronics and
Telecommunications,
Politecnico di Torino, 10129
Torino, Italy

Deadline for manuscript
submissions:

closed (1 August 2022)

Message from the Guest Editors

The purpose of the proposed Special Issue on Computing-in-Memory Devices and Systems is to provide a comprehensive view of recent advances in devices that combine memory and computing capabilities, system architectures, and algorithms based on new advances in computer architecture and applications. Topics of interest for this Special Issue include, but are not limited to:

- Non-volatile memory solutions with computing capabilities;
- Novel circuit design solutions for computing-in-memory;
- Computing-in-memory applications and architectures;
- EDA tools for the design and benchmarking of computing-in-memory architectures;
- Novel electronic devices that show computing-in-memory capabilities;
- Device modeling.

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Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and
Telecommunications,
Politecnico di Torino, 10129
Torino, Italy

Message from the Editor-in-Chief

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MDPI, St. Alban-Anlage 66
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