



Digital and Analog Circuits and Applications Based on TFET Transistors

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Deadline for manuscript submissions:

closed (31 December 2021)

Message from the Guest Editors

The design of circuits aiming at exploiting the steep turn-on transition of TFETs is currently of great interest. The aim of this Special Issue is to collect reports on TFET potentiality to outperform CMOS at low voltages, with an emphasis on TFET circuit topologies for memory cells, digital logic, and analog and mixed-signal architectures. Papers discussing circuit design criteria and strategies to overcome TFET issues are also welcome.

Topics of Interest:

- Tunneling Field Effect Transistors (Tunnel FETs, T-FETs);
- T-FET circuits for energy efficient computing and information processing;
- Energy-Efficient computing and information processing with T-FET transistor circuits and architectures;
- TFET-based memories;
- Mixed T-FET CMOS designs.

Welcome to contribute.





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Editor-in-Chief

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Message from the Editor-in-Chief

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