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Low-Voltage Integrated Circuits Design and Application

Guest Editor:

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Deadline for manuscript submissions:

closed (30 September 2020)

Message from the Guest Editor

One of the most challenging tasks for analogue and digital designers is to maintain the circuit performances by developing novel circuit structures capable of operating with a low supply voltage.

Moreover, the increasing demands for both low supply voltage and energy efficiency often have a detrimental effect on the robustness and the reliability of integrated circuits, especially analogue ones.

The topics to be covered in this Special Issue are as follows:

- Theory, design, and new applications of low-voltage, low-power circuits;
- Conventional and nonconventional low-voltage analog and digital design techniques;
- Supply and energy harvesting blocks;
- Implantable and wearable devices for biomedical monitoring applications;
- Low-voltage circuits for Internet of Things (IoT) applications;
- Low-voltage power-efficient analog-to-digital converters;
- Design techniques to achieve high robustness and reliability against the electromagnetic pollution.

Welcome to contribute











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Editor-in-Chief

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Message from the Editor-in-Chief

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