



Energy Aware Scientific Computing in Distributed Architectures, Low Power Processors and HPC Hybrid Systems

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Message from the Guest Editor

Dear Colleagues,

Modern society is witnessing a sea change in ubiquitous computing, whereby people have embraced a slew of computing systems as indispensable parts of day-to-day existence. The computational, storage, and communication abilities of smartphones, for example, have undergone monumental changes in the past decade itself. With these changes, came a rapid and ongoing proliferation of edge computing systems and applications. Energy consumption is a growing problem of these computing platforms. There is a critical need for energy-aware architectures in many core systems, GPUs, hardware accelerators, and edge computing devices. This Special Issue will provide a selection of papers on energy-aware and low-power computing in system-on-chips, hardware accelerators, GPUs, embedded systems, or edge computing systems. Advances in algorithms for hardware systems design and novel architectures for low power are encouraged.

Prof. Dr. Sanghamitra Roy
Guest Editor





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Message from the Editor-in-Chief

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Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

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