

Article Low Resistance TiO₂/p-Si Heterojunction for Tandem Solar Cells

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Abstract: Niobium-doped titanium dioxide $(Ti_{1-x}Nb_xO_2)$ films were grown on *p*-type Si substrates at low temperature (170 °C) using an atomic layer deposition technique. The as-deposited films were amorphous and showed low electrical conductivity. The films became electrically well-conducting and crystallized into the an anatase structure upon reductive post-deposition annealing at 600 °C in an H₂ atmosphere for 30 min. It was shown that the $Ti_{0.72}Nb_{0.28}O_2/p^+$ -Si heterojunction fabricated on low resistivity silicon ($10^{-3} \Omega$ cm) had linear current–voltage characteristic with a specific contact resistivity as low as 23 m Ω ·cm². As the resistance dependence on temperature revealed, the current across the $Ti_{0.72}Nb_{0.28}O_2/p^+$ -Si heterojunction charge carrier tunneling through the junction.

Keywords: thin films; solar cells; TiO₂/p-Si heterojunction; atomic layer deposition

1. Introduction

The successful development of a monolithic perovskite/silicon tandem solar cell has attracted considerable attention during past few years [1–7]. The interest in this field is motivated by the rapid development of power conversion efficiency of a perovskite solar cell, from less than 3.8% to above 22% during the last decade [8–16]. A monolithically integrated two-terminal (2-T) perovskite/silicon tandem solar cell consists of a top perovskite subcell being deposited onto a bottom silicon subcell. The two subcells are then electrically connected in a series through a recombination layer or a tunnel junction [5]. The tunnel junction consisting of two heavily doped p^+ and n^+ silicon regions was used in the first demonstration of a 2-T perovskite/silicon tandem solar cell [1]. However, the tunnel p^+ – n^+ silicon junction can potentially contribute to parasitic optical absorption [7]. Aiming to lower the parasitic optical absorption, Shen et al. proposed to use a recombination junction formed between p-Si and atomically deposited TiO₂, thus enabling to produce a high efficiency monolithic perovskite/silicon tandem solar cell [7].

The electrical properties of TiO₂ thin films grown by atomic layer deposition (ALD) on crystalline silicon substrates were studied recently [17]. It was found that a heterojunction was formed between the deposited TiO₂ and silicon substrate demonstrating nonohmic and asymmetric current–voltage characteristics. Usually, TiO₂ is treated as an n-type semiconductor with a wide bandgap reaching 3.4 eV, 3.2 eV, 3.02 eV and 2.96 eV for amorphous, anatase, rutile and brookite phases, respectively [17,18]. Therefore, nondoped, high quality TiO₂ has a high resistivity and may serve as an insulator for



capacitors [19,20]. The electrical conductivity of nondoped TiO₂ films can be changed by variations of oxygen concentration during the TiO₂ reduction [17,18]. The O₂ deficiency creates defects such as oxygen vacancies, titanium vacancies, and Ti³⁺ and Ti⁴⁺ interstitials, which may act either as acceptors or donors of electrons [21,22]. TiO₂ is an amphoteric semiconductor, therefore creation of high conductivity nondoped TiO₂ films is hindered.

High conductivity semiconductors are needed to produce low resistivity TiO₂/*p*-Si contact. Furubayashi et al. showed that Nb-doped anatase TiO₂ film is an optically transparent electrically conducting oxide [23]. The resistivity of TiO₂ films with a Nb concentration exceeding 6% was less than $2.3 \times 10^{-4} \Omega$ ·cm at room temperature. Furthermore, Ti_{1-x}Nb_xO₂ films with $x \ge 0.01$ showed a metallic behavior. This paper deals with details of fabrication of a highly conducting TiO₂/*p*-Si heterojunction. Transparent TiO₂ films were grown by ALD and doped with Nb.

2. Materials and Methods

Thin $Ti_{1-x}Nb_xO_2$ (mixed titanium niobium oxide) layers were formed on glass, low resistivity p^+ -type and low conductivity p-type silicon substrates, using a "Fiji F200" atomic layer deposition reactor (Cambridge Nano Tech, Waltham, MA USA). A modular ALD system was used for layer formation in a moderate vacuum. First, glass and silicon substrates (University Wafer, Inc., Boston, MA, USA) were cleaned in ethanol and acetone in an ultrasonic bath for 20 min. Then the silicon surface was thermally oxidized in a quartz tube furnace "SDO-125/3" (Termotron, Bryansk, USSR) at 1150 °C in air for 3 h. The thickness of the silicon oxide layer was measured by a profilometer "Dektak 6M" (Veeco Metrology LLC, Plainview, NY, USA); it was 150 nm. Round 100-µm diameter holes were formed in SiO₂ by means of a photolithography technique. Thin $Ti_{1-x}Nb_xO_2$ layers were deposited using tetrakis dimethylamido titanium (TDMAT, 99.9%, STREM Chemicals Inc.) and niobium ethoxide (Nb(OEt)₅, 99.9%, STREM Chemicals Inc.) as precursors for titanium and niobium oxides. Deionized water was used as an oxygen source for both processes. The reactions for both processes are presented below.

Partial surface reactions for TiO₂:

$$||\text{Ti-OH} + \text{Ti}[N(CH_3)_2]_4 \rightarrow ||\text{Ti-O-Ti} - [N(CH_3)_2]_3 + NH(CH_3)_2$$
 (1)

$$\|\text{Ti-}[N(CH_3)_2] + H_2O \rightarrow \|\text{Ti-}OH + NH(CH_3)_2$$
(2)

Full reaction for TiO₂:

$$Ti[N(CH_3)_2]_4 + 2 H_2O \rightarrow TiO_2 + 4 NH(CH_3)_2$$
 (3)

Partial surface reactions for Nb₂O₅:

$$\|Nb-OH + Nb(OC_2H_5)_5 \rightarrow \|Nb-O-Nb - (OC_2H_5)_4 + C_2H_5OH$$
 (4)

$$\|Nb - (OC_2H_5) + H_2O \rightarrow \|Nb - OH + C_2H_5OH$$
 (5)

Full reaction for Nb₂O₅:

$$2 \text{ Nb}(OC_2H_5)_5 + 5 \text{ H}_2O \rightarrow \text{Nb}_2O_5 + 10 \text{ C}_2\text{H}_5\text{OH}$$
(6)

The reaction chamber was evacuated up to 3×10^{-2} mbar before the deposition process. The substrates and the reaction chamber were heated up to 170 °C. A constant flow of 100 sccm of pass-thru and 40 sccm of carrier gas (argon) was used during the deposition process. This kept the reaction chamber at ~0.18 mbar working pressure. To reach the desired vapor pressure, TDMAT and Nb(OEt)₅ were heated up to 80 °C and 170 °C, respectively. According to the authors of the paper [24], Ti_{1-x}Nb_xO₂ films deposited at temperatures around 170 °C have maximum electrical conductivity. Deionized water, which was used as an oxidizer, was kept at room temperature. The mixed oxide was

formed by inserting a monolayer of niobium oxide after a few consecutive monolayers of titanium oxide (number of TiO_2 monolayers was selected depending on desired Ti:Nb ratio) and the process was repeated until the desired thickness was achieved. Fabrication of every monolayer consisted of four steps: precursor pulse/purge/water pulse/purge. Timings used for titanium oxide and niobium oxide were 0.2 s/10 s/0.06 s/5 s and 0.2 s/5 s/0.06 s/5 s, respectively. Four hundred monolayers were deposited to achieve an approximately 25 nm-thick coating.

Morphology and composition of $Ti_{1-x}Nb_xO_2$ layers were examined by a scanning electron microscope (SEM) "Helios NanoLab 650" (FEI, Hillsboro, OR, USA) equipped with energy dispersive X-ray spectrometer (EDX) "INCAEnergy" (Oxford Instruments, Abingdon, UK). Thin Film ID software (Oxford Instruments) was used to estimate the Ti/Nb ratio with a 3% relative error. A relatively low accelerating voltage (7 kV) was used to achieve higher surface sensitivity. For 5:1 titanium oxide and niobium oxide monolayers deposition the resulting atomic ratio was 72 at.% titanium and 28 at.% niobium. For 10:1 titanium oxide and niobium oxide monolayers deposition the ratio was 84 at.% Ti and 16 at.% Nb.

Crystallographic structure of $Ti_{1-x}Nb_xO_2$ layers was studied by X-ray diffraction (XRD) using SmartLab HR-XRD diffractometer (Rigaku, Tokyo, Japan) with an X-ray tube equipped with 9 kW Cu rotating anode. Grazing incidence diffraction geometry was used with the incidence angle of Cu K α beam set to 0.5° which enabled investigation of thin films and reduced influence of the substrate. The resistivity of $Ti_{1-x}Nb_xO_2$ layers was measured using a four-point probe method.

The Si substrates with as-deposited films were divided into two parts, and one part was annealed in the tube furnace at 600 °C in H₂ atmosphere for 30 min. 500 nm-thick aluminum layer was thermally evaporated using "VAKSIS PVD Vapor-5S_Th" (Vaksis, Ankara, Turkey) on the *p*-type Si immediately after its rear side treatment in HF to remove the unnecessary SiO₂. To complete the heterojunction devices, ohmic contacts to the *n*-type TiO₂ were fabricated by thermal evaporation of Ti:Au metal layers with respective thicknesses of 20 nm and: 500 nm onto a photo-resistive mask, and contact patterns were formed using the lift-off technique. Schematic cross-sections of the TiO₂/*p*-Si heterojunction device and microphotograph of the contacts on the top of TiO₂ are presented in Figure 1.

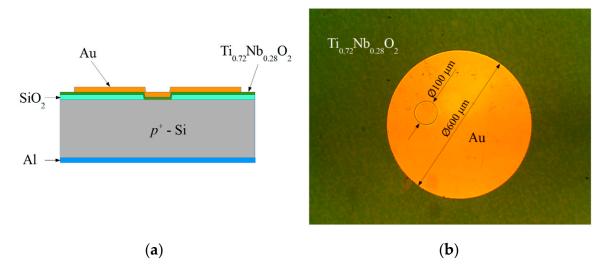


Figure 1. (a) Schematic cross-section of the TiO₂/*p*-Si heterojunction device. (b) Microphotograph top view of TiO₂/*p*-Si (\emptyset 100 µm) and Au/TiO₂ (\emptyset 600 µm) contacts (b).

Measurements of direct current (DC) current–voltage characteristics of the point-contact TiO_2/p -Si heterojunction were performed using a E5270B Precision IV Analyzer (Keysight Technologies, Inc., Santa Rosa, CA, USA). The point-contact electrical resistance dependence on temperature was measured in a liquid nitrogen vapor atmosphere from 78 K up to 350 K. The temperature of the sample

was controlled using K-type Nickel-Chromium/Nickel-Aluminum thermocouple (Thermometrics Corporation, Northridge, CA, USA).

Optical transmission spectra of the $Ti_{1-x}Nb_xO_2$ films were measured in the 300–1300 nm wavelength range using AvaSpec ULS2048XL spectrometer and AvaLight-DH-S deuterium-halogen light source (both from Avantes, Apeldoorn, the Netherlands). A 50 ms integration time and an averaging of 100 measured spectra were used for the measurements.

3. Results and Discussion

Figure 2 presents the XRD patterns of the $Ti_{0.72}Nb_{0.28}O_2$ film before (as-deposited) and after annealing in an H₂ atmosphere. It was seen that the as-deposited film had an amorphous structure (a broad feature with maxima at 2 Θ angle of about 21.6°) along with a crystalline TiO₂ of anatase structure. The lattice parameters of anatase tetragonal structure of the film (a = 0.3819 nm and c = 0.9541 nm) were increased in comparison to those presented in ICDD data base card #01-075-2545 (a = 0.3799 nm and c = 0.9509 nm). The increase in lattice parameters should be a result of insertion of Nb ions into crystalline lattice of anatase. After the annealing, the XRD peaks of anatase became sharper as a result of an increase in a crystallite size. The XRD pattern of the annealed film presented one additional peak at 2 Θ angle of 27.17°, which could be attributed to niobian rutile $Ti_{0.712}Nb_{0.288}O_2$ (#01-072-7371).

During the annealing, the amorphous phase transformed into an anatase crystalline phase, as XRD measurements confirmed. No characteristic peaks of Nb_2O_5 were observed in the Nb-doped TiO₂ thin film as indicated previously [25].

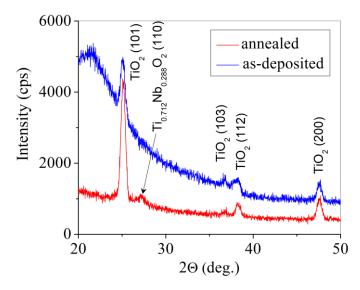


Figure 2. XRD patterns of the Ti_{0.72}Nb_{0.28}O₂ film before (dark line) and after annealing in H₂ atmosphere (red line).

Annealing of the $Ti_{0.72}Nb_{0.28}O_2$ films in H_2 atmosphere also resulted in a substantial decrease of its electrical resistivity from 5.0×10^2 to $1.2 \times 10^{-3} \Omega$ cm. In spite of high conductivity, the annealed $Ti_{0.72}Nb_{0.28}O_2$ film was highly transparent in the measured 400–1300 nm spectral region (Figure 3) with more than 93% transmittance within the 800–1000 nm range. Transmittance of the annealed and as-deposited $Ti_{0.72}Nb_{0.28}O_2$ films was significantly higher than that of $Ti_{0.8}Nb_{0.2}O_2$ film [23]. High conductivity and transmittance values suggest the annealed $Ti_{0.72}Nb_{0.28}O_2$ film is a suitable candidate for transparent electrical interconnection for perovskite/silicon tandem solar cells.

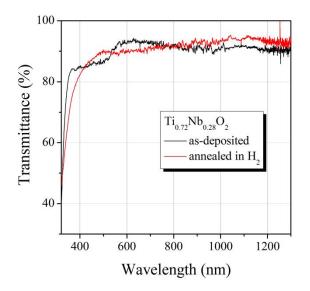


Figure 3. Transmittance of the annealed and as-deposited Ti_{0.72}Nb_{0.28}O₂ film on a glass substrate.

The current–voltage (I–V) characteristics of $TiO_{0.72}Nb_{0.28}O_2/p^+$ -Si heterojunction device on the base of $10^{-3} \Omega$ cm resistivity silicon substrate measured at room and liquid nitrogen temperatures are shown in Figure 4.

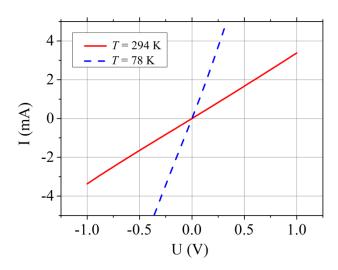


Figure 4. Current–voltage characteristics of $\text{TiO}_{0.72}\text{Nb}_{0.28}\text{O}_2/p^+$ -Si heterojunction device formed on the 10^{-3} Ω cm resistivity silicon substrate at room (red solid line) and liquid nitrogen (blue dashed) temperatures.

It is worth noting that the I–V characteristics were linear; it was another valuable property for the above-mentioned application. The resistance of such $\text{TiO}_{0.72}\text{Nb}_{0.28}\text{O}_2/p^+$ -Si heterojunction device decreased as the sample was cooled down. Dependencies resistance-vs.-temperature for $\text{TiO}_{1-x}\text{Nb}_x\text{O}_2/p$ -Si heterojunction devices with different content of x and formed on different Si substrates are depicted in Figure 5. It was seen that the resistance of $\text{TiO}_{0.72}\text{Nb}_{0.28}\text{O}_2/p^+$ -Si heterojunction (substrate $\rho_{\text{Si}} = 10^{-3} \Omega \text{ cm}$) linearly increased with temperature. Such linear dependence was an inherent feature of the tunnel diode at small bias voltage.

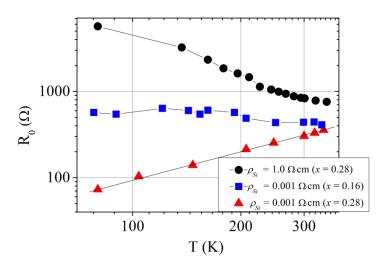


Figure 5. Dependence of the low bias resistance of $\text{TiO}_{1-x}\text{Nb}_x \text{O}_2/p^+$ -Si heterojunction devices on temperature. The devices differ in the amount of Nb in the TiO_2 layer (x = 0.28 and 0.16) and in conductivity of the silicon substrate ($\rho_{\text{Si}} = 10^{-3} \Omega$ cm and 1Ω cm).

In general, when the interband carrier tunneling takes place, the tunnel current across a p-n junction can be expressed as [26,27]:

$$I_t = AeU(E_v - E_c)^2 / 4kT,$$
(7)

where *A* is a constant, E_c and E_v represent the conduction and the valence band edges, respectively, and *k* is the Boltzmann constant. Expression (7) shows that the resistance of a tunnel *p*-*n* junction at low bias was a linear function of temperature. The Hall effect measurements indicate that electron density in TiO_{0.72}Nb_{0.28}O₂ is 3.5×10^{21} cm⁻³ and therefore it can be regarded as a degenerate semiconductor [24,28,29]. Low resistivity p^+ -Si ($\rho = 10^{-3} \Omega$ cm) is also a degenerate semiconductor. The hole concentration determined from Hall effect measurements in p^+ -Si was 1.3×10^{20} cm⁻³. The energy band diagram of mutually heavily doped n^+ -TiO₂/ p^+ -Si heterojunction in equilibrium condition is shown in Figure 6. It is seen that electrons from TiO₂ conduction band could tunnel through the gap to the empty sites of the p^+ -Si valence band under a small forward bias. Linear dependence of the resistance on temperature supports the assumption that the interband tunneling current took place in the investigated TiO_{0.72}Nb_{0.28}O₂/ p^+ -Si heterojunction. The specific contact resistivity of TiO_{0.72}Nb_{0.28}O₂/ p^+ -Si heterojunction was 23 mΩ·cm² at room temperature which was better than 30 mΩ·cm² achieved in [7].

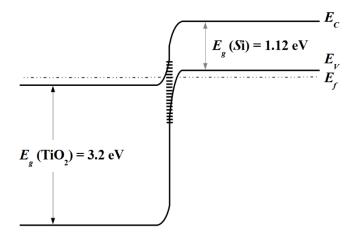


Figure 6. Energy band diagram of n^+ -TiO₂/ p^+ -Si tunnel heterojunction in an equilibrium condition.

As a rule, there are three main components of current in a tunnel diode: the tunnel current (I_t) , the excess current (I_x) and the diffusion current (I_d). The diffusion current is responsible for the current rise under high forward biases [26]. Therefore, I_d should be negligible in comparison with the tunnel current at low bias. The excess current at low bias was mainly determined by the multistep tunneling recombination process via surface states at the TiO_2/p^+ -Si interface [7,30]. Substantial density of localized surface states was determined by a large number of defects, $N_s \sim 7.0 \times 10^{13}$ cm⁻², at the TiO₂/p⁺-Si interface resulting from significant lattice mismatch between the heterojunction components [30]. These interfacial states can facilitate the band to band tunneling and act as generation-recombination centers at all bias voltages [7,31]. As the generation-recombination centers, the interface states had substantial influence on charge transport through TiO_2/p^+ -Si tunnel heterojunction. At reverse bias, every recombination center becomes a source of carrier generation, and high electrical conductivity can be reached by thermally generated carriers [7]. Since the carrier generation–recombination strongly depends on the thermal energy, the multistep tunneling recombination process via surface states resulted in a substantial increase of the heterojunction resistance at lower lattice temperature. Such dependence of the resistance on temperature was observed in TiO_{0.72}Nb_{0.28}O₂/p-Si heterojunction on low conductivity $(\rho_{Si} = 1 \Omega \text{ cm}) p$ -type silicon substrate (see Figure 5, black circles). The band-to-band tunneling was impossible in this case, therefore the current through the TiO_{0.72}Nb_{0.28} O₂/p-Si heterojunction was mainly determined by the multistep tunneling recombination process via the surface states.

I–V characteristic of the TiO_{0.72}Nb_{0.28}O₂/*p*-Si heterojunction was not linear (see Figure 7, solid black line) with forward current larger than the reverse one, as observed in other works [7,17,30]. Similar character demonstrated the I–V dependence of the TiO_{0.84}Nb_{0.16}O₂/*p*⁺-Si device formed on the low resistivity substrate (Figure 7, blue long-dotted line). Electron density in the TiO_{0.84}Nb_{0.16}O₂ film as determined from the Hall effect measurements was 1.9×10^{21} cm⁻³. Therefore, the TiO_{0.84}Nb_{0.16}O₂ film could be also regarded as a degenerate semiconductor [24,28,29], and the band to band tunnel current could be present in the investigated TiO_{0.84}Nb_{0.16}O₂/*p*⁺-Si heterojunction. Since electron density in the TiO_{0.84}Nb_{0.16}O₂/*p*⁺-Si heterojunction became of the same order of magnitude as the excess current in TiO_{0.84}Nb_{0.16}O₂/*p*⁺-Si heterojunction became of the surface states at the TiO₂/*p*⁺-Si interface. This consideration was supported by the dependence of the resistance of the TiO_{0.72}Nb_{0.28}O₂/*p*-Si heterojunction device on temperature depicted in Figure 5 (blue squares). Very weak temperature dependence of the resistance was an inherent feature of the tunnel current consisting of two components, *I_t* and *I_x* [32].

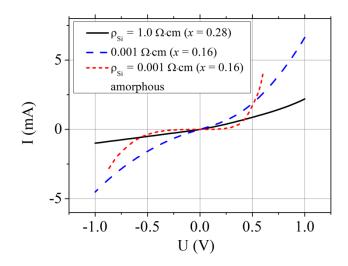


Figure 7. Current–voltage characteristics of $TiO_{1-x}Nb_x O_2 / p$ -Si heterojunction device measured at room temperature. Solid curve is for x = 0.28 and low conductivity *p*-Si.

Figure 7 also shows the I–V characteristic of the $TiO_{0.84}Nb_{0.16}O_2/p^+$ -Si heterojunction device with as-deposited titanium oxide layer (red short-dotted line). As mentioned above, the as-deposited $TiO_{0.84}Nb_{0.16}O_2$ film had an amorphous structure and therefore its conductivity was low. As a result, the I–V characteristic of the $TiO_{0.84}Nb_{0.16}O_2/p^+$ -Si heterojunction device was nonohmic and asymmetric, as observed in other works [7,30].

4. Conclusions

Two different (x = 0.16 and x = 0.28) niobium composition containing heavily doped Ti_{1-x}Nb_xO₂ thin films were ALD-deposited on *p*-type Si substrates. Reductive post-deposition annealing was required to crystallize amorphous titanium dioxide into the anatase structure and to increase its electrical conductivity. The current-voltage characteristic of the TiO_{0.72}Nb_{0.28}O₂/*p*⁺-Si heterojunction device is found to be ohmic, and the junction resistance linearly depends on temperature. In this case the current across the heterojunction is mainly stipulated by the interband charge carrier tunneling. When the highly conductive titanium dioxide is deposited on low conductivity ($\rho_{Si} = 1 \Omega \text{ cm}$) *p*-Si substrate, the current across the TiO_{0.72}Nb_{0.28}O₂/*p*-Si heterojunction is mainly stipulated by the interband charge carrier tunneling. The nullistep tunneling recombination process via the surface states. The contact resistivity of the TiO_{0.72}Nb_{0.28}O₂/*p*-Si heterojunction is higher than that of the TiO_{0.72}Nb_{0.28}O₂/*p*⁺-Si heterojunction. The formed titanium dioxide films also demonstrate excellent transparency with absorption less than 10% in the visible region. Therefore, the TiO_{0.72}Nb_{0.28}O₂/*p*⁺-Si heterojunction could be a suitable candidate as transparent interconnection in 2-T perovskite/silicon tandem solar cells.

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