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The Effect of Post-Baking Temperature and Thickness of ZnO Electron Transport Layers for Efficient Planar Heterojunction Organometal-Trihalide Perovskite Solar Cells

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Abstract: Solution-processed zinc oxide (ZnO)-based planar heterojunction perovskite photovoltaic device is reported in this study. The photovoltaic device benefits from the ZnO film as a high-conductivity and high-transparent electron transport layer. The optimal electron transport layer thickness and post-baking temperature for ZnO are systematically studied by scanning electron microscopy, photoluminescence and time-resolved photoluminescence spectroscopy, and X-ray diffraction. Optimized perovskite solar cells (PSCs) show an open-circuit voltage, a short-circuit current density, and a fill factor of 1.04 V, 18.71 mA/cm², and 70.2%, respectively. The highest power conversion efficiency of 13.66% was obtained when the device was prepared with a ZnO electron transport layer with a thickness of ~20 nm and when post-baking at 180 °C for 30 min. Finally, the stability of the highest performance ZnO-based PSCs without encapsulation was investigated in detail.

Keywords: zinc oxide; perovskite; photovoltaic; electron transport layer

1. Introduction

Recently, organometal-trihalide perovskite-structured photovoltaic devices (CH₃NH₃PbX₃, X = Cl, Br or I) have received much attention because of their outstanding power conversion efficiency (PCE) and unique optoelectronic characteristics. The characteristics of a high-performance perovskite-structured photovoltaic device include high visible absorption behavior [1], a long carrier diffusion length [2], high ambipolar charge mobility [3,4], tolerance of defect [5], and a simple low-temperature solution process [6]. The first attempt using CH₃NH₃PbX₃ as an active layer for light harvesting was adopted in liquid-type dye-sensitized solar cells with a PCE of 3.8% [7]. The perovskite-structured active layer suffers severe damages from liquid electrolytes because it can be dissolved in polar electrolytes. However, the problem can be improved by using solid hole transport materials to replace the liquid

electrolytes [8]. Two main architectures have been used to fabricate perovskite solar cells (PSCs). One is based on a mesoporous TiO_2 scaffold, a perovskite-structured active layer, a hole transport layer (2,2',7,7'-Tetrakis-(*N,N*-di-4-methoxyphenylamino)-9,9'-spirobifluorene, spiro-MeOTAD), and an Au electrode, which has achieved a power conversion efficiency as high as 22.1% [9]. The other consists in planar PSCs and adopts poly(3,4-ethylenedioxythiophene): polystyrene sulfonic acid (PEDOT:PSS) and [6,6]-phenyl-C61-butyric acid methyl ester (PCBM) as hole and electron transport materials, respectively. Since the PEDOT:PSS and PCBM are organic materials, the process temperature must be lower than 150 °C. In contrast, the perovskite-structured photovoltaic device with a mesoporous metal oxide scaffold, such as TiO_2 or Al_2O_3 , can provide a high specific area substrate on which the perovskite-structured active layer absorber can be grown [3]. As a structure-directing support, the TiO_2 scaffolds need to accept the electrons from the absorber and transport them to the electrode. However, TiO_2 scaffolds as electron transport layers have certain problems, such as severe *J-V* hysteresis phenomena owing to poor conductivity of TiO_2 [10]. While TiO_2 presents a single anatase phase, the electron transport properties can be improved by ascending the calcination temperature to 500 °C [11]. However, the disadvantage of TiO_2 is that high-temperature sintering is often required to obtain the desired nanostructures, which is a barrier to low cost and stretchable device fabrication. ZnO is a practical alternative to TiO_2 to the electron transport layer of solar cells [12,13], lasing [14], detectors [14], LED [15], etc. since it is also an n-type material with a wide direct band gap ($E_g = 3.37$ eV at 300 K). The low-temperature process ability, comparable energy levels, and ambient stability make it an excellent electron transport material [16]. Furthermore, ZnO shows an electron mobility higher than that of TiO_2 , which makes it a potential candidate for electron transport layers. Moreover, the ZnO electron transport layer can be easily deposited through electrodeposition [17], atomic layer deposition [18–20], or spin-coating [21–23]. These methods are carried out without the need for a high-temperature sintering step, so it is an ideal material for deposition onto thermally sensitive substrates, such as ITO glass or ITO/PET substrates.

Timothy et al. [24] and Song et al. [22] reported low-temperature ZnO nanoparticle processing. Zhang et al. [25] employed low-overvoltage electrodeposition on ZnO layers with PSCs. Seok et al. [26] implemented highly dispersed Zn_2SnO_4 nanoparticles as an electron transport layer in a perovskite photovoltaic device at a low temperature. Despite being promising for low-temperature processing, these steps comprise time-consuming precipitation, dilution, and washing processes coupled with long reaction times, a need for sophisticated handling, and/or delicate electrochemical machinery [27]. In spite of the several advantages of ZnO, the PCE of ZnO-based PSCs with a traditional n-i-p structure is generally lower than that of TiO_2 -based PSCs. The problem is that low-temperature processing leaves surface hydroxyl groups and/or residual acetate ligands on the surface of ZnO, and the hydroxyl group decomposes $\text{CH}_3\text{NH}_3\text{PbI}_3$ into PbI_2 and $\text{CH}_3\text{NH}_3\text{I}$ [28,29]. To avoid this process, the surface of ZnO has been modified by thermal annealing to remove the hydroxide [29], and a buffer layer, such as PCBM and poly(ethylenimine) (PEI), has been placed between the perovskite and ZnO layers to minimize the effect of the surface state of ZnO on the performance of the PSCs [28].

Here, we prepared the ZnO electron transport layer by the simple spin-coating method and then deposited the $\text{CH}_3\text{NH}_3\text{PbI}_3$ layer in a two-step process [1]. The influences of the thickness and baking temperature of the ZnO electron transport layer on the performance and stability of the solar cells were investigated systematically. The surface morphology, crystallization, and defects of the ZnO films were evaluated via scanning electron microscopy (SEM), X-ray diffraction (XRD), photoluminescence and time-resolved photoluminescence spectroscopy (TRPL), and atomic force microscopy (AFM). The effect of ZnO electron transport layers on the performance of perovskite photovoltaic devices was revealed. The stability of the performance of the perovskite photovoltaic device was determined over the course of more than 1300 h.

2. Materials and Methods

2.1. Synthesis of ZnO Precursor

The ZnO precursor solution (0.5 M) was synthesized by mixing 0.910 g of zinc acetate dihydrate ($\text{Zn}(\text{CH}_3\text{COO})_2$), 9.310 g of 2-methoxyethanol, and 0.305 g of monoethanolamine (MEA) under stirring at 65 °C for 2 h until a transparent and homogeneous solution was obtained [30].

2.2. Preparation of Perovskite Solar Cells

The fluorine-doped tin oxide (FTO, surface resistivity is $\sim 7 \Omega/\text{sq}$) glass serves as a substrate for this study. It was cleaned by a chemical solution including neutral liquid (Extran[®] MA 02, Merck, Darmstadt, Germany), acetone, isopropyl alcohol (IPA), and de-ionized water in an ultrasonic bath for 10 min, and dried with a jet of nitrogen. Finally, the substrate was then baked at 100 °C for 30 min to eliminate the residual solvents. Various ZnO electron transport layers were coated on the FTO glass by the spin-coating method at 5000 revolutions per minute (rpm) for 20 s, and baked at different temperatures (100 °C, 120 °C, 150 °C, 180 °C, and 250 °C) for 30 min. After the preparation of the ZnO electron transport layer, the perovskite light harvester was deposited in a two-step process [1]. Lead iodide (PbI_2) solution was prepared by dissolving 392.0 mg PbI_2 (99.998%, Sigma-Aldrich, St. Louis, MO, USA) in 1.0 mL of *N,N*-dimethylformamide (DMF, 99.8%, Sigma-Aldrich). The 80.0 μL PbI_2 solution was spin-coated on the ZnO electron transport layer at 3000 rpm for 30 s, and baked at 100 °C for 10 min. The 200.0 μL of 0.044 M (7 mg/mL) $\text{CH}_3\text{NH}_3\text{I}$ dissolved in IPA was spin-coated on the top of the PbI_2 layer at 3000 rpm for 30 s, and baked at 100 °C for 10 min to convert it into $\text{CH}_3\text{NH}_3\text{PbI}_3$ (MAPbI_3). The Spiro-MeOTAD in 1.0 mL chlorobenzene was added with 28.8 μL of 4-tert-butyl pyridine (96.0%, Sigma-Aldrich) and 17.5 μL of lithium bis(trifluoromethane sulfonyl) imide (LiTFSI) solution (520 mg/mL acetonitrile (99.8%, Sigma-Aldrich)). One hundred microliters of the Spiro-MeOTAD solution was spin-coated on the MAPbI_3 layer at 2000 rpm for 30 s. Finally, the MoO_3 layer with a 10 nm thickness and silver electrode with a 100 nm thickness were thermally evaporated on the top of the Spiro-MeOTAD layer, respectively. The construction of the perovskite-structured photovoltaic device is shown in Figure 1a and the energy level of materials in device is shown in Figure 1b.

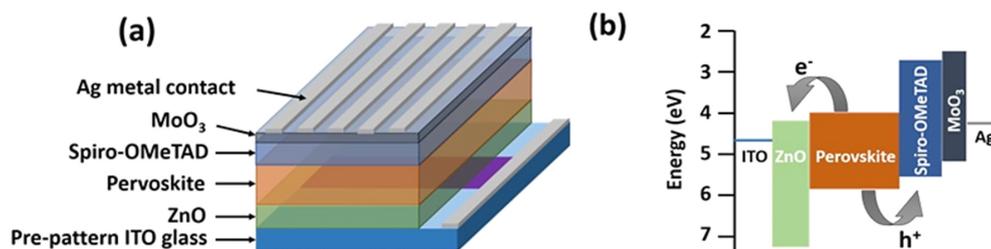


Figure 1. (a) The construction of the perovskite solar cell (PSC), and (b) the energy level diagram of materials used in the perovskite photovoltaic device in this study.

2.3. Characterization

A scanning electron microscope (S-800, Hitachi, Tokyo, Japan) operated at 10.0 kV was used to produce the SEM images. XRD patterns were collected from 10 to 50° of 2θ with a multi-function high power X-ray diffractometer (D8 Discover SSS, Bruker, Billerica, MA, USA) equipped with a 2D detector using $\text{Cu K}\alpha$ radiation. The solar simulator used was a 3A (AAA) solar simulator (KXL-500F, Wacom, Kazo, Japan). Before each measurement, the intensity of light for the simulator was calibrated by a silicon diode with a KG-5 filter (2.0 cm \times 2.0 cm). The silicon diode was calibrated from time to time using a secondary reference silicon cell. The current of the silicon cell with a KG-5 filter under 1.0 sun should be 70.6 mA, which was calibrated by TERTEC Org., Taoyuan, Taiwan

(following IEC 60904 [31]). The area of the test cell was 0.10 cm^2 , which was defined by a thin black metal mask. The computer-controlled digital source meter (Keithley 2400, Keithley, OH, USA) was applied to measure the photocurrent density–voltage (J - V) characteristics under 1.0 sun illumination (100 mW/cm^2 , AM 1.5G). AFM (Dimension-3100 Multimode, Veeco Digital Instruments, Plainview, NY, USA) in tapping mode was used to measure the surface morphology of various ZnO electron transport layers. The steady-state PL spectra were recorded by pumping the specimen with different ZnO electron transport layers using a continuous-wave diode laser ($\lambda_{\text{exc}} \sim 440 \text{ nm}$, PDLH-440-25, Dong Woo Optron Co. Ltd., Seoul, Korea). The recorded data were analyzed by a photomultiplier tube detector system (PDS-1, Dong Woo Optron Co. Ltd.) and a monochromator (Monora150i, Dong Woo Optron Co. Ltd.) of standard photon-counting electronics. TRPL spectra were measured by a time-correlated single photon counting (WELLS-001FX, Dong Woo Optron Co. Ltd.) spectrometer. A pulse laser ($\lambda_{\text{exc}} \sim 440 \text{ nm}$) with an average power of 1.0 mW, operating at 312.5 MHz, with a $2 \mu\text{s}$ duration was used for excitation.

3. Results and Discussion

3.1. Influence of ZnO Electron Transport Layer Thickness

The construction of the PSC and an energy level diagram is shown in Figure 1. Figure 2 shows the surface microstructure of various ZnO electron transport layers with different coating layers. It is evident that the ZnO electron transport layer replicates the underlying FTO relief morphology remarkably and that the surface lacks pinholes and rugged edges. This reveals that the optimization of the ZnO electron transport layer works to cover on the surface of the FTO glass well. Thus, it is suitable for the planar perovskite-structure photovoltaic device [32]. Here, the ZnO electron transport layer is also known as the hole blocking layer that can avoid the intimate contact between the MAPbI_3 and the FTO, so the layer can suppress the charge recombination inside the cells.

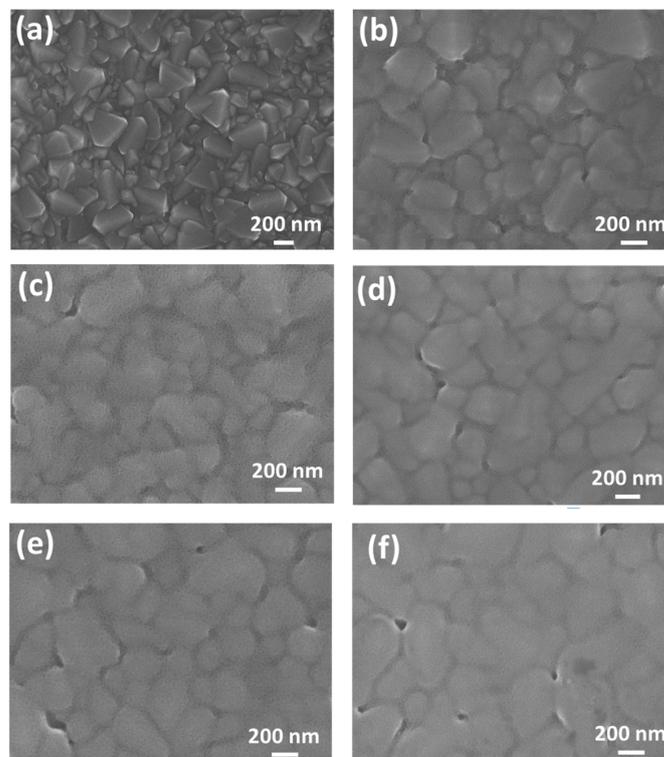


Figure 2. Various ZnO electron transport layers spin-coated on the FTO glass: (a) Bare FTO; (b) one layer; (c) two layers; (d) three layers; (e) four layers; and (f) five layers.

XRD patterns of various ZnO electron transport layers with different coating layers are shown in Figure 3. It is worth noting that the ZnO diffraction peaks cannot be observed when the ZnO thickness is less than two layers. As there are more than two coating layers, the 2θ diffraction peaks of ZnO weakly appears at 31.77° , 34.42° , and 36.25° corresponding to (100), (002), and (101), which is in accordance with JCPDS Card No. 36-1451. This is due to the fact that ZnO films are very thin and have low-crystallinity (most of the films are amorphous).

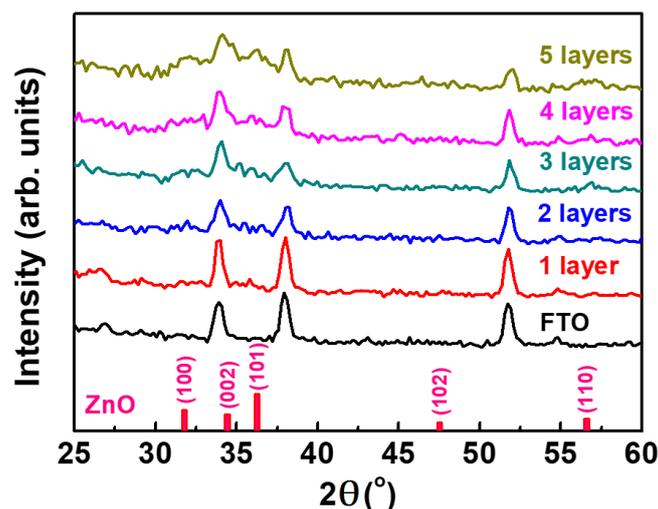


Figure 3. The XRD patterns of various ZnO electron transport layers, baking at 180°C .

In the PL spectrum (pumping with 532 nm laser), an emission peak is noted at 768 nm, indicating a direct bandgap emission from first conduction band to first valence band [20]. Because the exciton binding energy of MAPbI_3 is about 30–50 meV, which is larger than the thermal energy of 25 meV at room temperature [33]. Taking 50 meV as an example, the fraction of excitons generated by photons can be deduced from statistical physics to yield. About 34% of the excitons produced by photons will be spontaneously dissociated and about 66% of the excitons will remain. Therefore, the PL originates mainly from the radiative recombination of electron-hole pairs (excitons). The PL intensity can be used to evaluate the exciton quenching (exciton dissociation) at the interface between MAPbI_3 and ZnO. The thickness of ZnO thin films highly influences the PL intensity of MAPbI_3 , which means that the exciton quenching efficiency is related to the electrical properties of ZnO thin films. The PL spectra of the sample in this study, with MAPbI_3 coated on various ZnO electron transport layers on bare glass, are shown in Figure 4. The radiative recombination mainly dominates the PL signal. Therefore, the relative PL signal intensity can be applied to evaluate the electron-hole pair quenching behavior at the interface between the ZnO electron transport layer and the MAPbI_3 layer. The lower relative PL signal intensity represents high quench efficiency. Specifically, the electron can be efficiently collected by ZnO electron transport layer [20]. In Figure 4, it is obvious that one layer of ZnO shows a relatively lower signal intensity, which indicates that this layer collects electrons highly efficiently. As the ZnO coating layer increases, the relative PL signal intensity gradually increases, which is due to the thickness-dependent electron mobility of ZnO films [34]. The increased PL signal reveals that the electron-hole pair is hardly separated and collected at the interface of perovskite-structured active layer and ZnO. In other words, the electron collection ability decreases as the thickness of the ZnO electron transport layer increases [20,35]. This might be attributed to the increase in series resistance (R_s) as the ZnO thickness increases. Based on the above evaluations, we believe that a one-time coating (~ 20 nm) is found to be optimum since it had the best quench efficiency in this study.

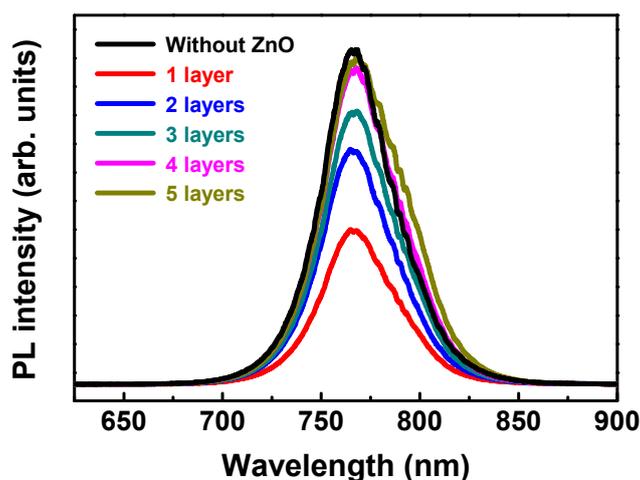


Figure 4. PL spectra of perovskite films with various ZnO electron transport layers on a bare glass substrate, baking at 180 °C.

3.2. Influence of Baking Temperature

As mentioned in the introduction, the surface of ZnO can be modified by thermal annealing to remove the hydroxide. Therefore, the baking temperature of the ZnO electron transport layer is an important issue for the fabrication of the efficient ZnO-based PSCs. Here, the baking temperature of the ZnO electron transport layer was set at 100 °C, 120 °C, 150 °C, 180 °C, and 250 °C, respectively, and the baking time was fixed at 30 min. The AFM was used to observe the topography of the ZnO electron transport layers with respect to the various baking temperatures. The AFM topographic images were acquired in tapping mode over an area of 10 $\mu\text{m} \times 10 \mu\text{m}$ at a scanning speed of 2 MHz, as shown in Figure 5, where the various ZnO electron transport layers are compact and flat. Moreover, various ZnO electron transport layers with different baking temperatures usually show ~3 nm of root-mean-square roughness value, which benefits subsequent layers, such as MAPbI₃ and Spiro-MeOTAD. The flatter surface contributes to the full coverage of the MAPbI₃ and Spiro-MeOTAD layers and their molecular arrangements.

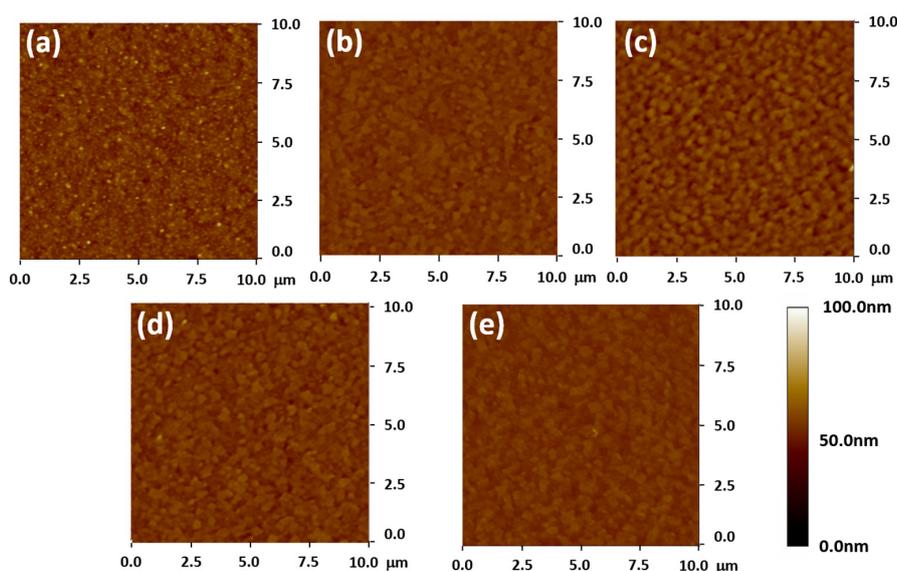


Figure 5. AFM images of the ZnO electron transport layer (~20 nm) with different baking temperatures: (a) 100 °C; (b) 120 °C; (c) 150 °C; (d) 180 °C; and (e) 250 °C.

The time-resolved photoluminescence decays (TRPL) of the MAPbI₃/ZnO/FTO glass with different baking temperatures are shown in Figure 6. The lifetimes of the excitons for samples baking at 100 °C, 120 °C, 150 °C, 180 °C, and 250 °C can be obtained by fitting the relaxation curves with a constant exponentially decaying function: 5.81 ns, 13.81 ns, 10.45 ns, 8.66 ns, and 9.36 ns, respectively. At the baking temperature of 100 °C, the sample had the largest PL intensity at the initial time ($t = 0$), and this indicates that the interface between the ZnO electron transport layer and MAPbI₃ shows the lowest efficiency of exciton dissociation, which results in the smallest short current density. When the ZnO film baking temperature was increased from 120 to 180 °C, the lifetime of excitons was reduced from 13.81 to 8.66 ns, which is due to the higher conductivity of the ZnO electron transport layer. In accordance with the Onsager–Braun theory, the efficiency of the exciton dissociation at the interface between MAPbI₃ and the ZnO electron transport layer increases with the conductivity (electron mobility) of the ZnO electron transport layer [36].

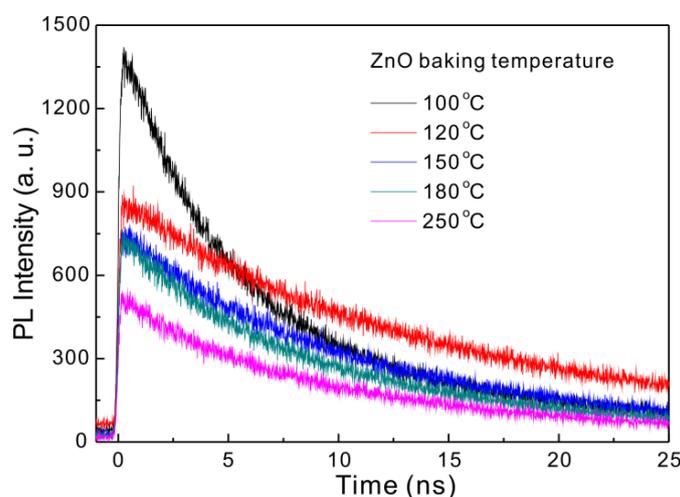


Figure 6. The time-resolved photoluminescence decays of MAPbI₃/ZnO (~20 nm)/FTO glass with the different baking temperatures.

The J - V curves of perovskite-structured photovoltaic devices based on various ZnO electron transport layers with different baking temperatures are shown in Figure 7. The series resistance (R_s) of ZnO-based perovskite photovoltaic device decreases when the baking temperature is increased, which reveals that the interface resistances of ZnO/perovskite and the conductivity of the ZnO electron transport layer improved when the baking temperature was increased from 100 to 180 °C. When the baking temperature was higher than 250 °C, the shunt resistance (R_{sh}) of the perovskite photovoltaic device decreased, as a result of the formation of the pinholes in the ZnO electron transport layer. In our previous work [20], it was shown that the short-circuit current density of perovskite photovoltaic devices was proportional to the efficiency of the exciton dissociation at the interface between the perovskite layer and the ZnO electron transport layer, and this could be evaluated by nanosecond TRPL spectroscopy. Table 1 summarizes the photovoltaic performances of various ZnO-based PSCs with different baking temperatures of ZnO electron transport layers.

Finally, the stability of the highest performance ZnO-based PSC without encapsulation was investigated as a function of time and is shown in Figure 8. The devices were placed in the dark at 5.0% RH and 25 °C and present a slight decay in performance after 1300 h. This reveals that ZnO films in an optimized thermal treatment process, in order to avoid the formation of hydroxide or acetate ligands, have an excellent ZnO/perovskite interface, resulting in a PSC with high efficiency and stability, which may confirm Kelly's group report [29]. The performance decay is due to the decrease in the short circuit current density (J_{SC}), which declined from 20 to 18 mA/cm². On the contrary, the V_{OC} is found to be remarkably stable. The degradation of the J_{SC} may also originate from the migration

of iodine ions in the perovskite film, leading to an oxidation of the metallic contact and current loss. Another reason could be the leakage of additives in the spiro-MeOTAD, causing a progressive decrease in conductivity, as this compound has been identified as one of the main sources of instability in the solar cell. Moreover, an increase in contact resistance occurs due to the partial peel of Ag electrodes after several device measurements. These devices will be fabricated well with encapsulation and welding wires in further experiments in order to fully understand the degradation mechanism of PSCs at accelerated aging test conditions.

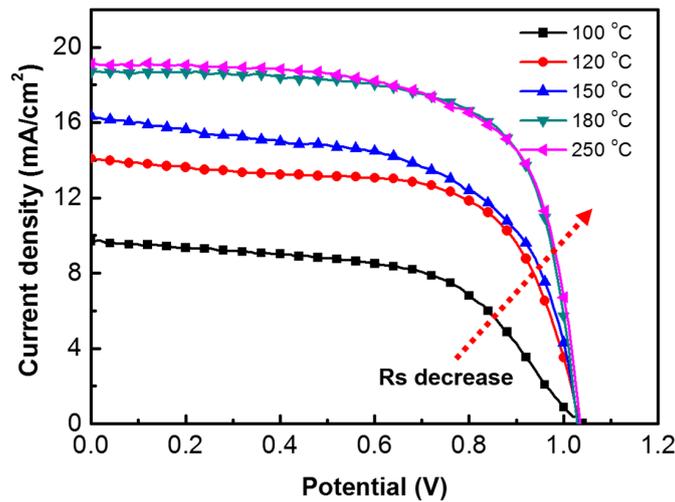


Figure 7. The *J-V* curves of perovskite photovoltaic devices based on ZnO layer with different baking temperatures.

Table 1. The photovoltaic performances and carrier lifetime of MAPbI₃ solar cell with different baking temperatures of the ZnO electron transport layer.

Baking Temp. (°C)	V _{OC} (V)	J _{SC} (mA/cm ²)	FF (%)	Efficiency (%)	Lifetime (ns)
100	1.05	8.81	56.0	5.18	5.81
120	1.03	14.09	65.3	9.47	13.81
150	1.03	16.35	59.2	10.49	10.45
180	1.04	18.71	70.2	13.66	8.66
250	1.03	19.12	67.8	13.34	9.36

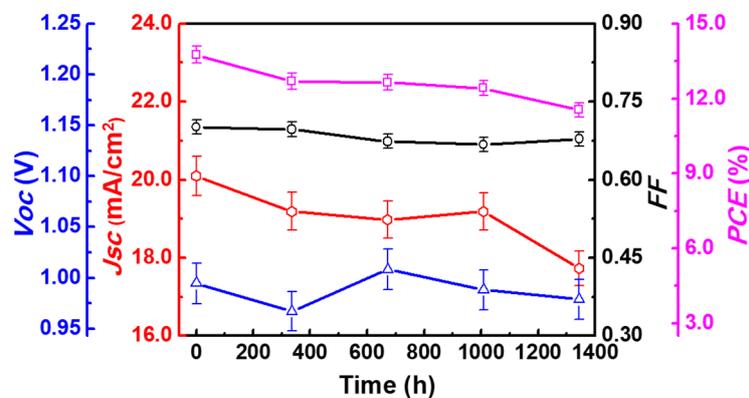


Figure 8. Stability of the ZnO-based perovskite photovoltaic device stored in a dark condition (5.0% RH and 25 °C).

4. Conclusions

The photovoltaic performance is explored here by a planar heterojunction perovskite-structured solar cell with a ZnO/CH₃NH₃PbI₃/Spiro-MeOTAD/MoO₃/Ag configuration. We carried out a detailed study on a ZnO electron transport layer and considered film thickness, surface morphology, crystallinity, post-baking temperatures, and PL properties, which are critical parameters for improving the performance of ZnO-based perovskite-structured photovoltaic devices. The highest-performing device exhibited a power conversion efficiency of about 13.66% when it was prepared with a ZnO thickness of ~20 nm and a post-baking temperature of 180 °C for 30 min. This is partly due to the highly conductive nature of ZnO thin films. Finally, the device performances stay remarkably constant for more than 1300 h stored in dark and dry conditions.

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Author Contributions: Kun-Mu Lee and Ming-Chung Wu conceived and designed the experiments; Yin-Hsuan Chang and Ting-Han Lin performed the experiments; Ting-Han Lin and Chuan-Jung Lin analyzed the data; Kun-Mu Lee and Vembu Suryanarayanan wrote the paper.

Conflicts of Interest: The authors declare no conflicts of interest.

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