





Proceeding On-Chip Platform for Slack-Free Carbon Nanotube Resonators ⁺

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Abstract: This work describes an on-chip integrated micro-actuator device for slack-free carbon nanotube (CNT) resonators, improving frequency tunability and Q factor and to study non-linear mode interaction. The device fabricated on SOI wafer with low thermal budget (<600 K) encompasses a restricted symmetrical out-of-plane vibration and a stiff in-plane electro-thermal actuator with a displacement of ~112 nm at 2.7 mW. This corresponds to 5.6% of strain for a 2 μ m long suspended carbon nanotube. The in-plane mechanical resonance designed to be far from CNT resonances in MHz regime is measured at 209 kHz. The design is optimized for low power consumption, electrical and thermal isolation and is customized for dry transfer of CNTs.

Keywords: carbon nanotube; resonator; micro-actuator; strain-sensor

1. Introduction

Single-walled carbon nanotube resonant sensors have previously been demonstrated as ultra sensitive compared to other NEMS and MEMS based resonant sensors [1,2]. In CNT resonant sensors, both as-grown and dry transferred CNTs over a trench usually have a slack [3] which alters the dynamic behavior of CNT resonators [4]. A nanotube with slack, shows both in-plane and out-of-plane resonances [5] below the fundamental flexural mode. In contrast, it has been shown that reduction of slack increases the resonant frequency up to 20 times more along with an increase in the quality factor [6], both of which are two important parameters for improving the performance of a sensor. Yet only very little work has been reported to eliminate slack. This work presents an on-chip design for potentially slack-free suspended CNTs by axial straining and henceforth minimizing slack dependence and increasing performance of CNT based resonant sensors. Our design is furthermore compared to previously reported work.

2. Modeling and Fabrication

Figure 1 shows the design of a suspended CNT-FET configuration integrated with thermal micro-actuators. The FET architecture was designed to be compatible for dry transfer of CNTs [7] on to the source-drain electrodes. Carbon nanotubes can then be axially elongated and strained by applying a voltage difference across the thermal beams. The Joule heat generated increases the temperature and expands the beam in turn moving one of the two FET electrodes in the direction y as shown. CNTs can be elongated with an experimentally observed breaking strain of up to 5.8% [8]. For 2 μ m long resonator, a 5% strain requires a 100 nm elongation. Secondly, the mechanical resonances of the design should not interfere with CNT resonances in MHz regime. Furthermore, the interconnecting beam between FET and actuator should account for thermal and electrical insulation. The mechanical behavior of thermal actuator was modeled by considering a single pair of V-shaped

thermal beams (Figure 1-Inset). The displacement of a pair of thermal beams of length *l* and cross sectional area A_c due to finite temperature increase ΔT is given by [9]

$$y = A_c \alpha \Delta T l \frac{l^2 \sin \theta}{12 I \cos^2 \theta + A I^2 \sin^2 \theta} = A_c \alpha \Delta T l * A F$$
(1)

where α is the coefficient of thermal expansion, θ is the beam angle for V-shape thermal actuator, *I* is the moment of inertia about the out-of-plane *z* axis and *AF* is introduced as an amplification factor depending on beam dimension and angle. For a device layer of 5 µm, an optimum angle $\theta = 1.15^{\circ}$ with a beam length of 100 µm were used as shown in Figure 2a. In order to thermally insulate the CNT sensor, the temperature rise ΔT was reduced along the interconnected suspended beam acting as a heat sink. Figure 2b shows the temperature profile for 4.8 mW at 1 V actuator voltage simulated using COMSOL Multiphysics. The mechanical resonances were studied through eigenmode analysis and the first in-plane fundamental mode was found at 191 kHz. The FET S/D electrodes were connected by a U-Beam to avoid out-of plane anti-symmetrical resonances.



Figure 1. The Design configuration consisting of a thermal actuator integrated with suspends CNT FET architecture through a heat sink beam. Inset-Schematic of a pair of V-shaped beams with stiffness kT subjected to an axial movement due to an average increase in temperature.



Figure 2. (a) Amplification factor for a single thermal beam as a function of beam angle and beam length; (b) Temperature profile (in Kelvin) along the device for 4.8 mW at 1 V actuator voltage.

The design was fabricated on Silicon-on-Insulator (SOI/300 μ m + 1 μ m + 5 μ m/handle + oxide + device layer) with a low thermal budget of less than 600 K as shown in Figure 3. The suspended FET architecture consists of two Pd coated bottom-gate electrodes in a 275 nm trench depth allowing axial pulling of source/drain electrodes. The 5 μ m thick doped-Si actuator is connected to CNT-FET with 325 μ m long suspended beam. The Pd is separated from the doped-Si layer by 550 nm SiN_x layer (Figure 1) for electrical insulation between the resonator signal and actuator current. Compared to the previous version of polyMUMPs based TEM-compatible design [3], the present work simplifies

the process by eliminating the shadow mask. As explained in subsequent sections, the platform is designed for low thermal stress and low power consumption.



Figure 3. SEM (**a**,**b**) and optical image (**c**) of suspended CNT-FET with integrated on-chip thermal actuator.

3. Measurement and Results

The mechanical behavior of the thermal actuator was measured by planar motion analyzer (Polytec PMA 400) at ambient conditions (Figure 4a) and the relative displacement between the source and drain electrode was extracted by digital imaging. The mechanical response was linear for power < 5.5 mW. The resistance increase due to heating was seen after 5.5 mW at 1.5 V (Figure 4b-Inset). The linear fit suggests the onset of displacement at 0.26 mW at 0.3 V. This could be attributed to beam stiffness and initial residual stress in the actuator beam.



Figure 4. (a) Planar motion analyzer for measuring displacement between drain (B) and source (A) electrode; (b) Actuator displacement vs. power. The error bar results from averaging of the transient response. (Inset-b) Measured voltage vs. current. The resistance increases after 1.5 V due to joule heating.

Actuation and recovery time (Figure 4a-Inset) are < 1 ms for 2.7 mW power at 1 V corresponding to 112 nm of displacement. For a 2 μ m CNT, this corresponds to 5.6% axial strain and beyond and suitable for CNT straining. The first in-plane eigenfrequency (Figure 5) was measured at 209 kHz by mounting the device on piezo-stage and using white noise excitation.



Figure 5. (**a**) First in-plane eigenmode (displacement normalized to 1) simulated in COMSOL; (**b**) First in-plane resonance measured using LDV by tilting the sample (~90°) mounted on a piezo stage.

We expect deviations from the simulated eigenmode study as they did not account for intrinsic stress after fabrication.

4. Discussion and Conclusions

We compare our results to previously reported work for CNT straining as shown in Table 1. The dry transfer compatible design consumes 5 times less power than previous on-chip design in [3] with minimized temperature induced stress and device's mechanical resonance in kHz region.

Ref.	Design	CNT Placement	Power @ 100 nm
[6]	PC controlled piezo-SEM stage	3 probe pick-up-drop	-
[10]	AFM cantilever	3 nanoprobe system	-
[9]	On-chip thermal actuator	Probe nanomanipulation	~28 mW
[3]	On-chip thermal actuator	As grown and dry transfer	~10 mW
This work	On-chip thermal actuator	Dry-transfer	2.7 mW

Table 1. Previously reported work for CNT straining.

Apart from improving and studying CNT resonators, the design can be extended as a platform for other nanoresonators like nanowires, for in-situ tensile and TEM analysis and investigation of strain sensors and gauge factors on a chip level.

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