

## Special Issue

# Advances in Power Management Integrated Circuits (PMICs)

### Message from the Guest Editor

Power management integrated circuits (PMICs) are essential for the efficient control and distribution of power within electronic systems. With the rapid pace of technological advancement, the demand for more efficient, compact, and versatile PMICs has surged. This demand arises from the need to support an ever-expanding range of applications, including implantable and wearable biomedical devices, portable consumer electronics, complex industrial systems, and cutting-edge automotive technologies. This Special Issue, titled 'Advances in Power Management Integrated Circuits (PMICs)', aims to compile cutting-edge innovations and advancements in the theory, analysis, design, and practical implementation of power management ICs and systems. We welcome submissions of exceptional research and development findings covering a broad range of topics related to PMICs and will consist of research articles, short communications, review articles, and perspectives.

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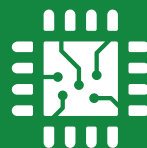
### Guest Editor

Dr. Zhanghao Yu  
Intel, Santa Clara, CA, USA

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### Deadline for manuscript submissions

30 November 2025



## Chips

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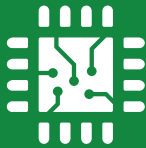
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## About the Journal

### Message from the Editor-in-Chief

*Chips* is a new journal with the aim to become a leading reference on all aspects of the IC domain. The journal is devoted to publishing rigorously peer-reviewed articles (such as original research, reviews, and communications) with the specific target to disseminate novelties in terms of research and knowledge as well as the most advanced state of the art on IC technologies, design, testing, and production. The journal offers the opportunity to actively spread new concepts and advancements in the IC domain and its increasing interrelated and multidisciplinary areas in a timely manner. More specifically, the journal will cover chip design, including CAD tools, chip production, and their wide spectrum of applications.

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### Editor-in-Chief

Prof. Dr. Gaetano Palumbo  
Dipartimento di Ingegneria Elettrica Elettronica e Informatica,  
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#### Open Access:

free for readers, with article processing charges (APC) paid by authors or their institutions.

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 22.2 days after submission; acceptance to publication is undertaken in 3.9 days (median values for papers published in this journal in the second half of 2024).

#### Recognition of Reviewers:

APC discount vouchers, optional signed peer review and reviewer names are published annually in the journal.