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## Integrated Circuits for Systems on Chip and Systems in Package towards Advanced Industrial Applications

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Deadline for manuscript  
submissions:

**closed (31 March 2023)**

### Message from the Guest Editors

This Special Issue is to focus on the recent developments in reconfigurable and integrated circuits including the design, implementation and measurements of SoC and SiP for sensor interfaces and industrial applications. This includes (but is not limited to) high-voltage circuits (on-chip digital isolators, power amplifiers, level shifters ...) and low-voltage circuits (data converters, read-back circuits, filters ...). Research works based on advanced packaging solutions for SoC and SiP are welcomed for this Special Issue. Authors are invited to submit their latest research findings (including simulation and measurement results) for publication. Both regular articles and review papers are welcomed.

The deadline to submit papers to this special issue is 31 March 2023. However, papers submitted in advance will be peer-reviewed and may be published well before this deadline. For more details, please click [here](#).



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# Special Issue



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## Message from the Editor-in-Chief

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