



Computer-Aided Design for Hardware Security and Trust

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Deadline for manuscript
submissions:

closed (30 June 2024)

Message from the Guest Editors

Dear Colleagues,

Despite the significant attention paid to hardware security over the past decade, there remains a need for a more mature set of security-aware tools to help chip designers verify security vulnerabilities automatically and effectively across all levels of abstraction throughout the chip design process. To address this need, we aim to leverage Computer-Aided Design (CAD) tools and commercial design tools for security and trust to ensure the reliability of the chip. This Special Issue will focus on exploring the latest academic and industrial research on all aspects of CAD for hardware security and trust.

Topics of interest to this Special Issue include, but are not limited to:

- Security analysis engines;
- Security-aware CAD tools;
- VLSI verification for security and trust;
- Automatic side-channel vulnerability assessment;
- Security equivalence checking;
- Formal method-based security verification.





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Message from the Editor-in-Chief

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