Analog Integrated Circuits in Edge Computing

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Message from the Guest Editors

The main goal of this Special Issue is to present effective implementations of analog integrated circuits, hardware-oriented algorithms, design methods, and optimizations of CMOS circuits aimed at processing analog data close to the source. Today, such solutions are particularly desirable in medical applications, implantable chips, low-power electronics, human energy harvesting, autonomous distributed systems, wearable devices, etc. The topics of interest include, but are not limited to:

- Mixed-signal integrated circuits;
- Analog CMOS preprocessors and accelerators;
- Field-programmable analog array (FPAAs), analog IPcores;
- Edge AI, CMOS implementation of neural networks;
- Sensor data analysis, sensor techniques;
- Hardware-oriented algorithms for analog signal processing;
- Low-power IC, weak-inversion mode, moderate-inversion mode;
- Optimization and design automation of analog CMOS circuits;
- Analog-to-digital Converters, $\sum\Delta$ modulators.

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Special Issue

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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