



Computer-Aided Design for Integrated Circuits and Systems

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Message from the Guest Editors

Dear Colleagues,

Currently, we are witnessing a rapid development of semiconductor technology. As a result of the transition from microelectronics to nanoelectronics, new families of VLSI chips have appeared. The modern integrated circuits and systems consist of billions and billions of semiconductor transistors. Now, a single chip is enough to implement very complex digital and mixed digital-analog systems. The stiff competition between the manufacturers of integrated circuits and VLSI systems guarantees that only the best products come to the market. The enormous complexity of modern circuits and systems requires the use of Computer-Aided Design (CAD) tools that automate the different stages of the design flow.

The aim of this Special Issue is to present novel practical and theoretical approaches related to the CAD of integrated circuits and systems. In particular, methodologies, tools, algorithms, and architectures that allow improving the efficiency of integrated circuits and systems in terms of performance, area occupancy, or power consumption are welcome.





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Message from the Editor-in-Chief

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