



Computing-in-Memory Devices and Systems

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Deadline for manuscript
submissions:

closed (1 August 2022)

Message from the Guest Editors

The purpose of the proposed Special Issue on Computing-in-Memory Devices and Systems is to provide a comprehensive view of recent advances in devices that combine memory and computing capabilities, system architectures, and algorithms based on new advances in computer architecture and applications. Topics of interest for this Special Issue include, but are not limited to:

- Non-volatile memory solutions with computing capabilities;
- Novel circuit design solutions for computing-in-memory;
- Computing-in-memory applications and architectures;
- EDA tools for the design and benchmarking of computing-in-memory architectures;
- Novel electronic devices that show computing-in-memory capabilities;
- Device modeling.

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Editor-in-Chief

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

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