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Fault-Tolerant Design and Applications of Electronic Circuits and Systems

Guest Editor:

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Deadline for manuscript submissions:

closed (31 March 2022)

Message from the Guest Editor

Dear Colleagues,

Electronic circuits and systems used in many real-world safety-critical applications such as space, aerospace, defense, nuclear power plants, electric power transmission and distribution, industrial control and automation, and banking and finance etc. usually involve fault-tolerant design at the hardware and/or software level for enhanced reliability. Fault-tolerant design is essential to cope with the faults or failures of circuits and systems. Fault-tolerant design involves some form of redundancy which may be static or dynamic or a mix of both. This special issue aims to deal with recent advances in fault-tolerant design at the hardware and software levels. Given this, we invite high quality academic and industrial research work on all aspects of fault-tolerant design and reliability. The topics of interest include but are not limited to:

- Methods for assessing reliability of devices, circuits and systems
- Fault tolerance in low power electronics: microelectronics, nanoelectronics and optoelectronics
- Fault tolerance in renewable energy systems including solar, wind, wave, geothermal etc.











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Message from the Editor-in-Chief

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