



## Hardware and Architecture

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### Message from the Guest Editor

Novel trends in Cyber physical Systems, the Internet of things, and also neuronal networks and machine learning, need new hardware and architecture. The goal is to reduce energy consumption more, and to simultaneously increase performance. This Special Issue will cover novel trends, research, and the development of hardware and architecture, and contributes to the community with different views and solutions.

### Keywords:

- Embedded system
- Low power
- Reconfigurable computing

Prof. Dr. Michael Huebner  
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## Editor-in-Chief

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## Message from the Editor-in-Chief

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