



## FPGA-Based Accelerators of Deep Learning and Neuromorphic Computing

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submissions:

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### Message from the Guest Editor

Dear Colleagues,

This Special Issue will cover advanced techniques for the hardware acceleration of deep learning and neuromorphic algorithms on FPGA, ranging from micro-architecture design to automatic compilation, as well as hardware-friendly algorithm optimization, including the latest ongoing research efforts in these fields but not limited to:

1. Algorithm–hardware co-design for FPGA-based intelligent acceleration;
2. System and software for FPGA accelerator compilation;
3. Reconfigurable/adaptive computing for AI/ML;
4. FPGA-based rapid prototyping of AI/ML system;
5. Programmable neuromorphic computing architectures on FPGAs;
6. Implementation of novel intelligent applications on FPGAs;
7. AI/ML systems based on coarse-grained reconfigurable architectures (CGRAs);
8. Implementation and evaluation of spiking neural networks on FPGAs.

Dr. Yufei Ma  
*Guest Editor*





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## Message from the Editor-in-Chief

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