



Low Power Circuits in Microelectronics

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Message from the Guest Editors

Dear Colleagues,

This Special Issue focuses on innovative ideas in relation to the design and application of low-power microelectronics architectures. The current trend towards the integration on one single chip of a whole system requires low-power designs not only due to power budget and energy savings, but also because of autonomy in wearable or IoT devices, self-supplied circuits or the battery lifecycle. Additionally, deep circuit integration may presuppose a high energy dissipation per unit of area, incompatible with a proper performance. In those cases, low power consumption is mandatory. Finally, special attention must be paid to artificial intelligence and power handling. The topics of interest include but are not limited to:

- Wearable devices;
- Internet of Things;
- Automotive;
- Consumer electronics;
- Biomedical/Healthcare applications;
- Neuromorphic circuits;
- Deep-learning, neural networks, and machine learning hardware applications;
- FPGA-based digital design;
- Sensor applications;
- Power circuit and energy harvesting;
- Low-power design methodology/flow.





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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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