



## Memristor Devices and Semiconductor: Models, Developments and Applications

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### Message from the Guest Editor

Dear Colleagues,

Since the start of the fourth industrial revolution, neural process-based artificial intelligence and internet of things have rapidly increased the demand for data processing. Modern computing systems have encountered a “bottleneck” and high-power consumption due to a number of the sequential data processing. In this context, a novel design of computing architectures (or system) based on memristors have shown excellent potential in replacing conventional computing systems and the emergence of new hardware such as memristors is a necessary and interest and efforts in related research are needed.

Considering this situation, the aim of this Special Issue is to gather the most recent research and structural developments of memristors for models, developments, and applications, including the following topics:

- Electrical characteristics for memristors;
- Nanomaterials and nanocomposites for memristors;
- Two-dimensional structures for memristors;
- Crossbar array of memristors;
- Neuromorphic-behaving memristors;
- Integration and neuromorphic computing system based on memristors;
- Low-power consumption of memristors.





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## Editor-in-Chief

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## Message from the Editor-in-Chief

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