



Microprocessor and Integrated Circuits

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Message from the Guest Editor

An Intelligent, self-learning, connected digital world through smart devices is shaping human lifestyles. At the end of Moore's law era, the future of electronics hardware will be defined by architecture breakthrough implemented with innovative circuit techniques. In the era of Artificial Intelligence (AI), this Special Issue focuses on machine learning hardware, processor architectures to address memory bandwidth, hardware accelerators for machine learning, alternate computation such as approximate, analog mixed signal, and variable resolution computing, energy-efficient interconnect solution, and smart sensing processing technology. We are also interested in promoting hardware solutions implemented beyond CMOS technology, such as carbon nanotube, graphene, etc.





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Editor-in-Chief

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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