Design Tools and Architectures for Coarse-Grained Reconfigurable Computing

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Message from the Guest Editors

The objective of this Special Issue is to bring together the latest advances in coarse-grained reconfigurable computing (CGRC), including design tools, architectures, and applications.

CGRC topics include, but are not limited to:

- Coarse-grained reconfigurable architectures;
- Fine-grained versus coarse-grained computing;
- Spatial and temporal computation in CGRA;
- Run-time reconfiguration/adaptation of CGRA;
- Models of computation for CGRA;
- Programming tools and compilers for CGRA;
- Energy-aware compilation for CGRA;
- Simulation tools for CGRA;
- CGRA for domain-specific applications (image processing, deep-learning, network processing, etc.);
- Edge computing and high-performance computing with CGRA.

Please click here to find information!
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SpecialIssue

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Message from the Editor-in-Chief

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