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# Challenges and Opportunities in Signal and Power Integrity: Theory and Applications

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Deadline for manuscript submissions:
**closed (1 May 2021)**

## Message from the Guest Editors

Signal and power integrity (SI/PI) are essential and interlaced aspects of contemporary advanced digital and hybrid electronic system design. This Special Issue wishes to offer the opportunity to engineers and scientists to exchange state-of-the-art developments in the field of signal and power integrity applied to any kind of high-speed circuit and system, to the modeling, design, validation, and testing of electronic hardware.

Keywords:

- signal and power integrity
- high-speed digital circuits
- analog circuits
- RF systems
- IC and packages
- materials performance
- signal and power integrity measurements and sensors
- numerical simulations and EDA tools
- electromagnetic interferences
- Artificial Intelligence
- Internet of Things applications

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## Message from the Editor-in-Chief

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