



VLSI Implementation of Neural Networks

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Message from the Guest Editors

This Special Issue on neural networks provides a forum for developing and nurturing an international community of scholars and practitioners who are interested in all aspects of neural networks and related approaches to computational intelligence. This uniquely broad range facilitates the cross-fertilization of ideas between biological and technological studies, and helps to foster the development of the interdisciplinary community that is interested in biologically-inspired computational intelligence. Accordingly, the “VLSI Implementation of Neural Networks” Guest Editor board represents experts in fields including psychology, neurobiology, computer science, engineering, mathematics, and physics.

The aim of this Special Issue of Electronics is to present state-of-the-art investigations in various VLSI Implementation of Neural Networks technologies for future applications. We invite researchers to contribute original and unique articles, as well as sophisticated review articles.





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Message from the Editor-in-Chief

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