



## VLSI Architecture Design for Digital Signal Processing

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Deadline for manuscript  
submissions:

**closed (31 May 2019)**

### Message from the Guest Editor

The main aim of this Special Issue is to seek high-quality submissions that highlight emerging applications and address recent breakthroughs in the VLSI architecture design for DSP, including design and analysis of signal processing algorithms and architecture, performance analysis of signal processing systems, VLSI design methodology, design of arithmetic circuits and VLSI components used in signal processing. The topics of interest include, but are not limited to:

- Design and implementation of signal processing systems
- Machine learning architectures for DSP
- Circuits and systems for signal processing and communications
- Cryptography architectures and hardware security
- Forward error correction architectures
- Multimedia signal processing systems
- Adaptive digital processing systems with FPGA components
- VLSI signal processing architectures
- Special purpose signal processing architectures
- SoC designs for DSP
- DSP algorithms implemented in VLSI systems
- Embedded architectures and systems
- Digital circuits and systems



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## Editor-in-Chief

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## Message from the Editor-in-Chief

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