VLSI Architectures for Wireless Communications and Digital Signal Processing

Guest Editors:

Prof. Dr. Byeong Yong Kong
Division of Electrical, Electronic, and Control Engineering, Kongju National University, Cheonan 31080, Korea

Prof. Dr. Hoyoung Yoo
Department of Electronics Engineering, Chungnam National University, Daejeon 34134, Republic of Korea

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Message from the Guest Editors

This Special Issue solicits original and unpublished papers on high-performance and low-power VLSI architectures and the relevant algorithmic optimizations in the field of wireless communications and digital signal processing.

The topics of interest include but are not limited to:

- VLSI architectures for 5G and 6G telecommunications;
- FPGA and ASIC implementations of signal-processing systems;
- Baseband signal processing for communication systems;
- Circuits and systems for the Internet-of-Things (IoT);
- VLSI architectures for machine learning and artificial intelligence;
- Application-specific instruction-set processors for digital signal processing;
- Hardware-friendly algorithms and optimization techniques;
- Embedded systems on chip (SoCs) for signal-processing applications.

Special Issue

Editor-in-Chief

Prof. Dr. Flavio Canavero
Department of Electronics and
Telecommunications,
Politecnico di Torino, 10129
Torino, Italy

Message from the Editor-in-Chief

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