



VLSI Architectures for Wireless Communications and Digital Signal Processing

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Message from the Guest Editors

This Special Issue solicits original and unpublished papers on high-performance and low-power VLSI architectures and the relevant algorithmic optimizations in the field of wireless communications and digital signal processing.

The topics of interest include but are not limited to:

- VLSI architectures for 5G and 6G telecommunications;
- FPGA and ASIC implementations of signal-processing systems;
- Baseband signal processing for communication systems;
- Circuits and systems for the Internet-of-Things (IoT);
- VLSI architectures for machine learning and artificial intelligence;
- Application-specific instruction-set processors for digital signal processing;
- Hardware-friendly algorithms and optimization techniques;
- Embedded systems on chip (SoCs) for signal-processing applications.





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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest edited by leading experts in selected topics of interest.

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