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Analog/Digital Mixed Circuit and RF Transceiver Design

Guest Editor:

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Deadline for manuscript submissions:
closed (10 October 2020)

Message from the Guest Editor

Dear Colleagues,

Energy-limited wireless devices in the Internet of Things (IoT) are typically powered by batteries with a limited lifetime. Thus, low-power RF circuit design with RF energy-harvesting (EH) technologies are essential in IoT devices to increase their lifetime. Further, low-power sensor signal conditioning circuits and low-power converters (ADC/DAC) need to be designed to process data from multiple sensors. High-efficiency power management circuits such as DC–DC converters and LDO regulators are integrated today. The topics of interest include but are not limited to:

- Low-power IoT RF transceivers;
- Ultralow power wake-up receivers;
- RF energy harvesting;
- Wireless power transfer;
- High data rate 5G RF transceivers;
- Low-power ADC;
- Low-power DAC;
- High-efficiency DC–DC converters;
- High-efficiency LDO regulators.

Prof. Dr. Kang-Yoon Lee
Guest Editor

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Message from the Editor-in-Chief

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