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High-Performance Embedded Systems

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Message from the Guest Editors

Dear Colleagues,

Non-functional constraints such as execution time, memory capacity, and energy consumption are a significant challenge for designers in the field of embedded systems. New applications are being proposed that integrate an increasing variety of functionality into everyday objects, imposing several additional requirements on embedded system designers.

This Special Issue invites researchers to contribute original research, case studies, and reviews that address topics related to designs and applications of high-performance digital embedded systems.

The topics relevant for this Special Issue include (but are not limited to):

- Low-power IoT applications;
- Embedded FPGA and SoC implementations;
- Embedded ASIC implementations;
- Machine learning on the Edge;
- Efficient data-processing algorithms











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Message from the Editor-in-Chief

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