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Design of Ultra-Low Voltage/Power Circuits and Systems

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Message from the Guest Editors

Ultra-low voltage/power analog and digital designs are highly sought-after for tightly energy- and cost-constrained integrated systems such as sensor nodes for the Internet of Things, implantable devices, always-on or purely energy-harvested systems. The latter mandate extremely low power (even down to few nWs) and low minimum supply voltage ($\ll 1V$) to prolong operations under unfavorable environmental conditions, along with small area occupation, low design effort, and technology/design portability. The primary aim of this Special Issue is to attract original research outcomes related to the theory, design, and application of ultra-low voltage/power circuits and systems. Review papers on the topic are also welcome.

Deadline for manuscript submissions:

closed (30 November 2021)



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Message from the Editor-in-Chief

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