



Hardware in the Loop and Real-Time Simulation

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Message from the Guest Editors

Dear Colleagues,

We are pleased to announce this Special Issue of the MDPI journal *Electronics* on “Hardware in the Loop and Real-Time Simulation Based on FPGAs”.

HIL has not been widely used in areas where high frequencies are involved, such as power electronics. In order to overcome the speed limitations, field programmable gate arrays (FPGAs) were introduced in this field. In this context, it is crucial to minimize the delay and area without losing either resolution or accuracy.

This Special Issue focuses on novel developments in the field of hardware in the loop and real-time simulations with FPGAs in power electronics and their applications.

The topics of interest include, but are not limited to, the following:

Hardware in the Loop (HIL);

Real-time-simulation;

Embedded Processing;

Power Electronics;

Power Converters;

Field Programmable Gate Arrays (FPGA);

Resolution; Accuracy; Speed Limitation;

Testing; Emulation; Area Delay and Time-step.





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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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