



Multilevel Converters

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Message from the Guest Editors

The aim of this Special Issue is to publish original research regarding multilevel converters, presenting novel topologies, modulations, controls, related implementation technologies and applications, with the intention to increase efficiency, power density, reliability, robustness, to reduce cost and to comply with regulations. Refinements on existing techniques that introduce significant benefits are also welcome. Original contributions including experimental validation are expected.

Topics of interest include, but are not limited to:

- Multilevel converter topologies, including multi-cell and power converter array topologies.
- Modular multilevel converter design approaches.
- Advanced multilevel modulation techniques.
- Advanced multilevel converter controls.
- Fault tolerance and reliability of multilevel converters.
- Implementation technologies, including integration, design for electromagnetic compatibility, and cooling techniques for multilevel converters.
- Applications of multilevel converters.

Welcome to contribute.





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Message from the Editor-in-Chief

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