



## Novel Architectures, Tools and Applications for Reconfigurable FPGA-Based Embedded Systems

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### Message from the Guest Editors

Dear Colleagues,

Reconfigurable computing is nowadays a reality that is already being exploited in both research and industrial scenarios, which benefit from the combination of flexibility and high performance provided by this technology. Design flows for reconfigurable computing, which were previously only available in academic works, are now being integrated as part of commercial tools. However, multiple research opportunities can still be identified.

In this Special Issue, we look forward to receiving original contributions providing novel architectures to exploit all of the possibilities offered by embedded reconfigurable devices. These novel architectures require custom design flows and supporting tools to extend the options already incorporated into commercial solutions. Application papers showing implementations and deployments in different scenarios are very welcome as well, with a particular accent on reconfigurable systems working under safety-critical requirements, such as aerospace or automotive.

Welcome to contribute!





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## Editor-in-Chief

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## Message from the Editor-in-Chief

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