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Mixed-Signal VLSI Design

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Message from the Guest Editors

Dear Colleagues,

This Special Issue focuses on mixed-signal VLSI design. Authors working in conventional analog and mixed-signal circuits, as well as in the new circuit areas such as AI neural circuits and sensor systems, are welcome to submit papers.

The high integration of system ICs have large silicon areas of digital blocks. However, the analog and mixed-signal blocks are still the bottleneck of the VLSI implementation due to nature of the analog design works.

This Special Issue tries to provide advances on analog and mixed-signal design techniques for mixed-signal VLSI design area.

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Guest Editors



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Message from the Editor-in-Chief

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