System-on-Chip (SoC) Design and Its Applications

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Message from the Guest Editor

The main aim of this Special Issue is to attract submissions of recent high-quality research as well as review articles on the recent progress for "SoC and Its Applications." Topics in this Special Issue include (but are not limited to):

- Circuits for SoC: RF, analog, digital, mixed-signal circuit IP for SoC
- Signal processing for SoC: analog/digital VLSI signal processing for SoC design
- Low-power design: low-power design methodology, power/energy management, energy harvesting
- MPSoC architecture: on-chip interconnect, network-on-chip, memory architecture for multicore computing, platform architectures
- SoC for AI applications: machine learning, deep neural network, neuromorphic computing
- SoC for intelligent systems: automotive, IoT, medical/healthcare, wired/wireless communications, consumer electronics, etc.

Special Issue

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Message from the Editor-in-Chief

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