Special Issue

Neural Networks: From Software to Hardware

Message from the Guest Editors

This Special Issue aims to explore the intersection of algorithmic innovation and hardware deployment, bridging theoretical advancements with practical utilization. We invite contributions addressing the co-design of neural network architectures and hardware platforms, including but not limited to the following:

- Novel neural network paradigms for hardware deployment (e.g., spiking neural networks with faster inference and higher energy-efficiency, knowledge distillation techniques capable of learning richer information from complex networks using simpler architectures, sparse models with enhanced robustness and generalization capabilities, and model pruning techniques that enable the more precise identification of redundant components in neural networks)
- Flexible hardware architectures compatible with neural networks (e.g., embedded software–hardware interaction systems with lower latency and higher energy-efficiency).

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Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

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