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Advances in Power-Management Integrated Circuits

Guest Editor:

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Deadline for manuscript submissions:

closed (31 July 2022)

Message from the Guest Editor

Power-management integrated circuit (PMIC) design impacts power-usage efficiency in system-on-a-chip. The ultimate goal is to integrate all required passive components to the PMIC chips to achieve local on-chip regulation, while the chip-area cost due to on-chip passive components can be further reduced. This special issue “Advances in Power-management Integrated Circuits” is focused on bringing together innovative developments of dc-dc converters. We invite prospective authors to submit outstanding research and development results in topics that include but are not limited to the following:

- Ultra-low-power voltage references
- Analog and digital low-dropout regulators
- Switch-inductor dc-dc converters
- Switch-capacitor dc-dc converters
- Energy-harvesting circuits
- Wireless power transfer



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Special Issue



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Message from the Editor-in-Chief

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