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Function Design and Control of Multilevel Converters

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Deadline for manuscript submissions:

closed (31 January 2020)

Message from the Guest Editors

This Special Issue is intended to motivate further research, development, and design of multilevel converters. Increasing the state-of-the-art of emerging topologies, and novel techniques to find optimal solutions for desired applications. Original contributions including experimental validation are expected. The topics of interest include, but they are not limited to the following:

- Traditional multilevel and multicell converter topologies;
- Optimization methods for the design of power converters:
- Passive filter design and selection of the appropriate semiconductors;
- Multilevel modulation strategies;
- Natural voltage balancing in multilevel capacitorbased inverters;
- Multilevel converters for renewable energy power applications;
- Multilevel converters for EV's applications;
- Fault tolerant analysis in multilevel converters;
- Dedicated control strategies for fast response and high bandwidth;
- EMC behavior and design of EMC filters for multilevel converters.











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Message from the Editor-in-Chief

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