



FPGA and Reconfigurable Computing

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Deadline for manuscript
submissions:

closed (31 December 2017)

Message from the Guest Editor

The pervasive use of electronics in many areas of human activity has led to a remarkable expansion of the scope and importance of power- or energy-constrained computing environments. The challenges created by this trend are being met in part by flexible heterogeneous and reconfigurable computing platforms in order to achieve new levels of power or energy efficiency while processing growing amounts of data with increasingly sophisticated algorithms. This Special Issue of the *JLPEA* is dedicated to advances in all aspects of low-power reconfigurable computing from new reconfigurable fabrics in emerging technologies up to system-level monitoring and run-time management infrastructures, including new circuits and architectures for FPGAs and CGRAs, reconfigurable hardware accelerators and new applications of dynamic reconfiguration.





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Message from the Editor-in-Chief

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