



Low Power Memory/Memristor Devices and Systems

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Message from the Guest Editors

This Special Issue will bring together excellent and inspiring work currently being undertaken in the area of low power memory including the following:

- Demonstrations of novel memory devices exhibiting exemplary performance (especially low power operation).
- Novel memory circuits and systems (specifically designed to efficiently incorporate/leverage the benefits of novel nanotechnology).
- Other circuits and systems demonstrating the use of ultra-efficient memory (especially describing what was required in order to successfully implement or incorporate such memory into the system).

Naturally, this list is not exhaustive or exclusive and any contributions that pave the way towards the future of efficient memory are welcome.

We hope that you will contribute your work to this issue and help map the recent progress of memory technology and techniques.





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Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for the studies of electronics for low power applications. A special emphasize is made on ultralow power bio-medical applications. It publishes reviews, regular research papers and short communications.

Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

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