



Ultra-Low Power VLSI Design for Emerging Applications

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Message from the Guest Editors

Dear Colleagues,

Power consumption is a top-most design parameter in numerous circuits and systems. It is particularly significant in nanoscale CMOS technology where scaling of power is extremely challenging. Low voltage operation has been widely explored to reduce power consumption. However, supply voltage scaling alone cannot meet the power consumption requirement of many emerging applications. Therefore, various innovative design techniques for ultra-low power consumption need to be developed.

This Special Issue will present the most recent advancements in ultra-low power VLSI design for emerging applications such as Internet of Things, biomedical applications, and mobile electronics.

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Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for the studies of electronics for low power applications. A special emphasize is made on ultralow power bio-medical applications. It publishes reviews, regular research papers and short communications.

Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

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