

Special Issue

Low Power Memory/Memristor Devices and Systems

Message from the Guest Editors

This Special Issue will bring together excellent and inspiring work currently being undertaken in the area of low power memory including the following:

- Demonstrations of novel memory devices exhibiting exemplary performance (especially low power operation).
- Novel memory circuits and systems (specifically designed to efficiently incorporate/leverage the benefits of novel nanotechnology).
- Other circuits and systems demonstrating the use of ultra-efficient memory (especially describing what was required in order to successfully implement or incorporate such memory into the system).

Naturally, this list is not exhaustive or exclusive and any contributions that pave the way towards the future of efficient memory are welcome. We hope that you will contribute your work to this issue and help map the recent progress of memory technology and techniques.

Guest Editors

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Deadline for manuscript submissions

closed (28 February 2022)



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About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.

It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

Editor-in-Chief

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