

Special Issue

Selected Papers from SubVt 2011 Conference

Message from the Guest Editors

Dear Colleagues, Subthreshold microelectronic circuits are gaining momentum for ultra-low-power applications but their maturity remains low when compared to conventional above-threshold designs. Therefore, this special issue of *JLPEA* is intended to focus on the advances in subthreshold microelectronics with extended versions of papers presented from the 2011 Subthreshold Microelectronics Conference (SubVt 2011) held in Lexington, MA, USA on 26-27 September 2011. However, other works related to ultra-low-power microelectronics not presented at SubVt 2011 will also be considered.

Guest Editors

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About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for the studies of electronics for low power applications. A special emphasis is made on ultralow power bio-medical applications. It publishes reviews, regular research papers and short communications.

Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

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