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## **Advances of Semiconductor Nanostructures**

Guest Editor:

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## Message from the Guest Editor

Since gate-all-around nanowire devices were combined with 3D integration, semiconductor nanostructures have promise to scale up the overall performance of electronics technology. Traditional scaling-down has allowed the construction of components and final chips with higher speed and density, lower power, increased functionality, etc. However, a change paradigm suggests that "More Moore" and "More Than Moore" will evolve towards novel strategies involving new materials, advanced devices, or higher-value nanosystems.

This Special Issue of *Materials* attempts to cover the recent advances in semiconductor nanostructures for logic and memory, sensors and actuators, biological and mechanical nanosystems, etc.













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### **Editor-in-Chief**

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# **Message from the Editor-in-Chief**

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