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## One- and Two-Dimensional Architectures for Electronic and Optoelectronic Devices

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Deadline for manuscript  
submissions:

**closed (10 November 2023)**

### Message from the Guest Editors

Miniaturization and nano-building are the two most spoken words in preparation materials for electronic and optoelectronic applications. Even if the interest is in fabricating flexible electronic devices, thin-film transistors or sensing tools, or focus on the production of light-emitting diodes or photovoltaic structures, the main goals are small dimensions and high efficiency.

1D/2D architectures offer various possibilities to realize small size and high performance with large active areas, easily tunable processes, and numerous physical and chemical deposition/growth techniques (including but not limited to solution-based methods, thermal evaporation, magnetron sputtering and atomic layer deposition). Special consideration has been paid to the theoretical/computational path (the R-matrix method, density functional theory, SIESTA, etc.) as it provides valid optimization, validation and starting points for future studies.

This Special Issue is focused on (but not limited to) the relation between 1D/2D architectures and electronic and optoelectronic devices, including both experimental and theoretical approaches. It is my pleasure to invite you to submit a manuscript for this SI.



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## Message from the Editor-in-Chief

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